

mitsubishi

Changes for the Better

MOTION CONTROLLER

Qseries

SV13/SV22

Q173CPU(N)

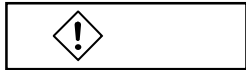
Q172CPU(N)

●
(

●
)

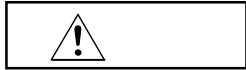
Q173CPU/Q172CPU

「 」, 「 」



가

,
.



가

,

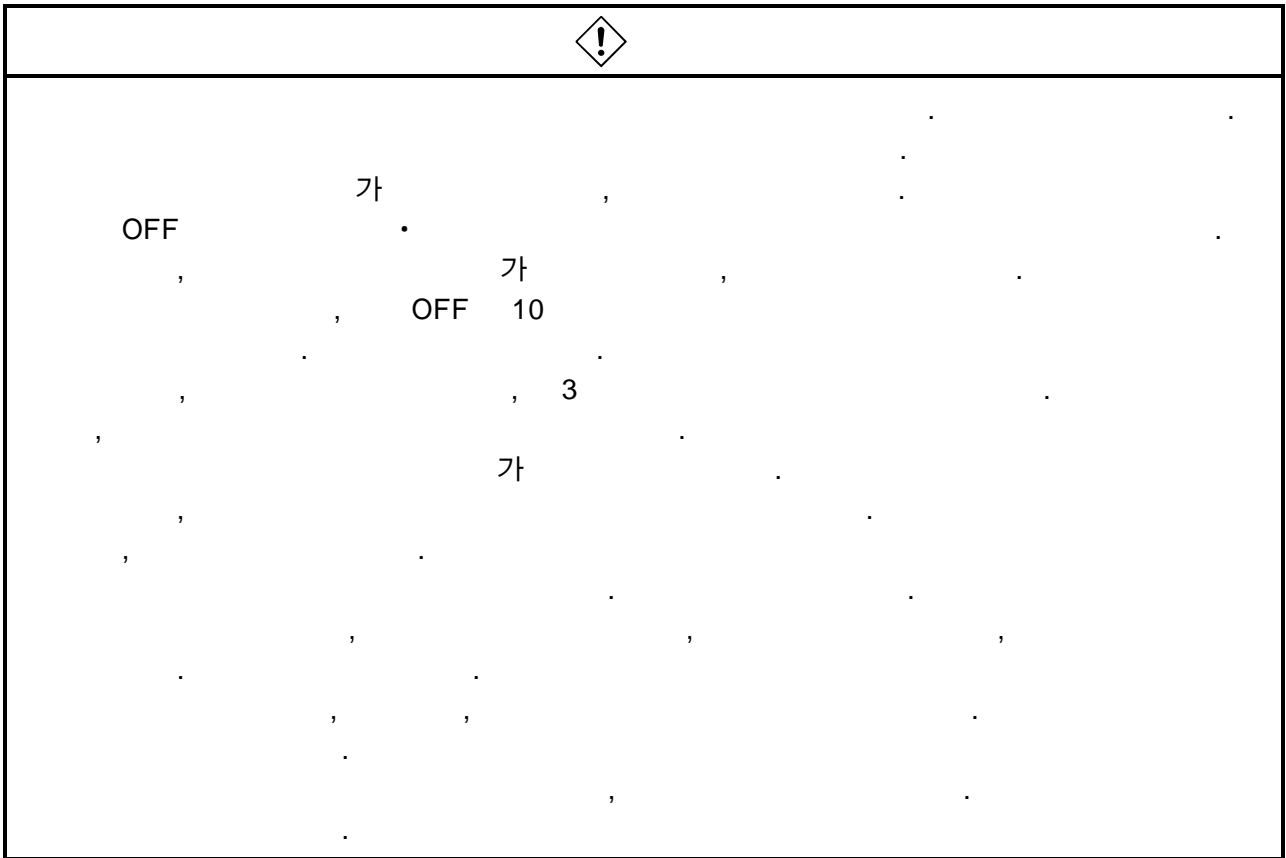


,

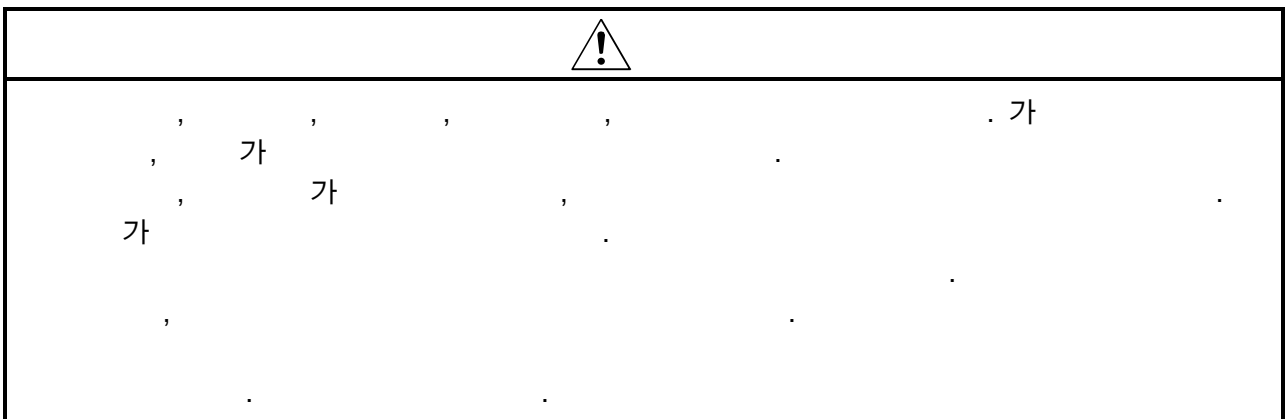
가

.

1.



2.

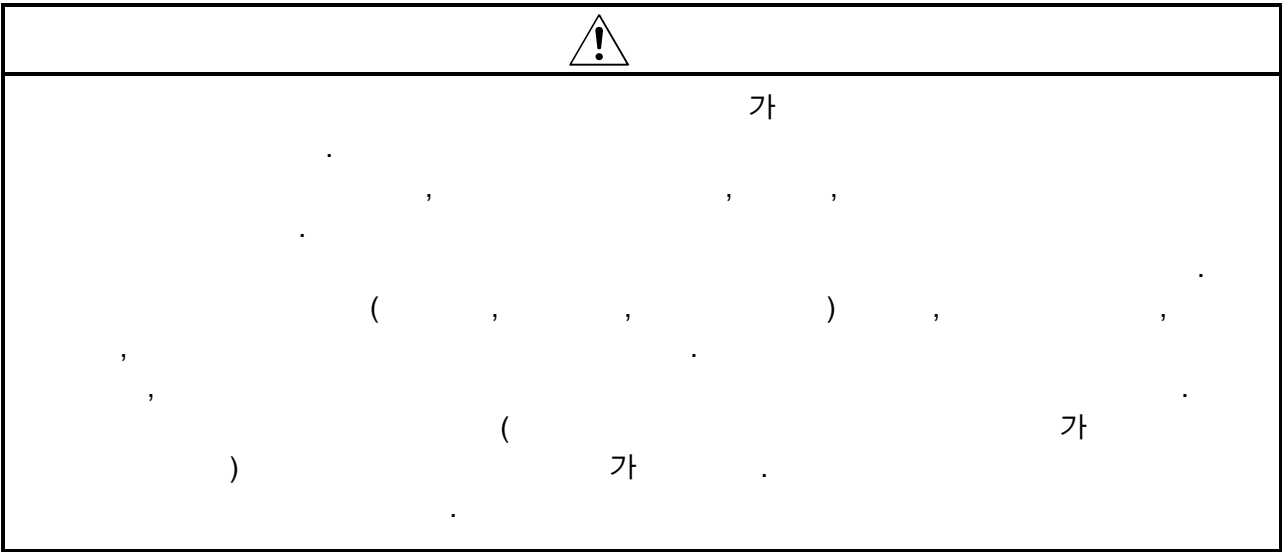


3.

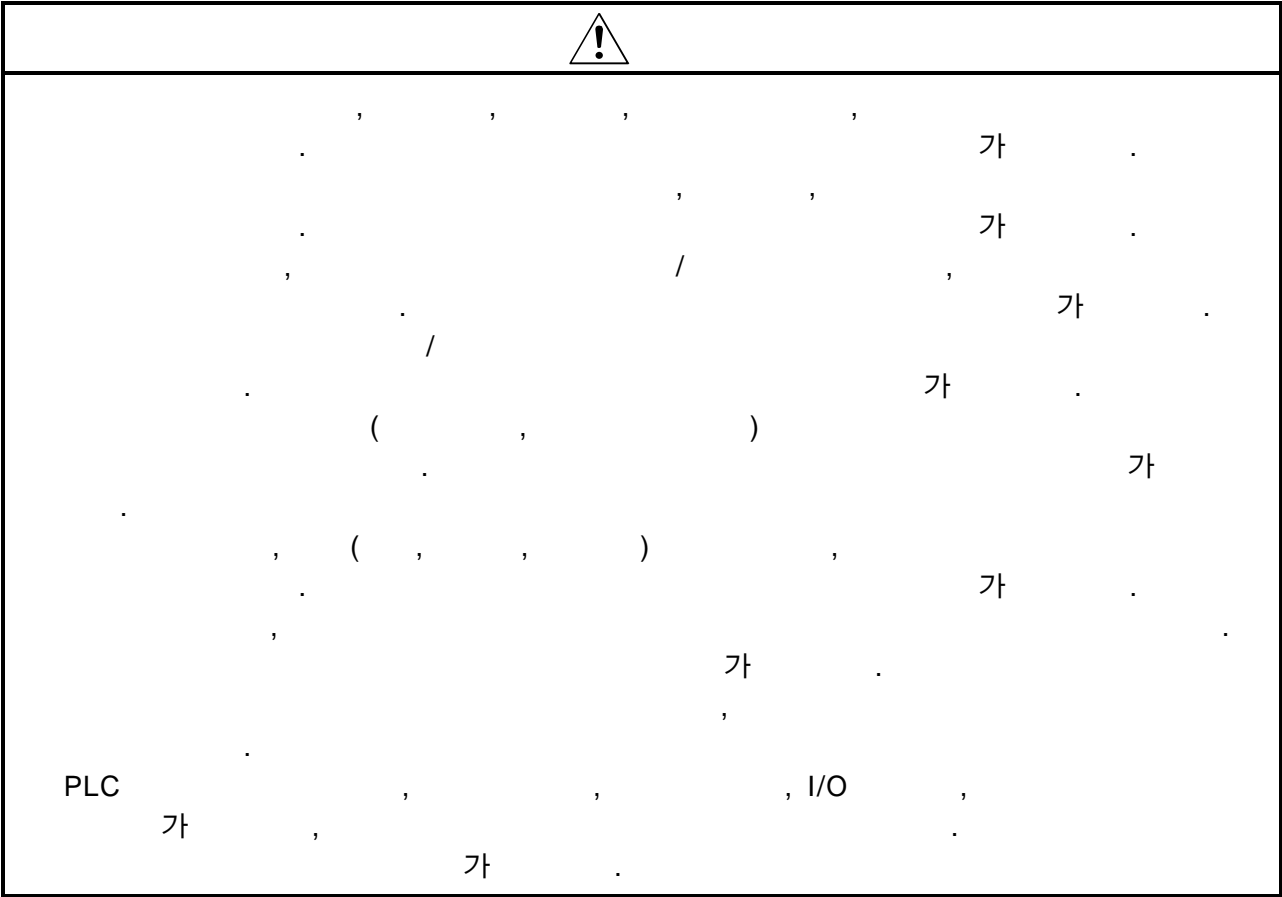
4.


(1)

A large, empty rectangular box with a black border, intended for a drawing or diagram. The box is centered on the page and occupies most of the lower half of the worksheet. Above the box, there is a small warning icon (a triangle with an exclamation mark) and some faint, illegible text.




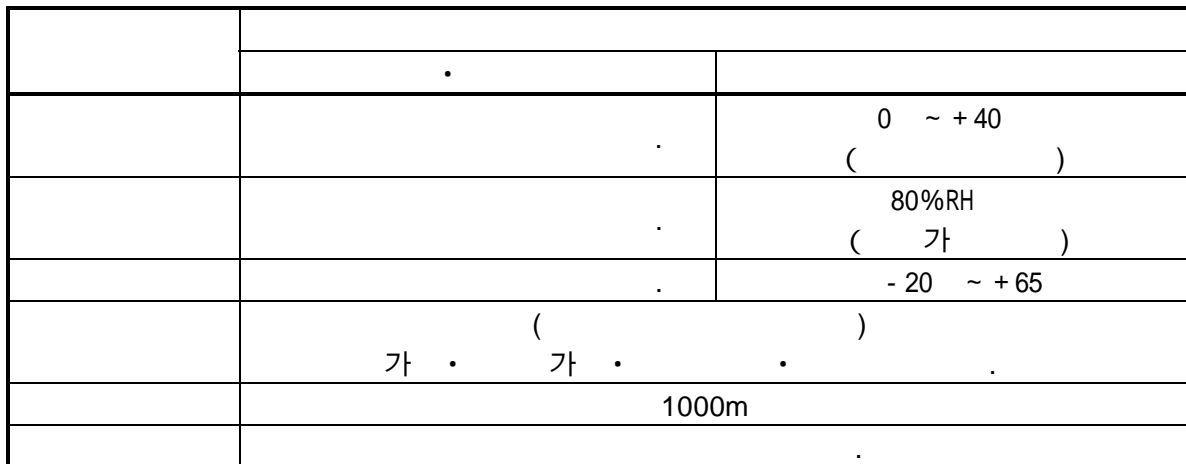
(2)



	
가	

(3) .

	
가	



가


$$(U, V, W)$$

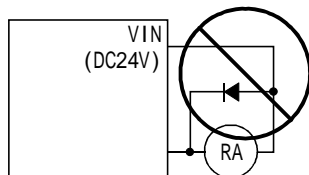
가


DC

가


가

가




	
<p>·</p> <p>가</p> <p>·</p>	<p>, , PLC ,</p>

(5) ·

	
<p>가</p> <p>·</p> <p>·</p> <p>·</p> <p>·</p>	<p>·</p> <p>·</p> <p>·</p> <p>·</p> <p>·</p>

(6)




가

CE , 「EMC Installation Guidelines」 (IB()
-67320) , EMC가

	Q61P-A1	Q61P-A2	Q63P	Q64P
	AC100 ~ 120V ^{+10%} _{-15%} (AC85 ~ 132V)	AC200 ~ 240V ^{+10%} _{-15%} (AC170 ~ 264V)	DC24V ^{+30%} _{-35%} (DC15.6 ~ 31.2V)	AC100 ~ 120V ^{+10%} _{-15%} /AC200 ~ 240V ^{+10%} _{-15%} (AC85 ~ 132V/ AC170 ~ 264V)
	50/60Hz ± 5%			
	20ms			

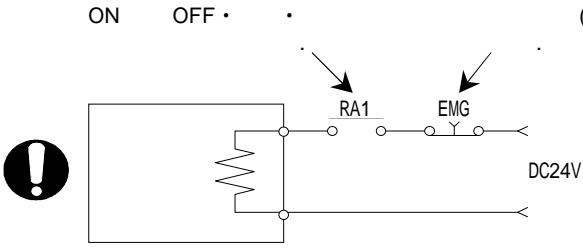
(7)



가

가


ON OFF • • (EMG)




가

가

(8) . .

	
가	
IC	가
가	가
가	()
(1)	S/W
(2)	S/W
가	가
가	가

(9)



(10)

가

※

	※	
2001 5	IB()-0300024-A	
2002 10	IB()-0300024-B	<p>[가]</p> <p>Q173CPUN/Q172CPUN, Q63P, Q64P, MR-J2M-B, A10BD-PCF, QY70, QY71, FREQROL-V500</p> <p>[가]</p> <p>• Windows2000/WindowsXP</p> <p>[]</p>

--

Q173CPU(N)/Q172CPU(N)

.....	A- 1
.....	A- 10
.....	A- 11
.....	A-15

1	1- 1~1- 4
---	-----------

1. 1	1- 1
1. 2 Q173CPU(N) / Q172CPU(N) A173UHCPU / A172SHCPUN	1- 3

2 CPU	2- 1~2-10
-------	-----------

2. 1 CPU	2- 1
----------------	------

3	3- 1~3-58
---	-----------

3. 1	3- 2
3. 1. 1	3-11
3. 1. 2	3-19
3. 1. 3	3-24
3. 2	3-35
3. 2. 1	3-39
3. 2. 2	3-45
3. 2. 3	3-46
3. 3	3-50
3. 4 (SP.M).....	3-51
3. 5 (SP.D).....	3-53

4	4- 1~4-28
---	-----------

4. 1	4- 1
4. 2	4- 1
4. 2. 1 1	4- 2
4. 2. 2	4- 5
4. 2. 3 /	4- 5
4. 2. 4	4- 6
4. 3 /	4- 7
4. 3. 1	4- 7
4. 3. 2 1, 2.....	4-15

4. 3. 3	1, 2.....	4-16
4. 3. 4	4-16
4. 3. 5	4-17
4. 3. 6	4-17
4. 3. 7	4-17
4. 3. 8	4-17
4. 3. 9	4-18
4. 3. 10	4-19
4. 3. 11	4-19
4. 3. 12	4-19
4. 3. 13	1.....	4-19
4. 3. 14	2.....	4-20
4. 3. 15	1, 2	4-21
4. 3. 16	4-21
4. 3. 17	4-22
4. 3. 18	4-22
4. 3. 19	5.....	4-22
4. 3. 20	PI-PID	4-22
4. 3. 21	4-22
4. 3. 22	4-22
4. 3. 23	(FR-V500)	4-23
4. 4	4-24
4. 4. 1	, 가 , ,	4-26
4. 4. 2	S	4-26
4. 4. 2	4-27

5	15- 1~5-16
---	------------

5. 1	5- 1
5. 1. 1	5- 1
5. 1. 2	5- 2
5. 2	5- 3
5. 3	5-10
5. 4	5-14
5. 4. 1	5-14
5. 4. 2	(D, W, #)	5-15

6	6- 1~6-170
---	------------

6. 1	6- 1
6. 1. 1	6- 1
6. 1. 2	6- 2
6. 1. 3	1	6- 6
6. 1. 4	6- 6
6. 1. 5	가 "degree"	6- 8
6. 1. 6	6-10
6. 1. 7	가	6-14
6. 2	1	6-16
6. 3	2	6-19

6. 4 3	6-23
6. 5 4	6-28
6. 6	6-32
6. 7	6-37
6. 8	6-43
6. 9	6-49
6. 9. 1	6-59
6. 10 1	6-68
6. 11 2	6-71
6. 12 3	6-74
6. 13 (I)	6-77
6. 14 (II)	6-81
6. 15	6-84
6. 15. 1	6-84
6. 15. 2	6-91
6. 16	6-96
6. 16. 1	6-96
6. 16. 2	6-102
6. 17	6-107
6. 17. 1	6-110
6. 17. 2	6-115
6. 17. 3 1	6-119
6. 17. 4 2~4	6-122
6. 17. 5	6-128
6. 17. 6	6-131
6. 17. 7 FIN	6-133
6. 18	6-140
6. 19	6-144
6. 20 JOG	6-147
6. 20. 1 JOG	6-147
6. 20. 2	6-148
6. 20. 3	6-152
6. 21	6-155
6. 22	6-161
6. 22. 1	6-161
6. 22. 2	6-163
6. 22. 3	6-165
6. 22. 4	6-166
6. 22. 5	6-167
6. 23	6-169

7	/	7- 1~7-16
---	---	-----------

7. 1 M	7- 1
7. 2	7- 4
7. 3	7- 6
7. 4	7- 8
7. 5	7-10
7. 6	7-12

7. 7	7-13
7. 7. 1	•	7-14
7. 8	7-15

	- 1~ -55
--	----------

1	CPU가	- 1
1. 1	(D9190).....	- 3
1. 2	- 7
1. 3	-14
1. 4	-17
1. 5	PC	-30
2	,	-31
2. 1	-31
2. 2	-34
3	-38
3. 1	M	-38
3. 2	-39
4	-40
5	CPU	-42

		()
C P U H / W · S / W	Q173CPU/Q172CPU CPU , Q182LX , Q172EX Q173PX , , , SSCNET (OS)	IB-0300021 (1CT760)
	Q173CPU/Q172CPU (SV13/SV22) (SFC) CPU , , , , (OS)	IB-0300023 (1CT761)
	Q173CPU/Q172CPU (SV22) (가) 가 , , , , (OS(SV22))	IB-0300025 (1CT763)
P L C S / W	QCPU (Q) () QCPU (Q) , , ()	SH-080020 (13JQ44)
	QCPU (Q)/QnACPU () , , ()	SH-080021 (13JC00)
	QCPU (Q)/QnACPU (PID) PID ()	SH-080022 (13JC01)
	QCPU (Q)/QnACPU (SFC) MELSAP3 , , , , ()	SH-080023 (13JC02)

		()
P L C	QCPU (Q) (•) CPU , , . ()	SH-080019 (13JQ43)
H / W	QPLC CPU , , / . ()	SH-080024 (13JQ45)

1.

1

1.1

1

(SV13/22) 가 가 .

CPU	
Q173CPU(N) (32)	32
Q172CPU(N) (8)	8

Q173CPU(N)/Q172CPU(N), CPU ()	Q173CPUN/Q172CPUN/Q173CPU/Q172CPU CPU
MR-H-BN	MR-H BN
MR-J2 -B	MR-J2S- B/MR-J2M-B/MR-J2- B/MR-J2-03B5
AMP	MR-H BN/MR-J2S- B/MR-J2M-B/MR-J2- B/MR-J2-03B5 , FREQROL-V500
Q172LX/Q172EX/Q173PX	Q172LX /Q172EX ABS /Q173PX
QCPU PLC CPU	Qn(H)CPU
CPU	Q PLC CPU
CPU _n	CPU n CPU (n=1~4)
S/W	「SW6RN-GSV P」 「GX Developer S/W」
GSV GSV P	S/W 「SW6RNC-GSVPRO」
SV13	: SW6RN-GSV13P
SV22	: SW6RN-GSV22P
GX Developer	GX Developer S/W 6
MR-HDP01	(MR-HDP01)
ABS MR-HENC	ABS (MR-HENC)
SSCNET *	↔
	(Q170FAN)
	(Q173DV)
	(Q170BAT)
A 0BD-PCF	A10BD-PCF/A30BD-PCF SSC I/F
	MELSECNET/H /Ethernet /CC-Link /
(FR-V500)	FREQROL-V500

* : SSCNET : Servo System Controller NETwork

1.



QCPU,

CPU

Q172CPU

(SV13/SV22)

(SFC)」

「Q173CPU/

SV22가

(SV22)

(가)」

「Q173CPU/Q172CPU



가

1.

1.2 Q173CPU(N)/Q172CPU(N) A173UHCPU/A172SHCPUN

(1) Q173CPU(N)/Q172CPU(N) A173UHCPU/A172SHCPUN

			Q173CPU(N)	Q172CPU(N)	A173UHCPU	A172SHCPUN
			32	8	32	8
	SV13		0.88ms／ 1～ 8 1.77ms／ 9～16 3.55ms／17～32 () (가)	0.88ms／ 1～8 () (가)	3.55ms／ 1～20 7.11ms／21～32	3.55ms／ 1～8
	SV22		0.88ms／ 1～ 4 1.77ms／ 5～12 3.55ms／13～24 7.11ms／25～32 () (가)	0.88ms／ 1～4 1.77ms／ 5～8 () (가)	3.55ms／ 1～12 7.11ms／13～24 14.22ms／25～32	3.55ms／ 1～8
			14k			13k
			3200 / (가)			
			PC/AT		PC9800 , PC/AT , A30TU, A31TU	
I/F			USB (12Mbps) / RS-232 (115.2kbps) /SSCNET (5.6Mbps)		RS-422 (9600kbps) /SSCNET (5.6Mbps)	
			1, , 2		1, , 1	
			3 가			1 가
			12 가	8 가	4 가	1 가
			32 , : /			
SSCNET I/F (PC 1CH)			5CH*1	2CH	4CH	2CH
			64 (Q 7)		8	2
. .			Q172LX 4 가 Q172EX 6 가 Q173PX 4 가 *2	Q172LX 1 가 Q172EX 4 가 Q173PX 3 가 *2	A172SENC 4 가	A172SENC 1 가

S F C	(가)		(0.88ms, 1.77ms, 3.55ms, 7.11ms, 14.2ms)		(1.77ms, 3.55ms, 7.11ms, 14.2ms)	
			QI60 16 ON		A1SI61 16 ON	
		PLC	PLC		PLC 1	
		NMI	QI60 16 ON		A1SI61 16 ON	
	(X/Y)		8192			2048
	(PX/PY)		256			
	(CPU)	(M)	M,L 8192		M,L(S) 8192	M,L(S) 2048
		(L)				
		(B)	8192			1024
		(F)	2048			256
		(TT)	—		2048	256
		(TC)	—		2048	256
		(CT)	—		1024	256
		(CC)	—		1024	256

1.

Q173CPU(N)/Q172CPU(N) A173UHCPU/A172SHCPUN ()

			Q173CPU (N)	Q172CPU (N)	A173UHCPU	A172SHCPUN
S F C	(CPU)	(M)	256			
		(D)	8192			1024
		(W)	8192			1024
		(T)	—		2048	256
		(C)	—		1024	256
		(D)	256			
		(#)	8192			
		(FT)	1 (888 μs)			
	PCPU SCPU		CPU		2	
		1	1~2147483647 [PLS]		1~65535 [PLS]	
		1	1~2147483647 [PLS] (PLS)		1~65535 [PLS] (PLS)	
			—		×1 , ×10 , ×100 , ×1000	
	PLC Ready (M2000)		(STOP RUN) M2000 ON , RUN 1 M2000 ON .		M2000 ON	
			(PX,M) (가)		(가)	
			(1 A6BAT/MR-BAT 가) ^{*3}		A6BAT/MR-BAT	

*1 : (Q173DV) (Q173J2B△CBL□M/Q173HB△CBL□M)

*2 : INC (SV22) 1 가 .

*3 : A6BAT/MR-BAT 가 , Q173DV (Q173CPU (N)) Q170BAT (Q172CPU (N)) .

2. CPU

2 CPU

2.1 CPU

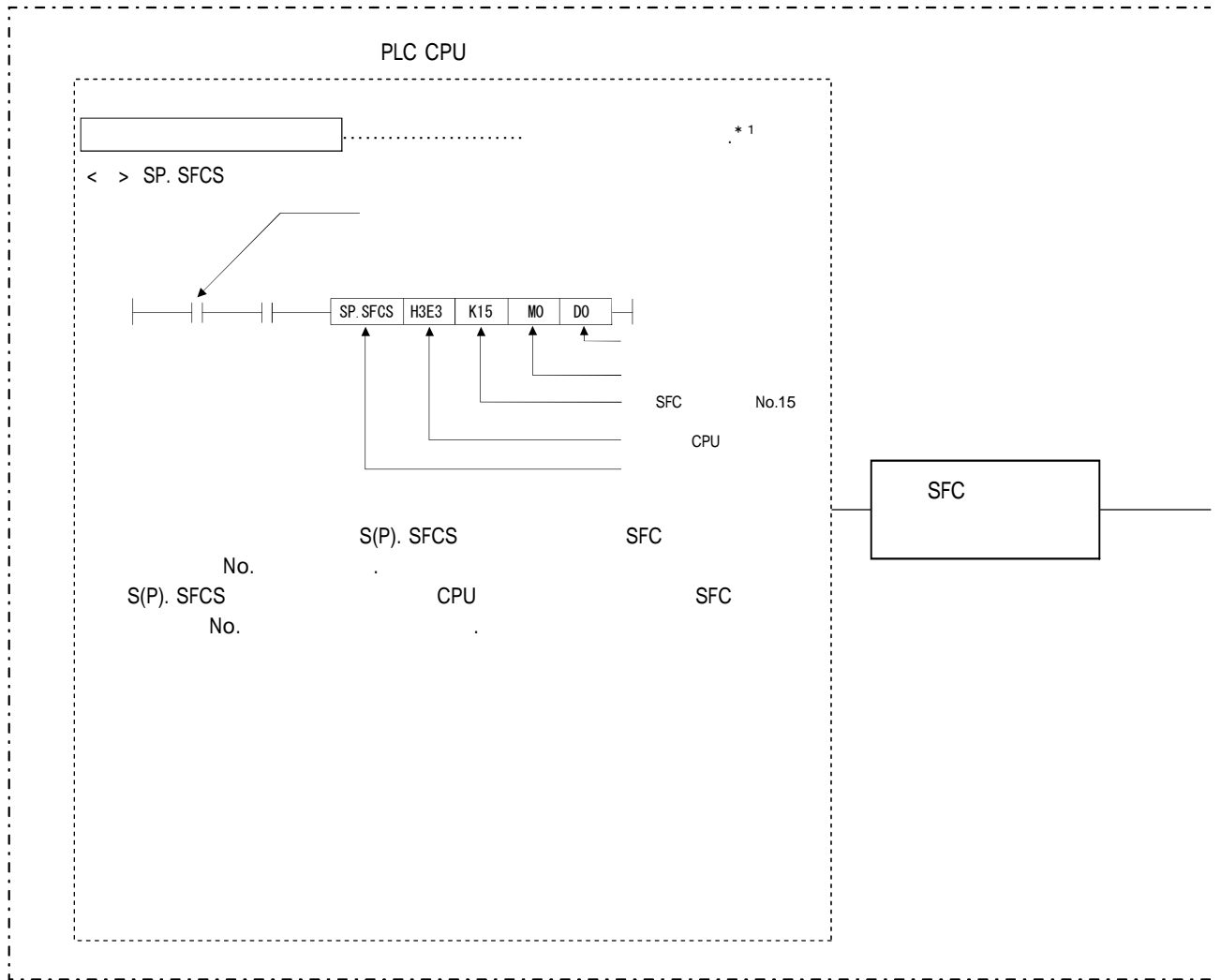
- CPU , Q173CPU(N) 32 , Q172CPU(N) 8
가 .
/ 4 .
- (1) SFC , SFC k .
SFC .
- (a) PLC CPU SFC (S(P).SFCS)
(b) SFC
) : NMI , k
.
- (c) SFC
- (2) CPU JOG .
- (3) CPU .
- (4) , F
(CHGV, CHGT) .
) : 「Q173CPU/Q172CPU
(SV13/SV22) (SFC)」 .

2. CPU

[SFC (S(P).SFCS)]

CPU PLC CPU S(P).SFCS SFC
 . (SFC
 .)
 SFC .

CPU



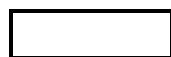
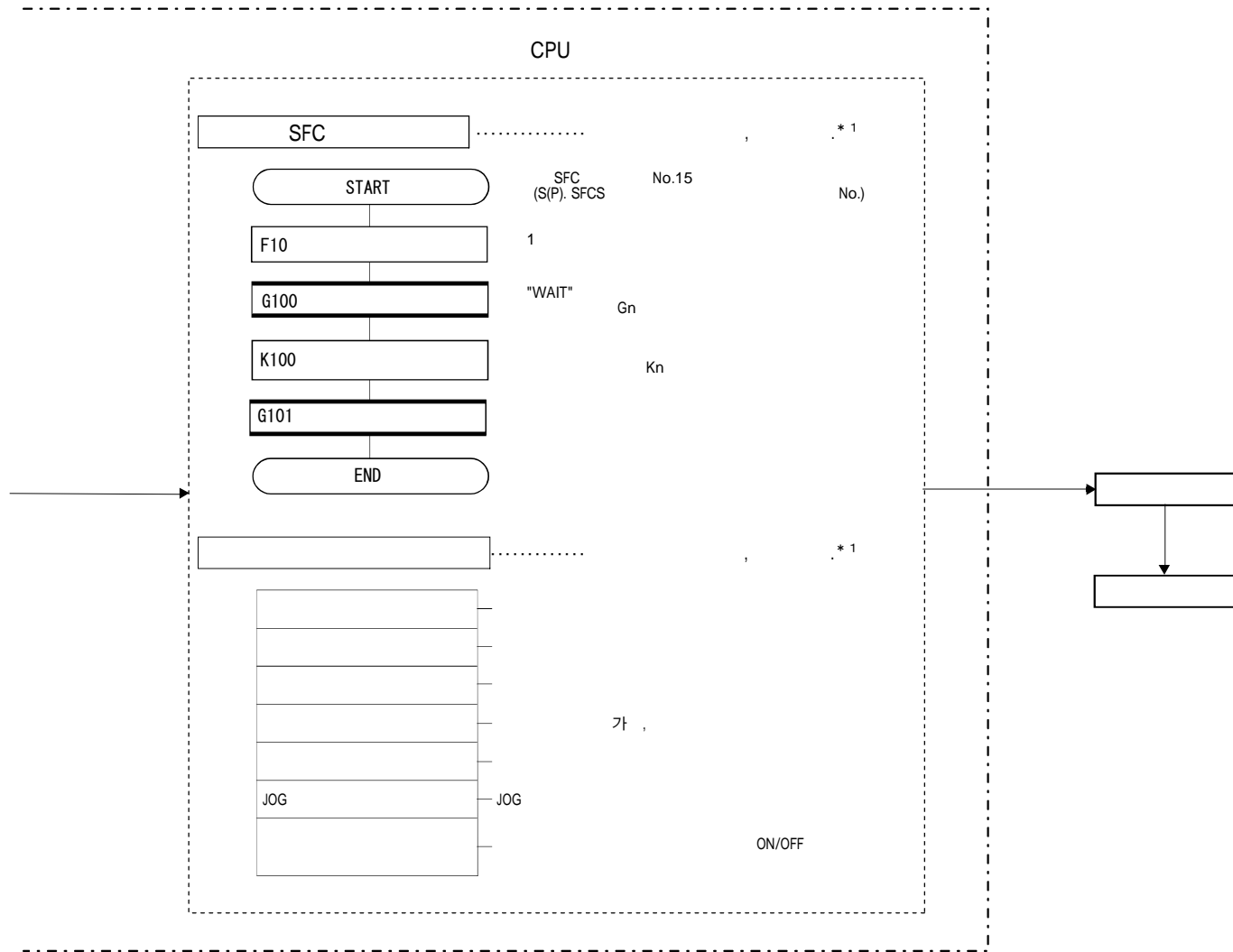
(1) , .

(2) PLC CPU (S(P). SFCS) .

(a) S(P). SFCS SFC No. .
 SFC No. , 가 .

(3) SFC .

2. CPU



*1 : 가

SW6RN-GSV P
Windows NT[®] 4.0/Windows[®] 98/Windows[®] 2000/Windows[®] XP가 가
(PC/AT)

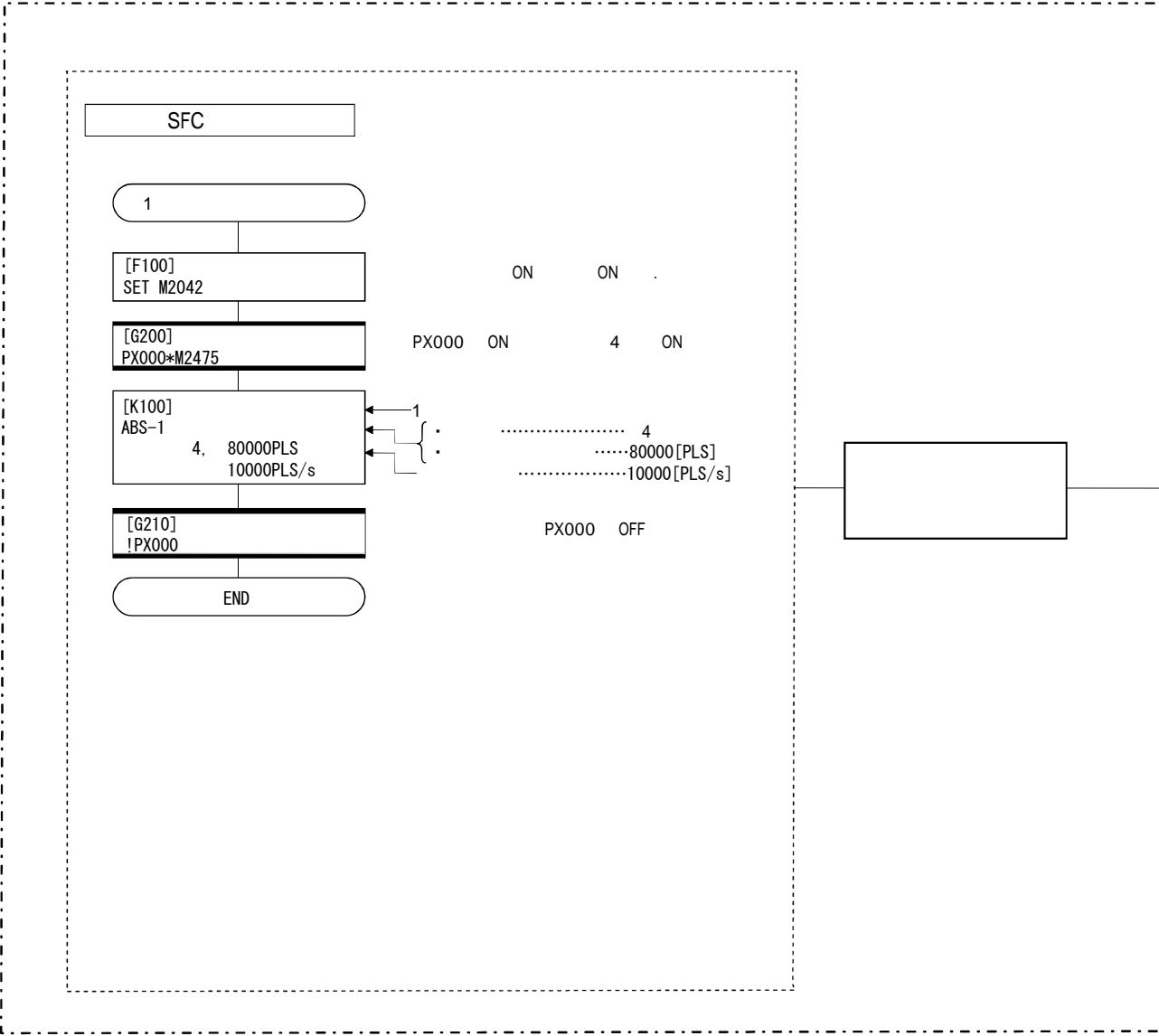
WindowsNT[®] /Windows[®] Microsoft Corporation
가

2. CPU

[(SFC)]

CPU SFC

CPU

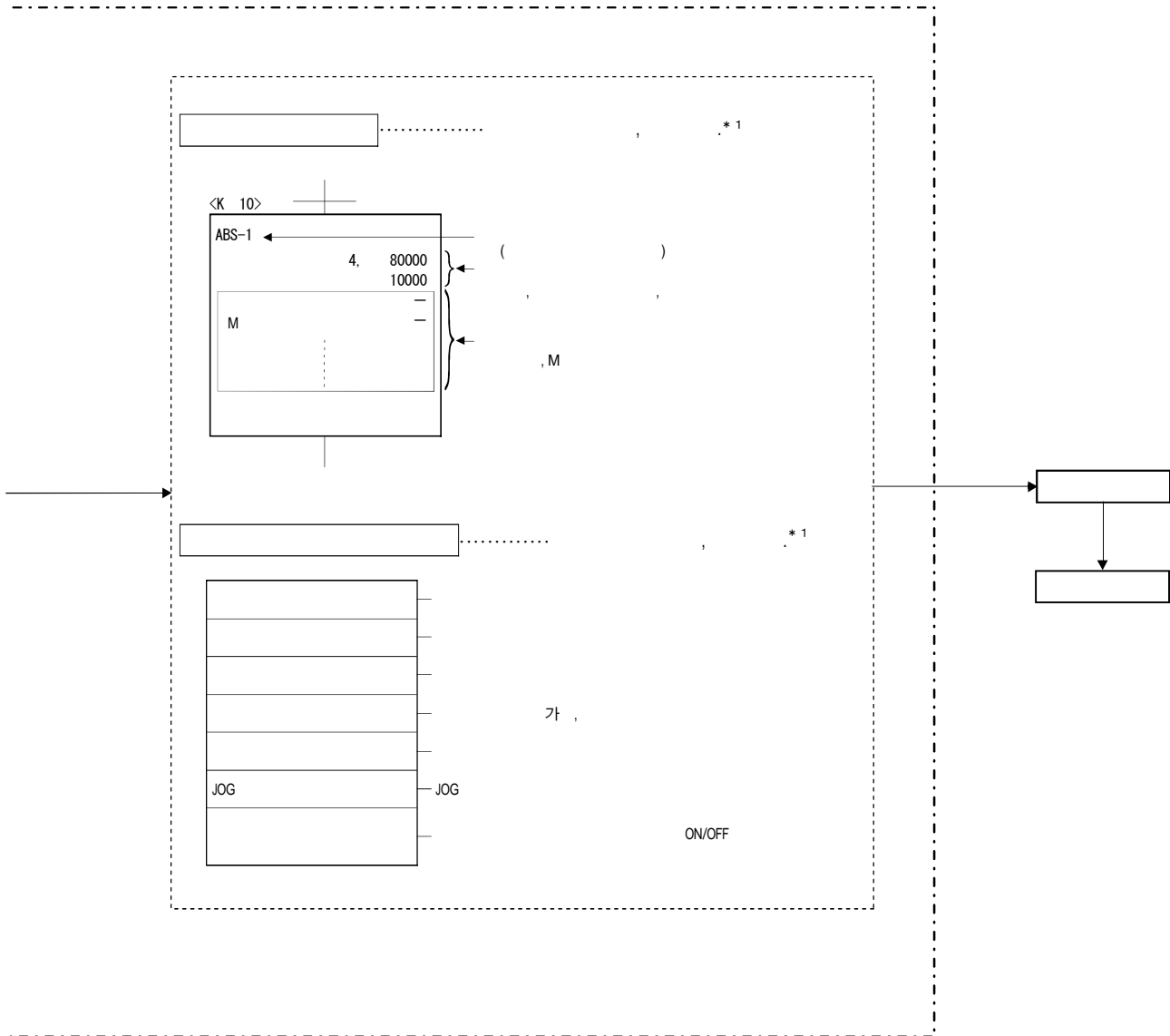


(1) ,

(2) SFC

(3)

2. CPU



*1 : 가

, SW6RN-GSV P

Windows NT[®] 4.0/Windows[®] 98/Windows[®] 2000/Windows[®] XP가 가
(PC/AT)

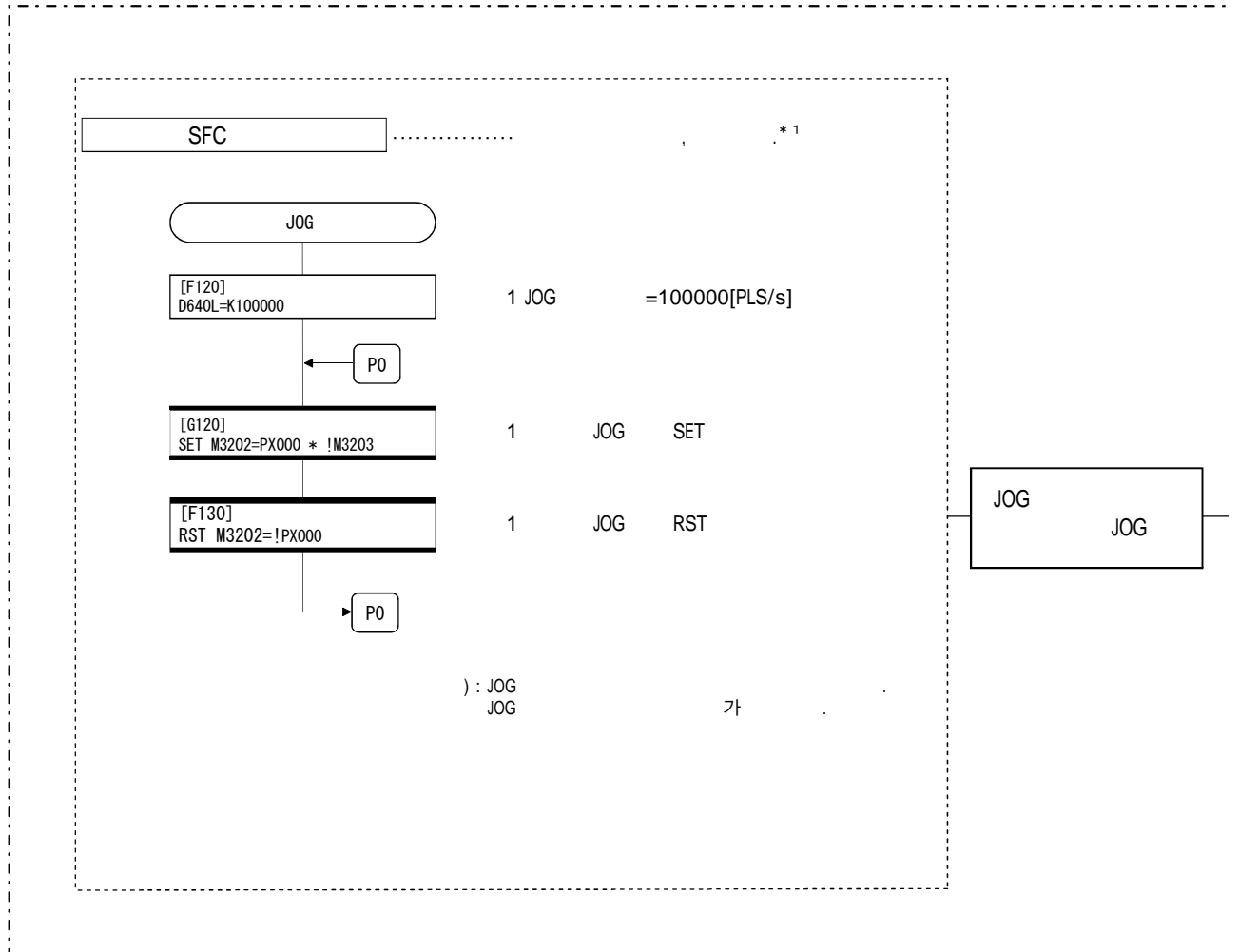
WindowsNT[®] /Windows[®] Microsoft Corporation
가

2. CPU

[JOG]

CPU , SFC JOG JOG .
JOG JOG .
JOG .

CPU

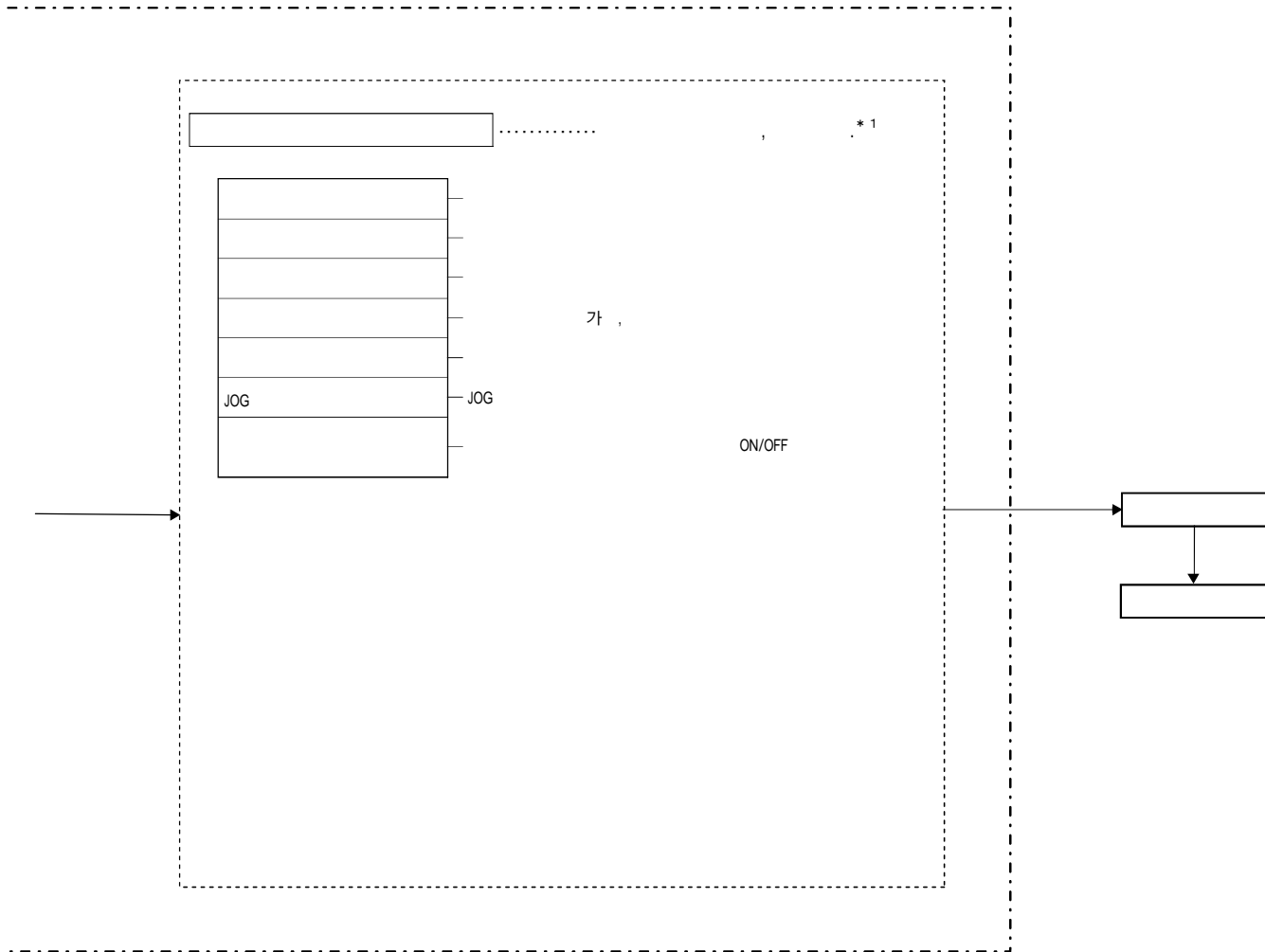


(1)

(2) SFC JOG JOG

(3) JOG SFC ON , JOG .

2. CPU



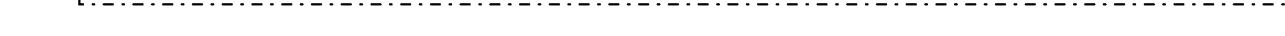
*1 : 가 , SW6RN-GSV P
 . Windows NT[®] 4.0/Windows[®] 98/Windows[®] 2000/Windows[®] XP가 가
 (PC/AT)

WindowsNT[®] /Windows[®] Microsoft Corporation
 가 .

가 가 .

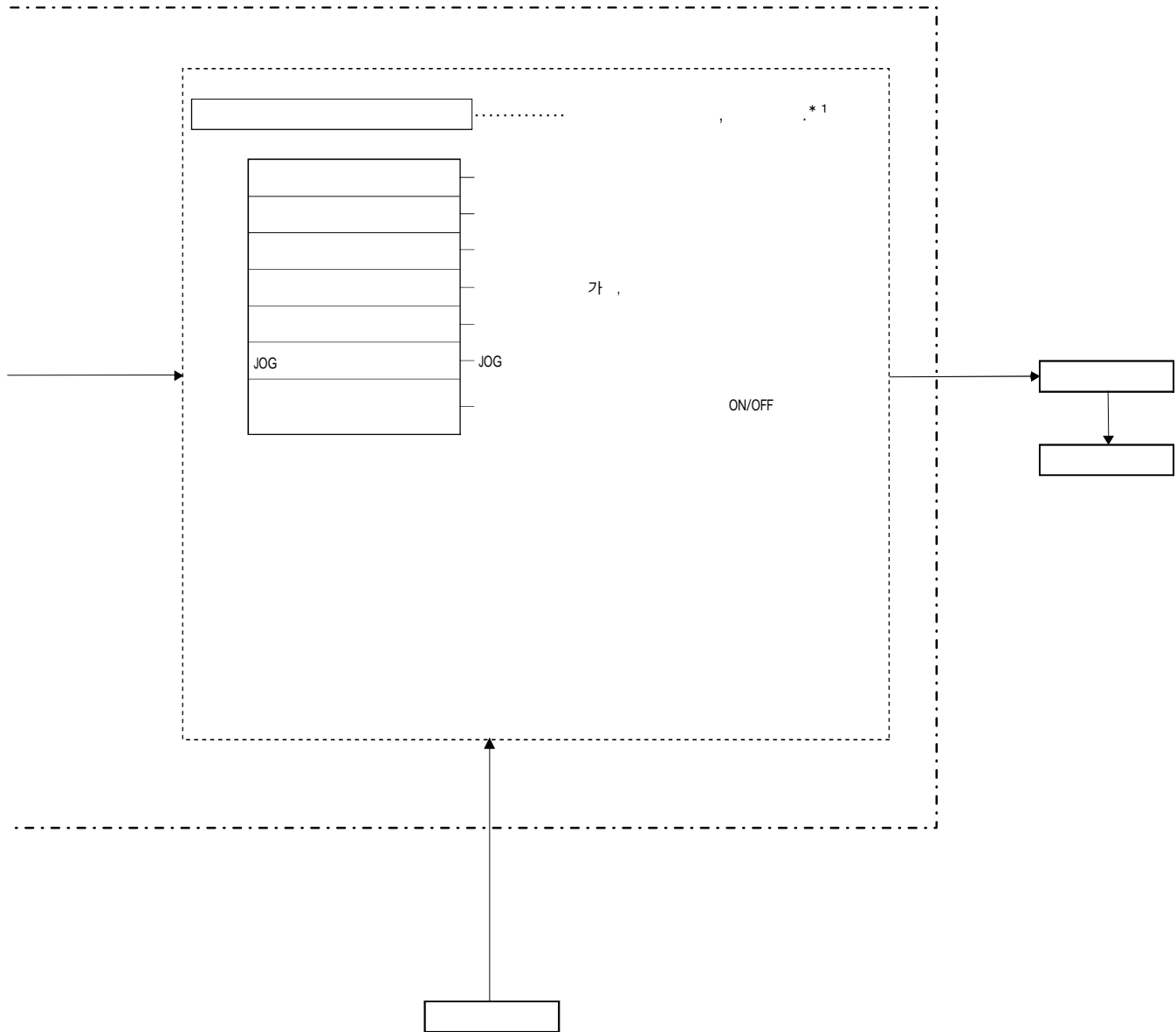
•

.....



.....가

2. CPU



*1 : 가 , SW6RN-GSV□P
 • Windows NT® 4.0/Windows® 98/Windows® 2000/Windows® XP가 가
 (PC/AT)

WindowsNT® /Windows® Microsoft Corporation
 가 .

2. CPU

(1)

7 가

가

1		CPU ,	4.1
2			4.2
3			4.3
4			6.22.1
5	JOG	JOG JOG , No.	6.20.1
6		16 가 , JOG 가 (가 ,)	4.4
7		, ON , 가/ ,	*

* : 「Q173CPU/Q172CPU (SV13/SV22) (SFC)」 13

(2)

SFC

No.,

「 5 」

• No. SFC

•

•

가

(3)

SFC

SFC

, JOG ,

「Q173CPU/Q172CPU

(SV13/SV22)

(SFC)」

CPU

CPU

(1)

CPU

CPU가 가

5

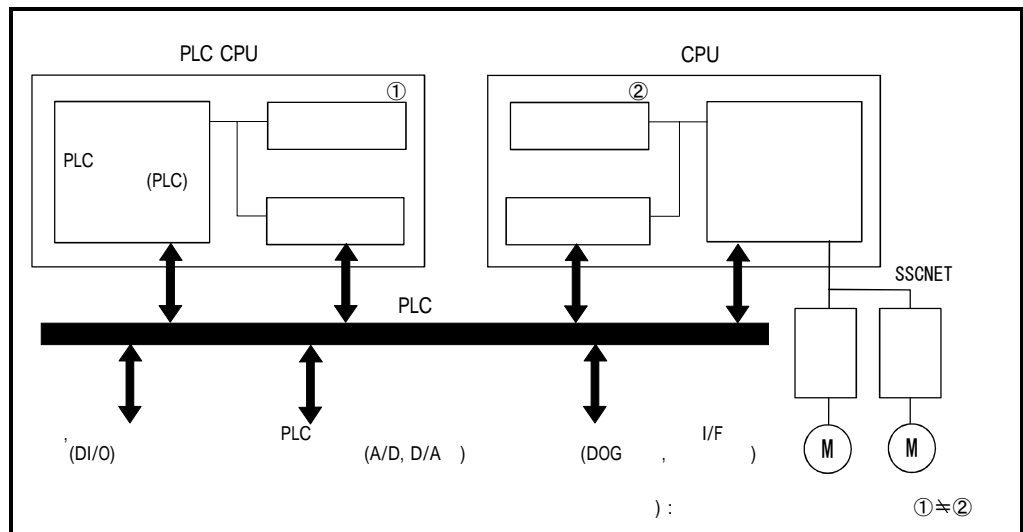
- (M)..... M2000~M3839 (1840)
- (SP.M) M9073~M9079 (7)
- (D)..... D0~D799 (800)
- #8000~#8191 (192)
- (SP.D) D9180~D9199 (20)

(2)

CPU

가

- / /
- ON/OFF
- →
- ON/OFF



3. 1 /

CPU
CPU

CPU

		Q173CPU (N)	Q172CPU (N)
		32	8
()	SV13	0.88[ms] / 1~8 1.77[ms] / 9~16 3.55[ms] / 17~32	0.88[ms] / 1~8
	SV22	0.88[ms] / 1~4 1.77[ms] / 5~12 3.55[ms] / 13~24 7.11[ms] / 25~32	0.88[ms] / 1~4 1.77[ms] / 5~8

3. 1

(1)

SV13		SV22	
M0		M0	
5	(2000)	5	(2000)
M2000		M2000	
5	(320)	5	(320)
M2320		M2320	
5	() (80)	5	() (80)
M2400		M2400	
5	(20 × 32)	5	(20 × 32) .. 가 ..
M3040		M3040	
5	가	5	가
M3072		M3072	
5	(64) ()	5	(64) ()
M3136		M3136	
5	() (64)	5	() (64)
M3200		M3200	
5	(20 × 32)	5	(20 × 32) .. 가 ..
M3839		M3839	

()

SV13		SV22	
M3840 S M8191	(4352)	M3840 S	가*1
		M4000 S	(640)
		M4640 S	(4 ×12)
		M4688 S	가*1
		M4800 S	(640)
		M5440 S	(4 ×12)
		M5488 S	가*1
		M5600 S	(2592)
		M8191	M8191

*1 : SV22

가

,

가

•
6352 (SV13), 5984 (SV22)

(2) ()

					*1
M2320					M9000
M2321	AC/DC DOWN				M9005
M2322					M9006
M2323					M9007
M2324					M9008
M2325					M9010
M2326	ON				M9036
M2327	OFF				M9037
M2328					M9026
M2329	PCPU WDT				M9073
M2330	PCPU				M9074
M2331					M9075
M2332					M9076
M2333					M9077
M2334					M9078
M2335					M9079
M2336	1				M9240
M2337	2				M9241
M2338	3				M9242
M2339	4				M9243
M2340	1				M9244
M2341	2				M9245
M2342	3				M9246
M2343	4				M9247
M2344					M9105
M2345	1 MULTR				M9216
M2346	2 MULTR				M9217
M2347	3 MULTR				M9218
M2348	4 MULTR				M9219
M2349					
가		—	—	—	—
M2399					

*1 :

(3)

No.																																																																																																														
1	M2400～M2419	<table><tr><td></td><td></td><td></td><td></td><td></td></tr><tr><td>0</td><td></td><td></td><td></td><td></td></tr><tr><td>1</td><td></td><td></td><td></td><td></td></tr><tr><td>2</td><td></td><td></td><td></td><td></td></tr><tr><td>3</td><td></td><td></td><td></td><td></td></tr><tr><td>4</td><td></td><td></td><td></td><td></td></tr><tr><td>5</td><td>•</td><td></td><td></td><td></td></tr><tr><td>6</td><td></td><td></td><td></td><td></td></tr><tr><td>7</td><td></td><td></td><td></td><td></td></tr><tr><td>8</td><td></td><td></td><td></td><td></td></tr><tr><td>9</td><td></td><td></td><td></td><td></td></tr><tr><td>10</td><td></td><td></td><td></td><td></td></tr><tr><td>11</td><td></td><td>FLS</td><td></td><td></td></tr><tr><td>12</td><td></td><td>RLS</td><td></td><td></td></tr><tr><td>13</td><td></td><td>STOP</td><td></td><td></td></tr><tr><td>14</td><td></td><td>DOG/CHANGE</td><td></td><td></td></tr><tr><td>15</td><td>Ready</td><td></td><td></td><td></td></tr><tr><td>16</td><td></td><td></td><td></td><td></td></tr><tr><td>17</td><td>가</td><td>—</td><td>—</td><td>—</td></tr><tr><td>18</td><td>가 (SV22) *1</td><td>가</td><td></td><td></td></tr><tr><td>19</td><td>M</td><td></td><td></td><td></td></tr></table>									0					1					2					3					4					5	•				6					7					8					9					10					11		FLS			12		RLS			13		STOP			14		DOG/CHANGE			15	Ready				16					17	가	—	—	—	18	가 (SV22) *1	가			19	M			
0																																																																																																														
1																																																																																																														
2																																																																																																														
3																																																																																																														
4																																																																																																														
5	•																																																																																																													
6																																																																																																														
7																																																																																																														
8																																																																																																														
9																																																																																																														
10																																																																																																														
11						FLS																																																																																																								
12						RLS																																																																																																								
13						STOP																																																																																																								
14						DOG/CHANGE																																																																																																								
15	Ready																																																																																																													
16																																																																																																														
17	가	—	—	—																																																																																																										
18	가 (SV22) *1	가																																																																																																												
19	M																																																																																																													
2	M2420～M2439																																																																																																													
3	M2440～M2459																																																																																																													
4	M2460～M2479																																																																																																													
5	M2480～M2499																																																																																																													
6	M2500～M2519																																																																																																													
7	M2520～M2539																																																																																																													
8	M2540～M2559																																																																																																													
9	M2560～M2579																																																																																																													
10	M2580～M2599																																																																																																													
11	M2600～M2619																																																																																																													
12	M2620～M2639																																																																																																													
13	M2640～M2659																																																																																																													
14	M2660～M2679																																																																																																													
15	M2680～M2699																																																																																																													
16	M2700～M2719																																																																																																													
17	M2720～M2739																																																																																																													
18	M2740～M2759																																																																																																													
19	M2760～M2779																																																																																																													
20	M2780～M2799																																																																																																													
21	M2800～M2819																																																																																																													
22	M2820～M2839																																																																																																													
23	M2840～M2859																																																																																																													
24	M2860～M2879																																																																																																													
25	M2880～M2899																																																																																																													
26	M2900～M2919																																																																																																													
27	M2920～M2939																																																																																																													
28	M2940～M2959																																																																																																													
29	M2960～M2979																																																																																																													
30	M2980～M2999																																																																																																													
31	M3000～M3019																																																																																																													
32	M3020～M3039																																																																																																													

*1 : SV13/SV22 가 .
 *2 : Q172CPU (N) No.1~ No.8 가 .
 *3 : Q172CPU (N) 9 가 .

(4) ()

					*1, *2
M3072	PLC Ready				M2000
M3073					M2040
M3074	ON				M2042
M3075	/가				M2043
M3076	JOG				M2048
M3077	1 가				M2051
M3078	2 가				M2052
M3079	3 가				M2053
M3080	가	—	—	—	—
↵					
M3135					

*1 : OFF ON 가 ON , ON OFF 가 OFF .
ON/OFF 가 , 가 .
*2 : 가 .

(5) ()

					*1, *2
M3136					M9025
M3137					M9028
M3138					M9060
M3139					M9104
M3140	가	—	—	—	—
↵					
M3199					

*1 : OFF ON 가 ON , ON OFF 가 OFF .
ON/OFF 가 , 가 .
*2 : 가 .

(6)

No.		
1	M3200~M3219	
2	M3220~M3239	
3	M3240~M3259	
4	M3260~M3279	
5	M3280~M3299	
6	M3300~M3319	
7	M3320~M3339	
8	M3340~M3359	
9	M3360~M3379	
10	M3380~M3399	
11	M3400~M3419	
12	M3420~M3439	
13	M3440~M3459	
14	M3460~M3479	
15	M3480~M3499	
16	M3500~M3519	
17	M3520~M3539	
18	M3540~M3559	
19	M3560~M3579	
20	M3580~M3599	
21	M3600~M3619	
22	M3620~M3639	
23	M3640~M3659	
24	M3660~M3679	
25	M3680~M3699	
26	M3700~M3719	
27	M3720~M3739	
28	M3740~M3759	
29	M3760~M3779	
30	M3780~M3799	
31	M3800~M3819	
32	M3820~M3839	

0				
1				
2	JOG			
3	JOG			
4	OFF			
5	가			
6	가	—	—	—
7				
8				
9	STOP			
10	가	—	—	—
11				
12				
13	(SV22)*1		가	
14	(SV22)*1			
15	OFF			
16	가	—	—	—
17				
18				
19	FIN			

*1 : SV13/SV22

*2 : Q172CPU(N)

*3 : Q172CPU(N)

No.1~ No.8

9

(7)

					*5
M2000	PLC Ready			*4	M3072
M2001	1				
M2002	2				
M2003	3				
M2004	4				
M2005	5				
M2006	6				
M2007	7				
M2008	8				
M2009	9				
M2010	10				
M2011	11				
M2012	12				
M2013	13				
M2014	14				
M2015	15				
M2016	16				
M2017	17				
M2018	18				
M2019	19				
M2020	20				
M2021	21				
M2022	22				
M2023	23				
M2024	24				
M2025	25				
M2026	26				
M2027	27				
M2028	28				
M2029	29				
M2030	30				
M2031	31				
M2032	32				
M2033	가	—	—	—	—
M2034	PC				
M2035					
M2036	가	—	—	—	—
M2037	(4)				
M2038					
M2039	SFC				
M2040				*4	M3073
M2041					
M2042	ON			*4	M3074
M2043	/가 (가)		가		M3075
M2044	/가 (가)				
M2045	/가 (가)	가			
M2046	(가)				
M2047					
M2048	JOG			*4	M3076
M2049	ON				
M2050					
M2051	1 가			*4	M3077
M2052	2 가				M3078
M2053	3 가				M3079
M2054					
M2055					
M2056	가	—	—	—	—
M2057					
M2058	(6)				
M2059					
M2060					
M2061	1				
M2062	2				
M2063	3				
M2064	4				
M2065	5			*1, *2	
M2066	6				
M2067	7				
M2068	8				
M2069	9				

					*5
M2070	10				
M2071	11				
M2072	12				
M2073	13				
M2074	14				
M2075	15				
M2076	16				
M2077	17				
M2078	18				
M2079	19				
M2080	20				
M2081	21			*1, *2	
M2082	22				
M2083	23				
M2084	24				
M2085	25				
M2086	26				
M2087	27				
M2088	28				
M2089	29				
M2090	30				
M2091	31				
M2092	32				
M2093					
M2094					
M2095					
M2096	가	—	—	—	—
M2097	(8)				
M2098					
M2099					
M2100					
M2101	1				
M2102	2				
M2103	3				
M2104	4				
M2105	5				
M2106	6				
M2107	7	*3		*1, *2	
M2108	8	(12)			
M2109	9				
M2110	10				
M2111	11				
M2112	12				
M2113					
M2114					
M2115					
M2116					
M2117					
M2118	가	—	—	—	—
M2119	(15)				
M2120					
M2121					
M2122					
M2123					
M2124					
M2125					
M2126					
M2127					
M2128	1				
M2129	2				
M2130	3				
M2131	4				
M2132	5				
M2133	6				
M2134	7				
M2135	8				
M2136	9				
M2137	10				
M2138	11			*1, *2	
M2139	12				

()

					*5						*5
M2140	13										
M2141	14										
M2142	15										
M2143	16										
M2144	17										
M2145	18										
M2146	19										
M2147	20										
M2148	21										
M2149	22										
M2150	23										
M2151	24										
M2152	25										
M2153	26										
M2154	27										
M2155	28										
M2156	29										
M2157	30										
M2158	31										
M2159	32										
M2160											
M2161	1										
M2162											
M2163	2										
M2164											
M2165	3										
M2166											
M2167	4										
M2168											
M2169	5										
M2170											
M2171	6										
M2172											
M2173	7										
M2174											
M2175	8										
M2176											
M2177	9										
M2178											
M2179	10										
M2180											
M2181	11										
M2182											
M2183	12										
M2184											
M2185	13										
M2186											
M2187	14										
M2188											
M2189	15										
M2190											
M2191	16										
M2192											
M2193	17										
M2194											
M2195	18										
M2196											
M2197	19										
M2198											
M2199	20										
M2200											
M2201	21										
M2202											
M2203	22										
M2204											
M2205	23										
M2206											
M2207	24										
M2208											
M2209	25										
M2210											
M2211	26										
M2212											
M2213	27										
M2214											
M2215	28										
M2216											
M2217	29										
M2218											
M2219	30										
M2220											
M2221	31										
M2222											
M2223	32										
M2224											
M2225											
M2226											
M2227											
M2228											
M2229											
M2230											
M2231											
M2232	(16)	가									
M2233											
M2234											
M2235											
M2236											
M2237											
M2238											
M2239											
M2240	1										
M2241	2										
M2242	3										
M2243	4										
M2244	5										
M2245	6										
M2246	7										
M2247	8										
M2248	9										
M2249	10										
M2250	11										
M2251	12										
M2252	13										
M2253	14										
M2254	15										
M2255	16										
M2256	17										
M2257	18										
M2258	19										
M2259	20										
M2260	21										
M2261	22										
M2262	23										
M2263	24										
M2264	25										
M2265	26										
M2266	27										
M2267	28										
M2268	29										
M2269	30										
M2270	31										
M2271	32										
M2272											
M2273											
M2274											
M2275											
M2276	(8)	가									
M2277											
M2278											
M2279											


3.

()

					*5						*5
M2280	(20) 가	—	—	—	—	M2300	(20) 가	—	—	—	—
M2281						M2301					
M2282						M2302					
M2283						M2303					
M2284						M2304					
M2285						M2305					
M2286						M2306					
M2287						M2307					
M2288						M2308					
M2289						M2309					
M2290						M2310					
M2291						M2311					
M2292						M2312					
M2293						M2313					
M2294						M2314					
M2295						M2315					
M2296						M2316					
M2297						M2317					
M2298						M2318					
M2299						M2319					

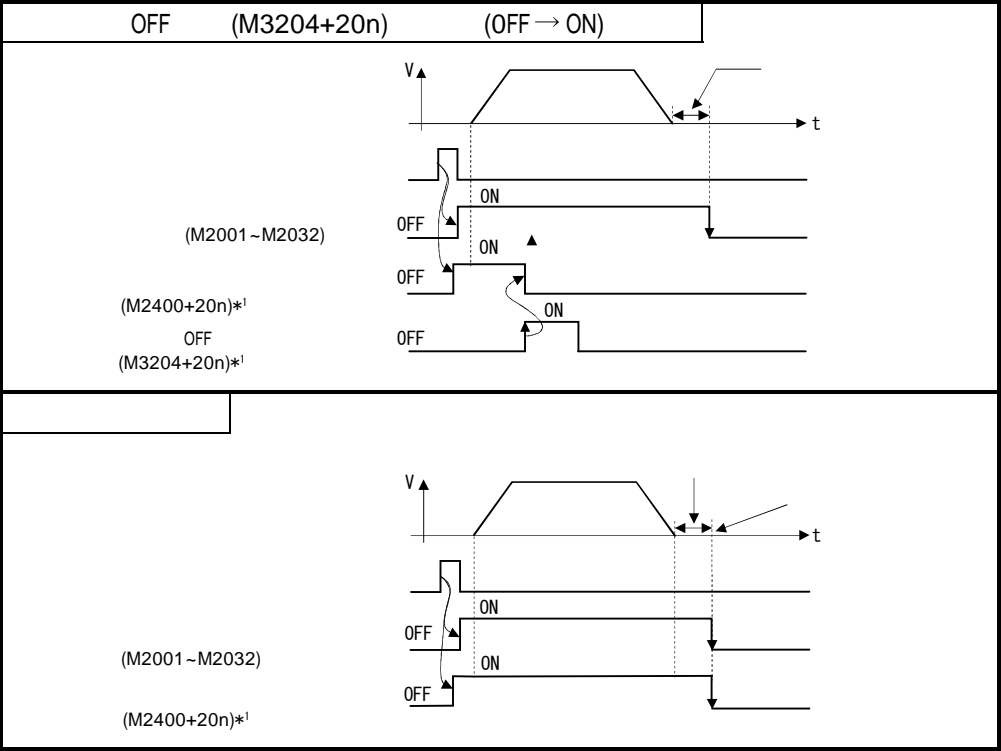
No.			
1	PLC Ready	M2000	D704
2		M2040	D705
3	ON	M2042	D706
4	/가 (SV22)	M2043	D707
5	JOG	M2048	D708
6	1 가	M2051	D755
7	2 가	M2052	D756
8	3 가	M2053	D757

- *1 : Q172CPU(N) No.1~ No.8 가 .
- *2 : Q172CPU(N) 9 가 .
- *3 : SV13/SV22 가 .
- *4 : D704~D708, D755~D757
 QCPU ON/OFF , D
 가 0 1 가 ON,
 가 1 0 가 OFF .
 S(P).DDR, S(P).DDWR , QPLC CPU
 . S(P).DDR, S(P).DDWR 「Q173CPU/Q172CPU
 (SV13/SV22) (SFC)」
 SFC ON/OFF가 가 .
- *5 : 가 .

	
● SFC	가
가	가 .

3. 1. 1

- (1) (M2400+20n)
(a) JOG , ON
M (7.1)
(b) OFF (M3204+ 20n) (OFF → ON),
OFF

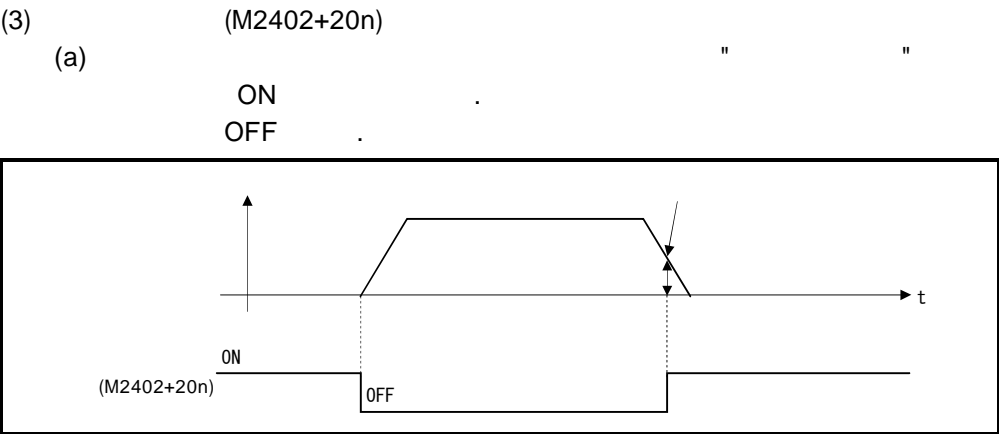
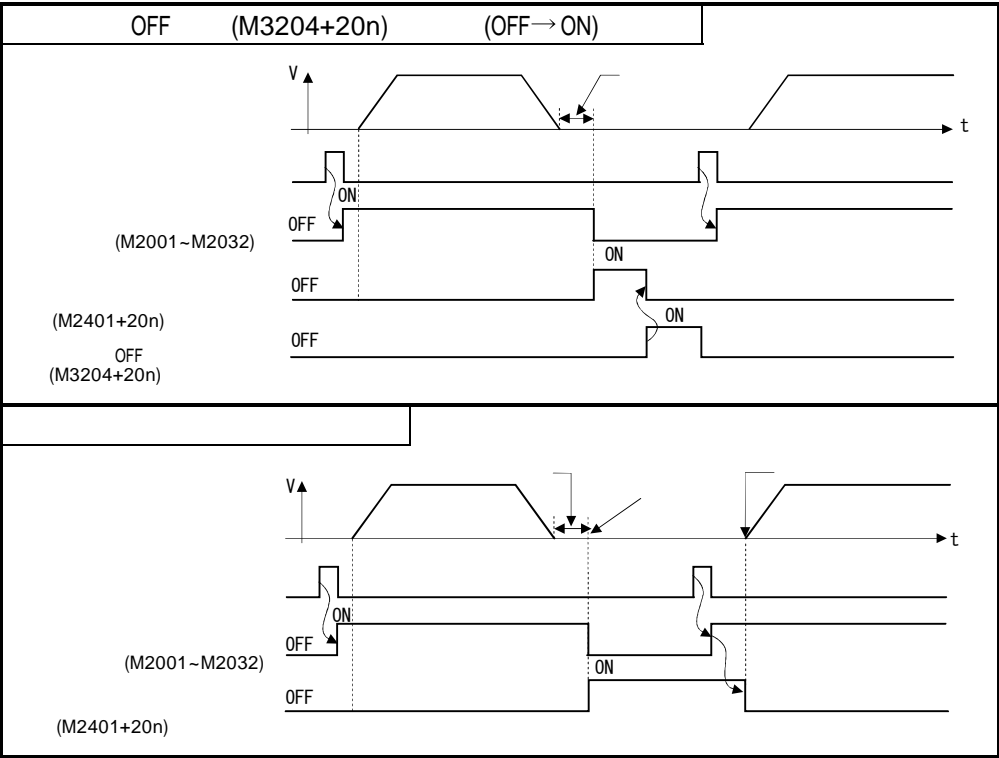


*1 : M3204+20n n , No.

No.	n	No.	n	No.	n	No.	n
1	0	9	8	17	16	25	24
2	1	10	9	18	17	26	25
3	2	11	10	19	18	27	26
4	3	12	11	20	19	28	27
5	4	13	12	21	20	29	28
6	5	14	13	22	21	30	29
7	6	15	14	23	22	31	30
8	7	16	15	24	23	32	31

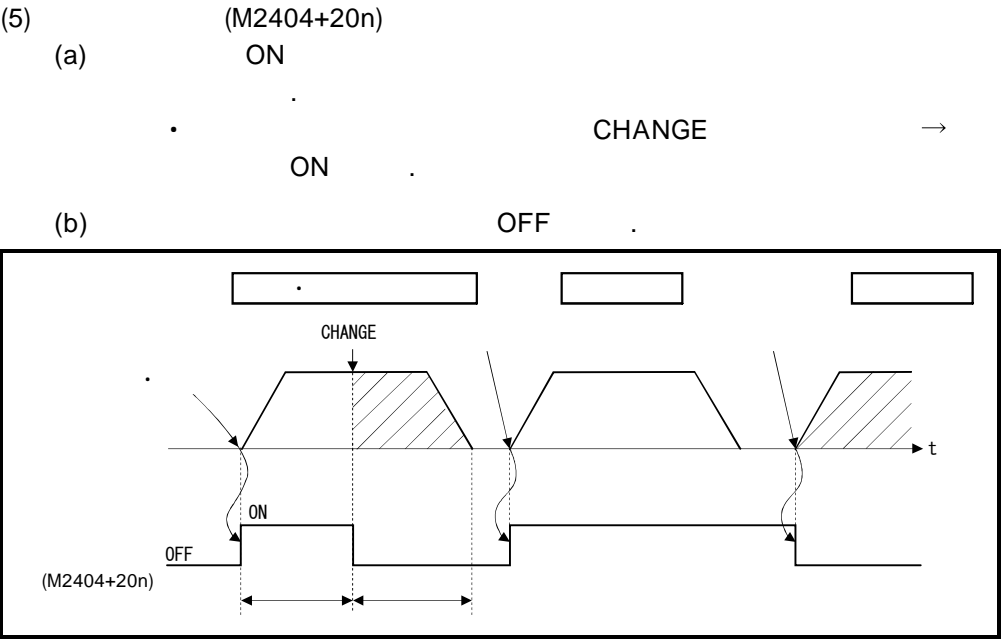
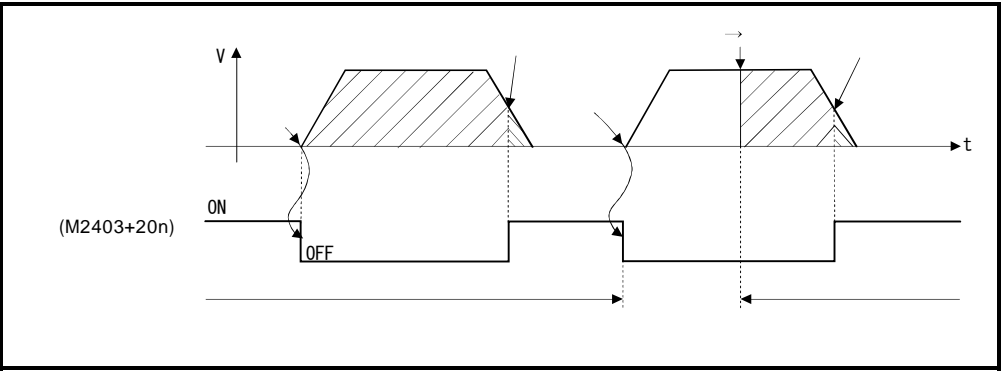
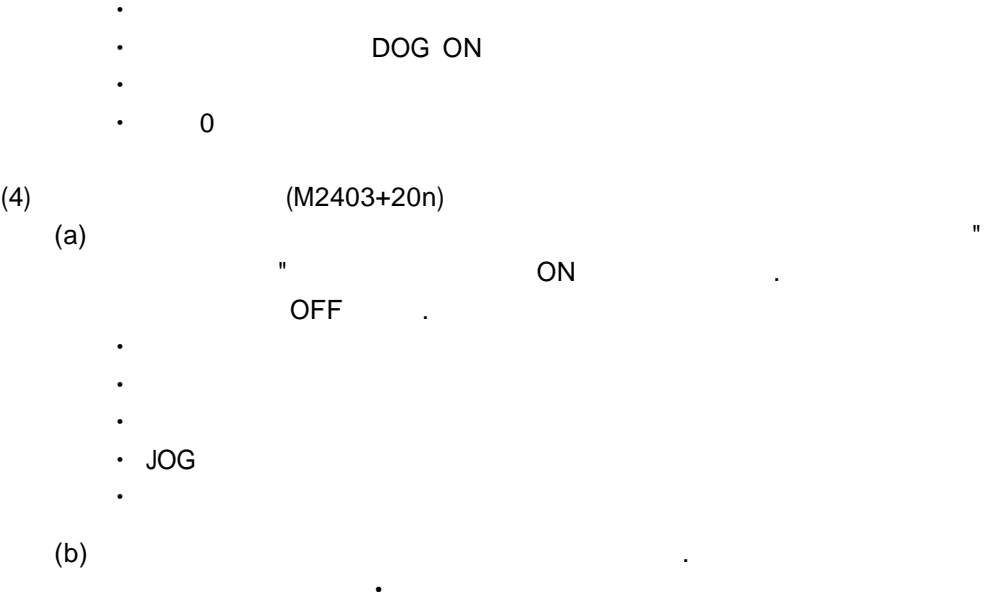
* : No.
() M3200+20n () =M3200+20×31=M3820
M3215+20n (OFF) =M3215+20×31=M3835
* : Q172CPU(N) No.1~ No.8 (n=0~7)가

(2) (M2401+20n)
(a) , JOG , ON
ON
ON
M (7.1)
(b) OFF (M3204+20n) (OFF→ON)
OFF



(b)

- ON
- JOG OFF



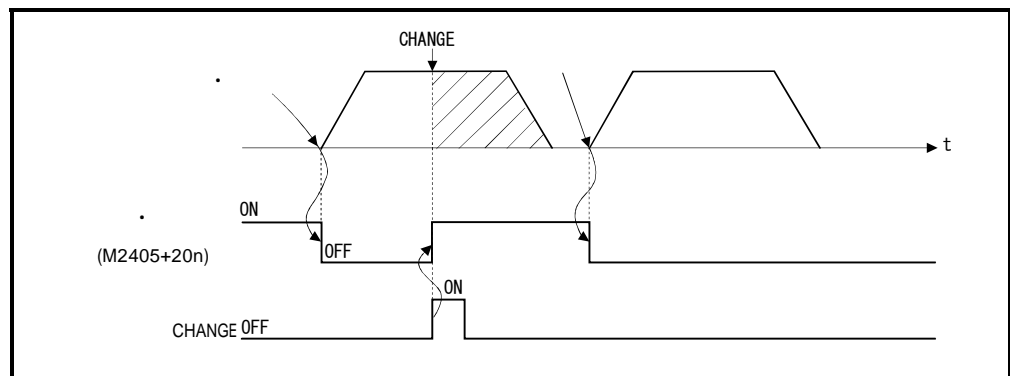
(6) (M2405+20n) ON

(a) →

스

(b) OFF

• JOG



(7) (M2406+20n)

CPU ON

ON

OFF ON

(8) (M2407+20n)

(a) ON /

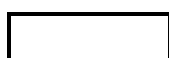
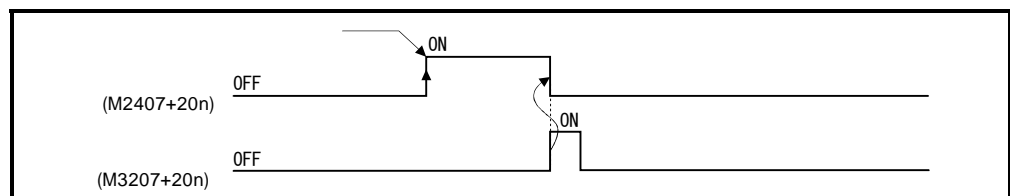
*1가

(3. 2. 1)

*2가

(3. 2. 1)

(b) (M3207+20n) ON OFF

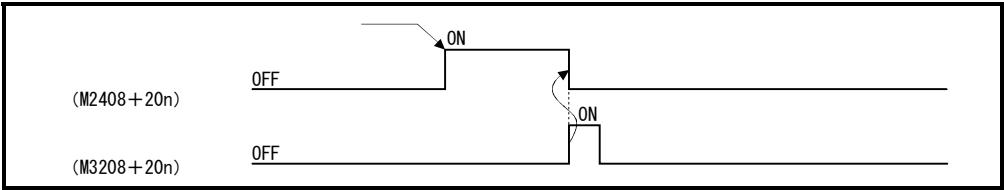


*1 : 1. 2

*2 : 1. 3

(9) (M2408+20n))^{*1}
(a) ON , *1가

(b) (M3208+20n) ON
OFF



*1 : 1.4

(10) (M2409+20n) ON

(a) ① ON CPU
② OFF
(b) ① ON
② () OFF

● , , , ,

(11) (M2410+20n)

(a) ON

(b) , JOG , OFF

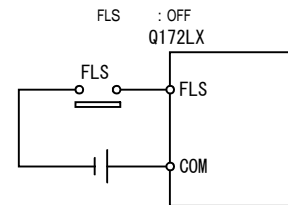
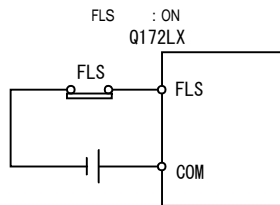
(c) 가 ON
" (115)"가

(12) FLS (M2411+20n)

(a) Q172LX (FLS) ON/OFF

· OFF····· FLS : ON
· ON ····· FLS : OFF

(b) FLS 가 ON/OFF (FLS)

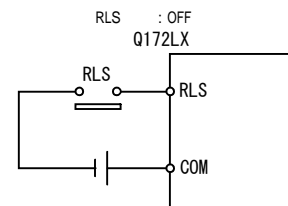
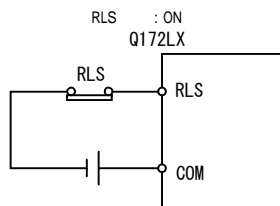


(13) RLS (M2412+20n)

(a) Q172LX (RLS) ON/OFF

· OFF····· RLS : ON
· ON ····· RLS : OFF

(b) RLS 가 ON/OFF (RLS)



(14) STOP (M2413+20n)

(a) Q172LX (STOP) ON/OFF

· 가 OFF····· STOP : OFF
· 가 ON ····· STOP : ON

(b) STOP 가 ON/OFF Q172LX (STOP)



(15) DOG/CHANGE (M2414+20n)

(a) , Q172LX DOG (DOG) ON/OFF
 , Q172LX (CHANGE) ON/OFF

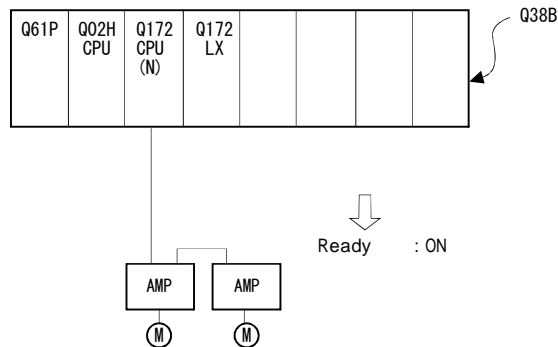
(b) 「A」 「B」 가
 CHANGE 가 ON/OFF (CHANGE)



(16) Ready (M2415+20n)

(a) 가 READY ON

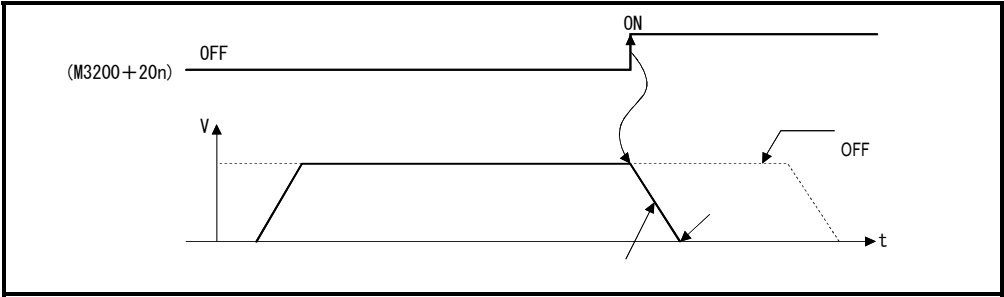
(b) OFF
 • M2042가 OFF
 • 가
 • 가
 •
 • OFF (M3215+20n) ON OFF
 • 가
 「 1.4 」



SSCNET	OFF	, 1	가	가
--------	-----	-----	---	---

3. 1. 2

(1) (M3200+20n)
(a) , (OFF→ON)
. (ON .)

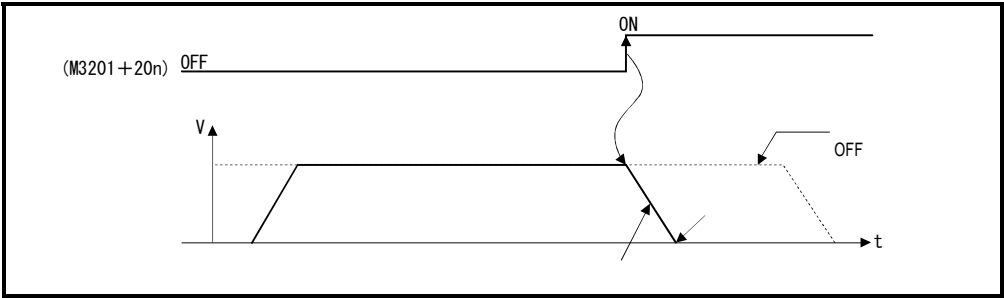


(b) (6.13 6.14 .)

	ON	
(I, II)		
JOG		
	(1)	
	(2)	가 [202]가

	(M3200+20n) ON
ON	ON JOG ,
ON	,

(2) (M3201+20n)
(a) , (OFF→ON)
. (ON .)



(b) ON

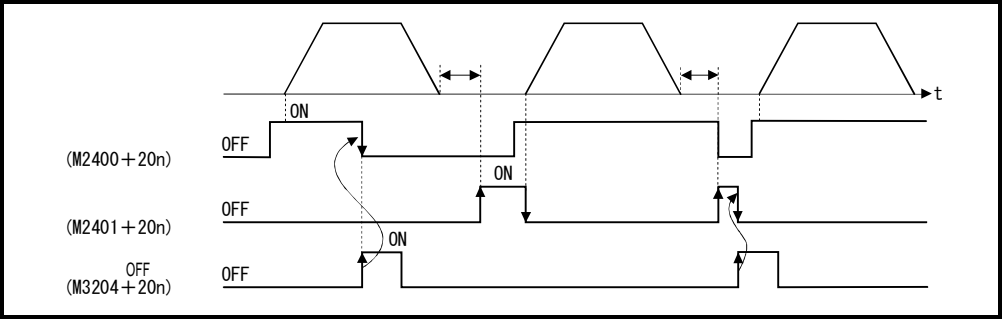
	ON	
(I, II)		
JOG		
	(1) (2) 가 [203]가	

	(M3201+20n) ON			
	ON		ON	JOG ,
	ON			

- (3) JOG (M3202+20n)/ JOG (M3203+20n)
- (a) M3202+20n ON , 가 JOG
M3202+20 OFF
- (b) M3203+20n ON , JOG
M3203+20n OFF

	JOG (M3202+20n)	JOG (M3203+20n)
ON		

- (4) OFF (M3204+20n)
- (a) (M2400+20n), (M2401+20n)
OFF

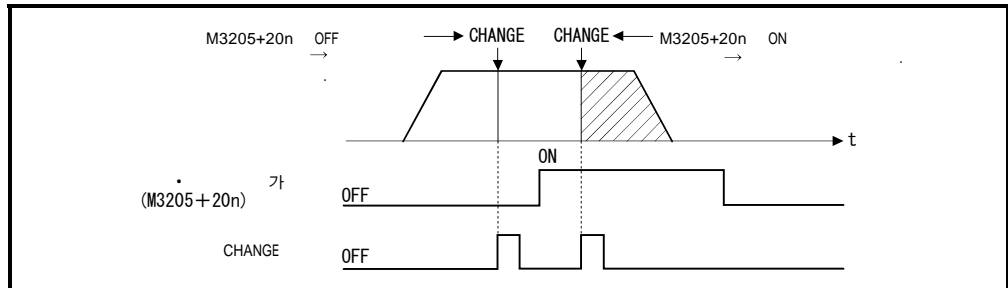


(5) 가 (M3205+20n)

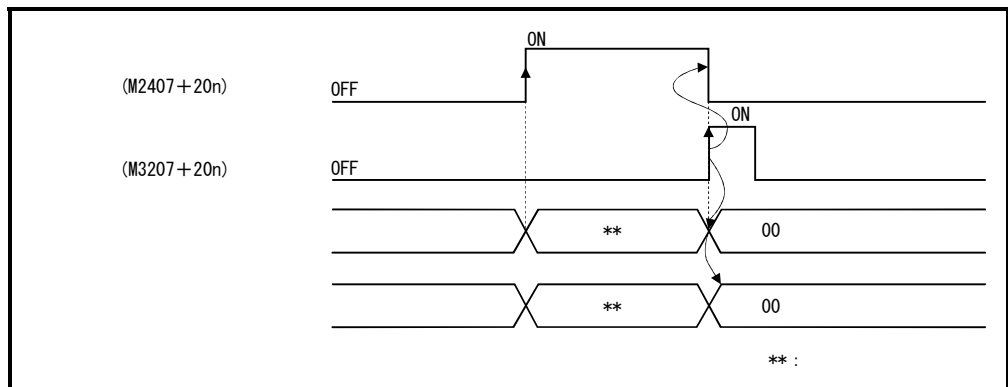
(a) CHANGE ()

• ON CHANGE 가 ON →

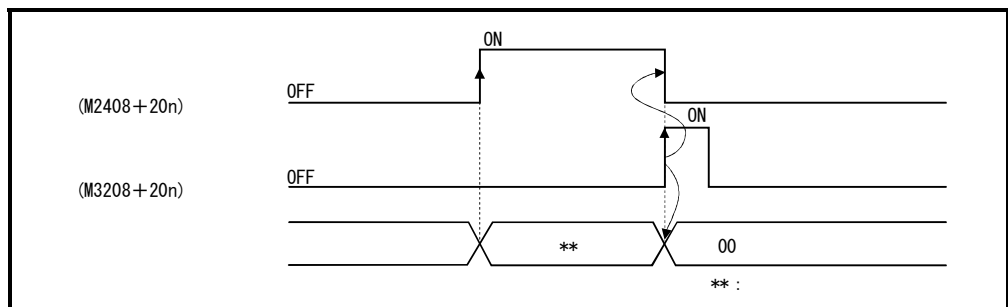
• OFF CHANGE 가 ON →



(6) (M3207+20n)
(M2407+20n : ON)
(M2407+20n)



(7) (M3208+20n)
(M2408+20n : ON)
(M2408+20n)



(8) STOP (M3209+20n)

 STOP /

 • ON STOP STOP ON

 • OFF STOP STOP ON

M3209+20n	ON		STOP			STOP		OFF→ON	
(STOP	ON	ON→OFF→ON)						

(9) (M3212+20n)

 • , /

 • ON

 • OFF

M3212+20n	ON				가		ON		
		OFF							

(10) OFF (M3215+20n)

 OFF()

 • M3215+20n : OFF ON

 • M3215+20n : ON OFF ()

●					OFF				

(11) FIN (M3219+20n)

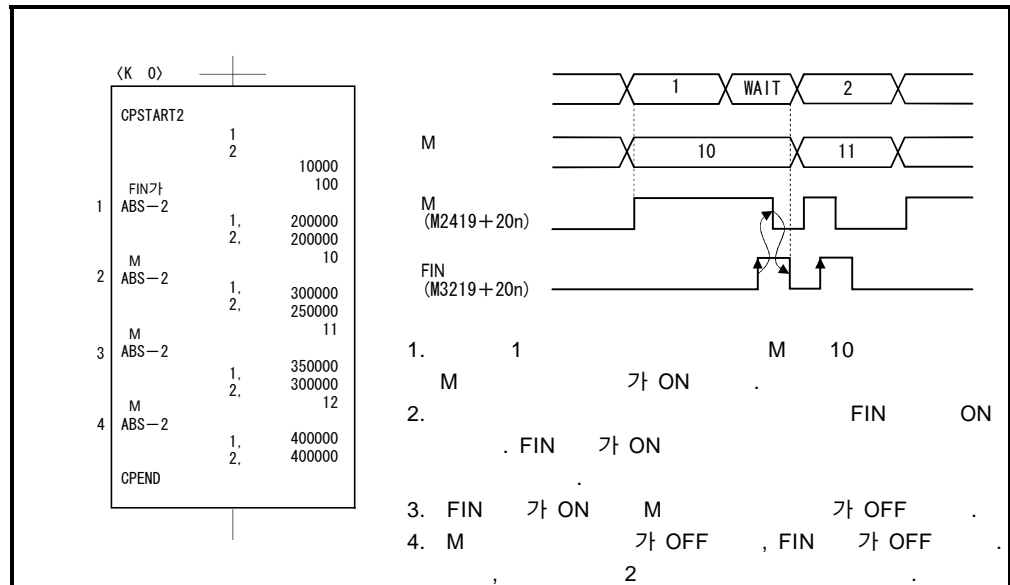
M

FIN 가 OFF→ON→OFF

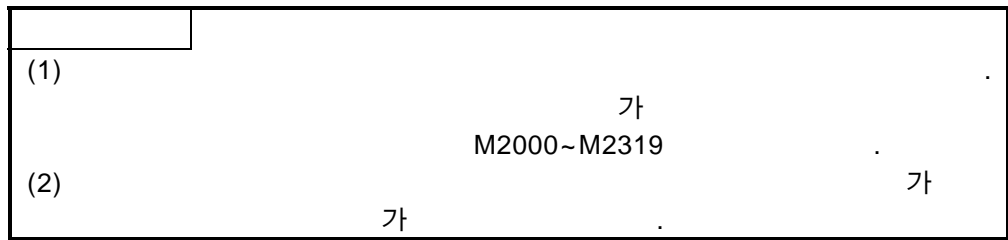
. FIN OFF→ON→ OFF

FIN가

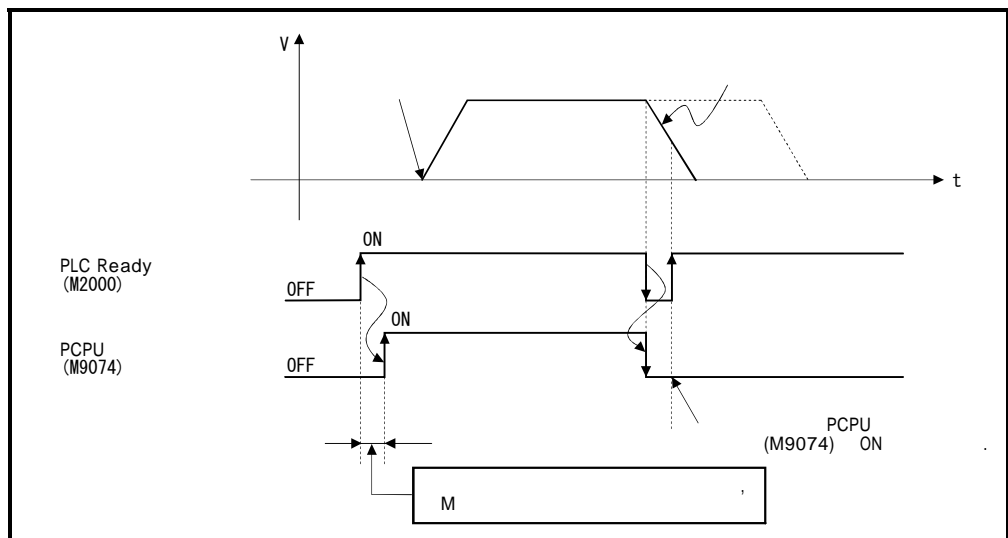
FIN



3. 1. 3



- (1) PLC Ready (M2000).....
- (a) PLC CPU가 CPU
M2000 ON , SFC
 , JOG ,
(M9075 ON) M2000 ON
- (b) , ,
M2000 OFF
M2000 ON ,
- (c) M2000 OFF→ON
- - N
 - 300[%]
 - (4.4)
 - PCPU (M9074) ON . (SFC
 - 가 .)
 - SFC
 - 가 (c)
 - (c)
 - , M2000 ON (c)



(d) M2000 ON→OFF

- PCPU (M9074) OFF
- SFC
- PY가 OFF

(e) STOP RUN

PLC Ready (M2000)가 ON

(STOP RUN) M2000 ON ()
M2000 OFF ON
• RUN/STOP STOP RUN
• RUN/STOP RUN

M2000 ON OFF
• RUN/STOP RUN STOP

(STOP RUN)+ 1 M2000 ON
(RUN 1 M2000 ON)
M2000 OFF ON
• RUN/STOP RUN PLC Ready
D704 1 (CPU D704
0 1)

M2000 ON OFF
• RUN/STOP RUN PLC Ready
D704 0 (CPU D704
1 0)
• RUN/STOP RUN STOP

(2) (M2001~M2032).....

(a) SFC

ON
가 ON .

(b) ON/OFF

SFC

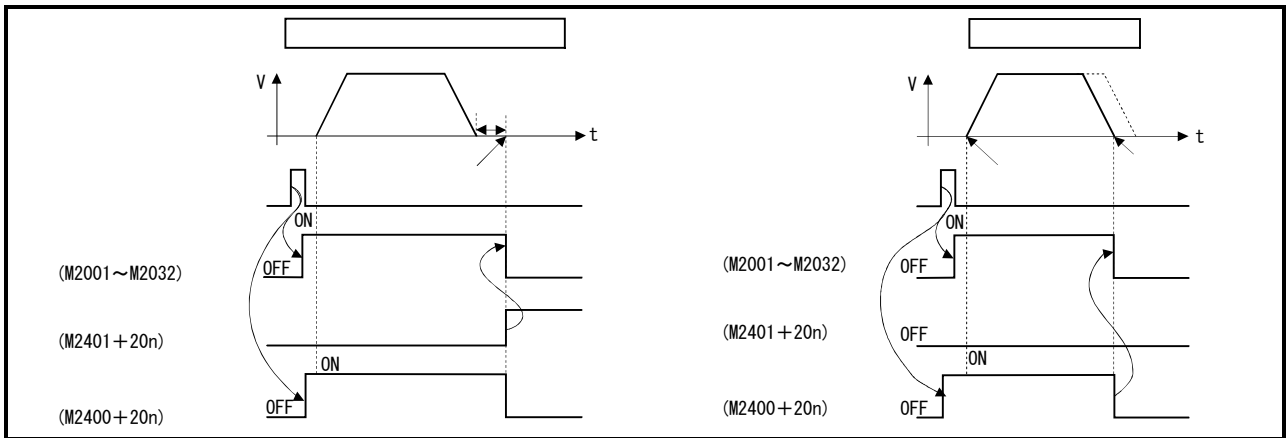
가 ON ,

OFF .

OFF .

(0

ON .)



JOG

(M3202+20n

M3203+20n) ON

ON , JOG

OFF

OFF .

가(M2051~M2053 : ON) , ON .

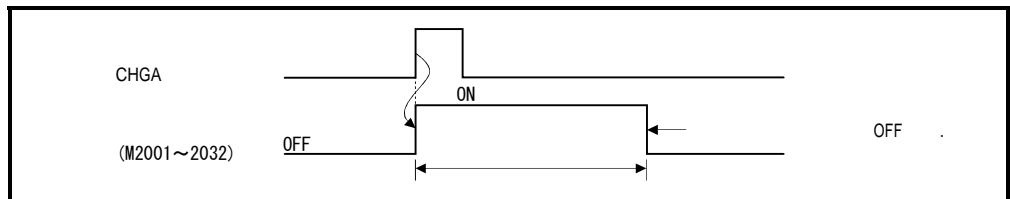
가(M2051~M2053 : OFF)

OFF .

CHGA

ON .

OFF .



ON/OFF

ON

SFC

OFF ,

가

가

OFF

SFC

ON ,

가

"

ON

"가

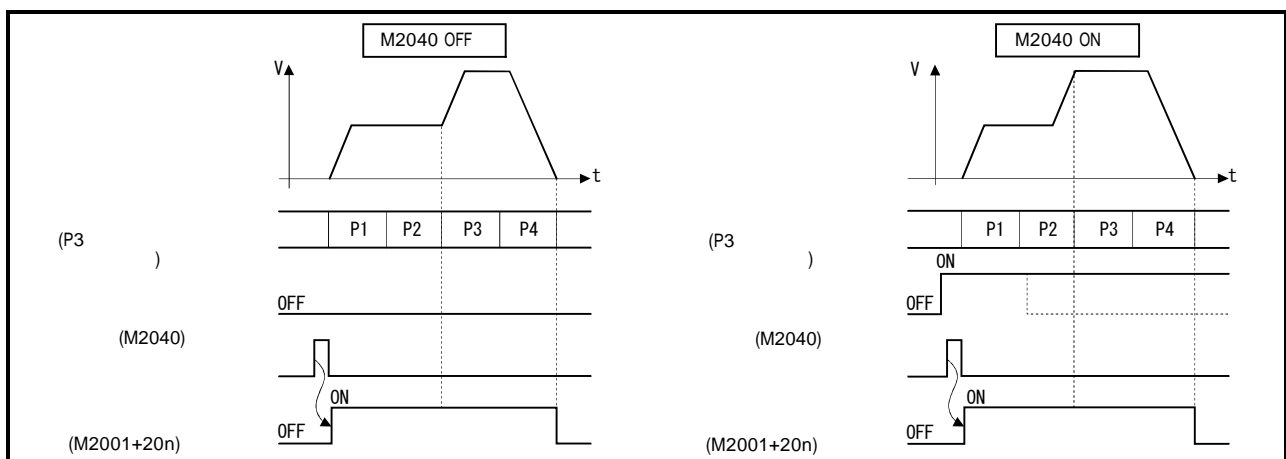
(3) PC (M2034)
 PC 가 ON .
 • ON : PC
 • OFF : PC
 (OFF .)
 PC 1.5 .

(4) SFC (M2039)
 SFC , ON .
 OFF , OFF .

(5) (M2040)

(a) M2040 () ON

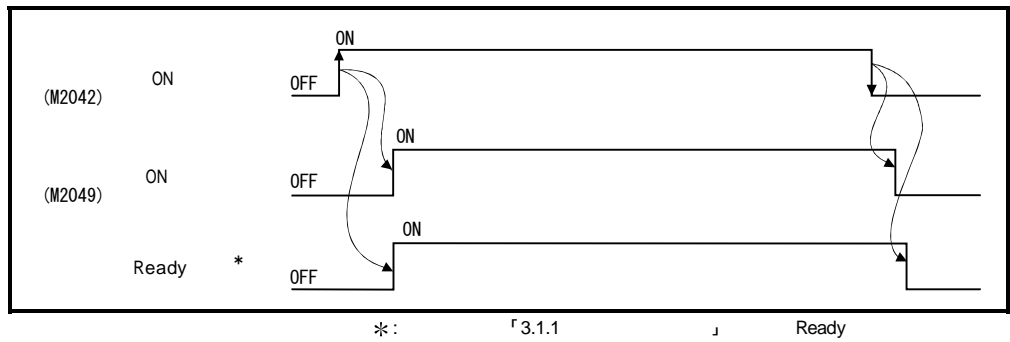
- OFF
- ON



- (6) (M2041)
CPU , " "
- (a) 가 CPU ERR.LED가 , GSV13P/GSV22P
- (b) M0241 ON , 가
CPU



- CPU
- (7) ON (M2042)
가
- (a) 가 OFF (M3215+20n)가 OFF,
M2042 ON
- (b) 가
• M2042가 OFF
• OFF (M3215+20n)가 ON



M2042 ON	CPU STOP	M2042 OFF
----------	----------	-----------

(8) (M2047)

/

- ON
- OFF

(a) (, OFF) SFC

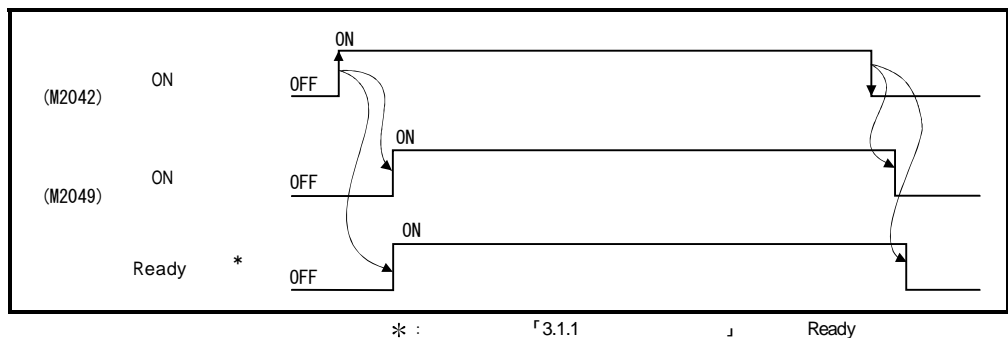
(9) JOG (M2048)

(a) M2048 ON JOG (D710~D713)
JOG JOG

(b) M2048 OFF JOG

(10) ON (M2049)

CPU가 ON (M2042) ON
ON/OFF ON/OFF Ready
(M2415+20n)



(11) 가 (M2051~M2053)

Q173PX P1~P3*

가/

- 가
- ON
- OFF

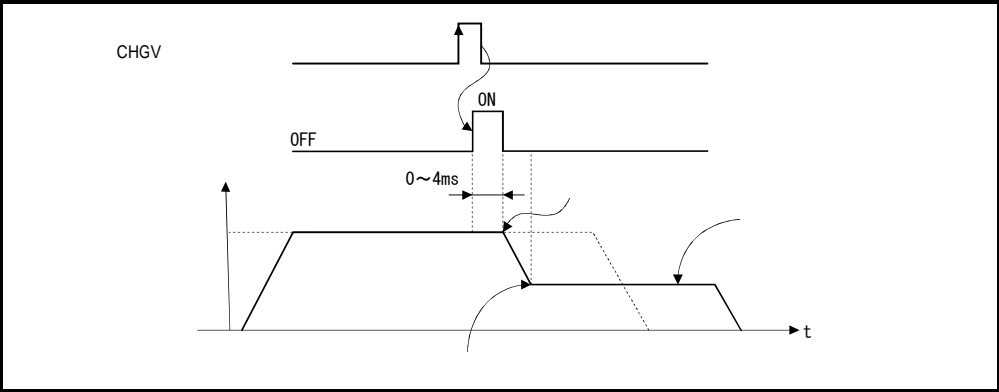


* : Q173PX P1~P3() 「Q173CPU/Q172CPU」

(12) (M2054) (D9197) ON
OFF
• CPU ON OFF
• CPU
•
【 】

SFC , NMI

(13) (M2061~M2092) ON
SFC (CHGV)



No.	No.	No.	No.	No.	No.	No.	No.
1	M2061	9	M2069	17	M2077	25	M2085
2	M2062	10	M2070	18	M2078	26	M2086
3	M2063	11	M2071	19	M2079	27	M2087
4	M2064	12	M2072	20	M2080	28	M2088
5	M2065	13	M2073	21	M2081	29	M2089
6	M2066	14	M2074	22	M2082	30	M2090
7	M2067	15	M2075	23	M2083	31	M2091
8	M2068	16	M2076	24	M2084	32	M2092

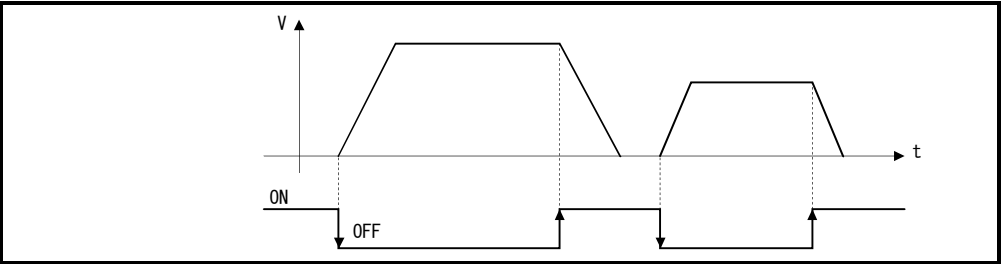
* : Q172CPU(N) No.1~ No.8 가



SV22 가 가

(14) (M2128~M2159)..... 가 ON

- (a) , ON , ,
가 OFF . ,
- (b) OFF .
- (c) ON .
- JOG OFF
 -
 -
 - =0



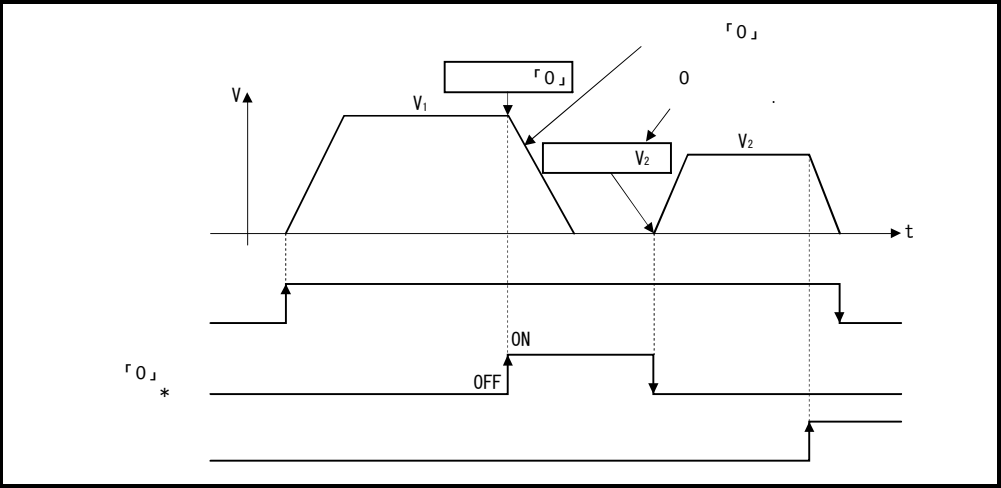
No.	No.	No.	No.	No.	No.	No.	No.
1	M2128	9	M2136	17	M2144	25	M2152
2	M2129	10	M2137	18	M2145	26	M2153
3	M2130	11	M2138	19	M2146	27	M2154
4	M2131	12	M2139	20	M2147	28	M2155
5	M2132	13	M2140	21	M2148	29	M2156
6	M2133	14	M2141	22	M2149	30	M2157
7	M2134	15	M2142	23	M2150	31	M2158
8	M2135	16	M2143	24	M2151	32	M2159

* : Q172CPU(N) No.1~ No.8 가



SV22 가 가

(15) 「0」 (M2240~M2271) , ON
「0」
ON
OFF



「0」

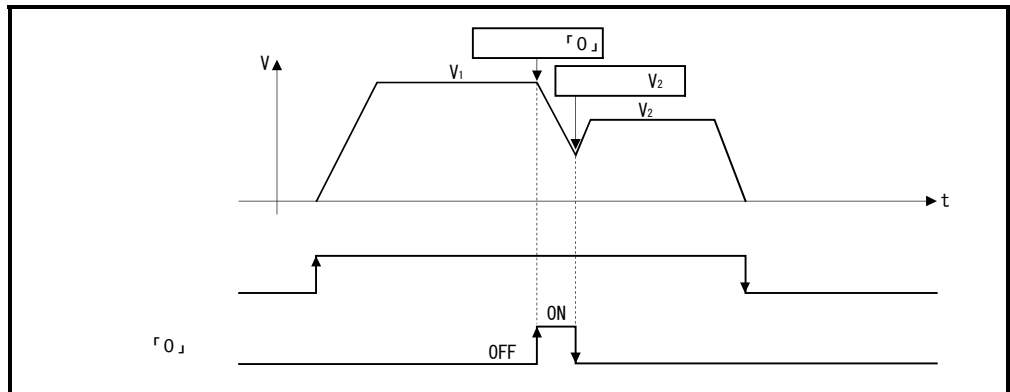
No.	No.	No.	No.	No.	No.	No.	No.
1	M2240	9	M2248	17	M2256	25	M2264
2	M2241	10	M2249	18	M2257	26	M2265
3	M2242	11	M2250	19	M2258	27	M2266
4	M2243	12	M2251	20	M2259	28	M2267
5	M2244	13	M2252	21	M2260	29	M2268
6	M2245	14	M2253	22	M2261	30	M2269
7	M2246	15	M2254	23	M2262	31	M2270
8	M2247	16	M2255	24	M2263	32	M2271

* : Q172CPU(N) No.1~ No.8 가

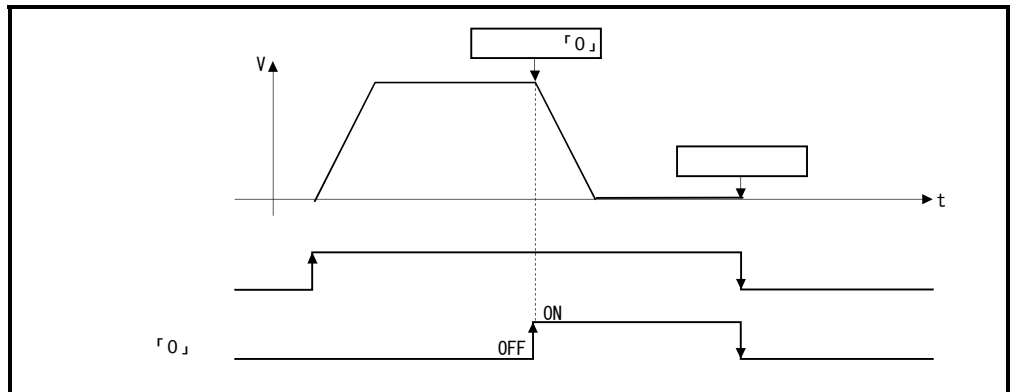


- (1) (M2001~M2032)가 ON 「0」
- (2) 가
- (3) 「0」 가
• JOG OFF
•
•
•
- (4) SV22 가

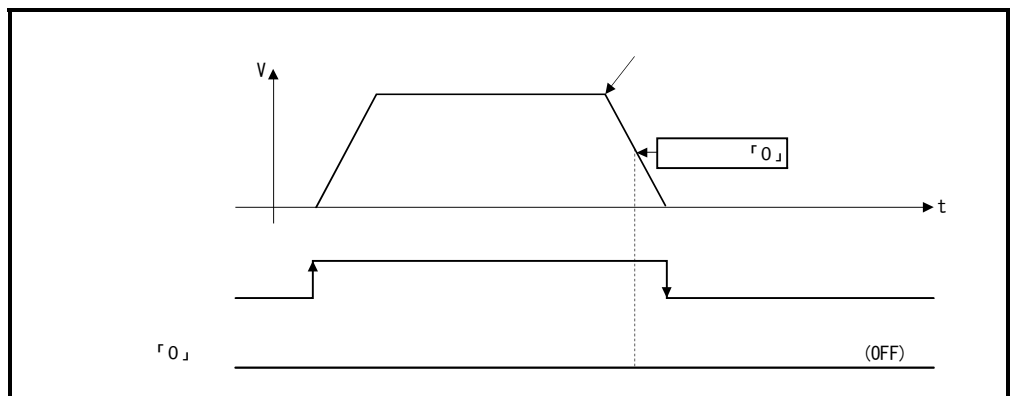
(a) 「0」 가
OFF .

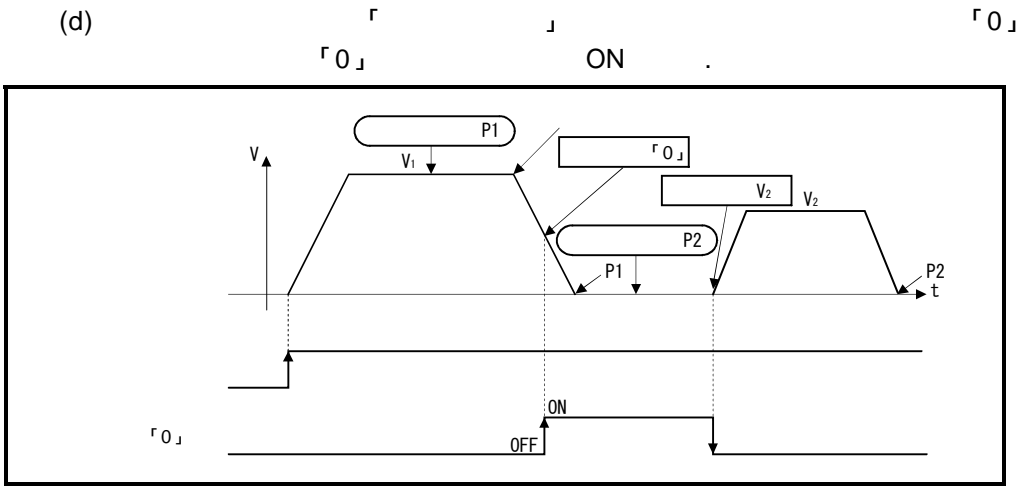


(b) 「0」 , OFF .



(c) ON 「0」 , 「0」
ON .





「 0 」 「 0 」

3.

3.2

(1)

SV13		SV22	
D0		D0	
↳	(20 ×32)	↳	(20 ×32) .. 가 ..
D640		D640	
↳	(2 ×32)	↳	(2 ×32)
D704		D704	
↳	(54) ()	↳	(54) ()
D758		D758	
↳	(42) ()	↳	(42) ()
D800		D800	가 *1
		↳	(10 ×32) ()
		D1120	
		↳	(10 ×12)
		D1240	*1
		↳	(10 ×32)
	(7392)	D1560	
		↳	(6632)
D8191		D8191	

*1 : SV22

가 .

7392 (SV13), 6632 (SV22)

(2)

No.																																																																																																			
1	D0～D19	<table><tr><td></td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>0</td><td rowspan="7"></td><td rowspan="7"></td><td rowspan="7"></td><td rowspan="7">PLS</td><td rowspan="7"></td></tr><tr><td>1</td></tr><tr><td>2</td></tr><tr><td>3</td></tr><tr><td>4</td></tr><tr><td>5</td></tr><tr><td>6</td></tr><tr><td>7</td></tr><tr><td>8</td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3">—</td><td rowspan="3"></td></tr><tr><td>9</td></tr><tr><td>10</td></tr><tr><td>11</td><td>ON</td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td></tr><tr><td>12</td><td>No.</td></tr><tr><td>13</td><td>M</td></tr><tr><td>14</td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3">.</td><td rowspan="3">%</td><td rowspan="3"></td></tr><tr><td>15</td></tr><tr><td>16</td></tr><tr><td>17</td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3">—</td><td rowspan="3"></td></tr><tr><td>18</td></tr><tr><td>19</td></tr><tr><td>20</td><td rowspan="3">STOP</td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td></tr><tr><td>21</td></tr><tr><td>22</td></tr><tr><td>23</td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td></tr><tr><td>24</td></tr><tr><td>25</td></tr><tr><td>26</td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td></tr><tr><td>27</td></tr><tr><td>28</td></tr><tr><td>29</td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td></tr><tr><td>30</td></tr><tr><td>31</td></tr><tr><td>32</td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td><td rowspan="3"></td></tr><tr><td></td></tr><tr><td></td></tr></table>											0				PLS		1	2	3	4	5	6	7	8				—		9	10	11	ON					12	No.	13	M	14			.	%		15	16	17				—		18	19	20	STOP					21	22	23						24	25	26						27	28	29						30	31	32							
0									PLS																																																																																										
1																																																																																																			
2																																																																																																			
3																																																																																																			
4																																																																																																			
5																																																																																																			
6																																																																																																			
7																																																																																																			
8									—																																																																																										
9																																																																																																			
10																																																																																																			
11	ON																																																																																																		
12	No.																																																																																																		
13	M																																																																																																		
14								.	%																																																																																										
15																																																																																																			
16																																																																																																			
17				—																																																																																															
18																																																																																																			
19																																																																																																			
20	STOP																																																																																																		
21																																																																																																			
22																																																																																																			
23																																																																																																			
24																																																																																																			
25																																																																																																			
26																																																																																																			
27																																																																																																			
28																																																																																																			
29																																																																																																			
30																																																																																																			
31																																																																																																			
32																																																																																																			
2	D20～D39																																																																																																		
3	D40～D59																																																																																																		
4	D60～D79																																																																																																		
5	D80～D99																																																																																																		
6	D100～D119																																																																																																		
7	D120～D139																																																																																																		
8	D140～D159																																																																																																		
9	D160～D179																																																																																																		
10	D180～D199																																																																																																		
11	D200～D219																																																																																																		
12	D220～D239																																																																																																		
13	D240～D259																																																																																																		
14	D260～D279																																																																																																		
15	D280～D299																																																																																																		
16	D300～D319																																																																																																		
17	D320～D339																																																																																																		
18	D340～D359																																																																																																		
19	D360～D379																																																																																																		
20	D380～D399																																																																																																		
21	D400～D419																																																																																																		
22	D420～D439																																																																																																		
23	D440～D459																																																																																																		
24	D460～D479																																																																																																		
25	D480～D499																																																																																																		
26	D500～D519																																																																																																		
27	D520～D539																																																																																																		
28	D540～D559																																																																																																		
29	D560～D579																																																																																																		
30	D580～D599																																																																																																		
31	D600～D619																																																																																																		
32	D620～D639																																																																																																		

* 1 : SV13/SV22

* 2 : Q172CPU(N)

* 3 : Q172CPU(N)

No.1~ No.8

9

가 .
가 .
가 .

3.

(3)

No.						
1	D640, D641	JOG				
2	D642, D643					
3	D644, D645					
4	D646, D647					
5	D648, D649					
6	D650, D651					
7	D652, D653					
8	D654, D655					
9	D656, D657					
10	D658, D659					
11	D660, D661					
12	D662, D663					
13	D664, D665					
14	D666, D667					
15	D668, D669					
16	D670, D671					
17	D672, D673					
18	D674, D675					
19	D676, D677					
20	D678, D679					
21	D680, D681					
22	D682, D683					
23	D684, D685					
24	D686, D687					
25	D688, D689					
26	D690, D691					
27	D692, D693					
28	D694, D695					
29	D696, D697					
30	D698, D699					
31	D700, D701					
32	D702, D703					

* 1 : SV13/SV22

가 .

* 2 : Q172CPU(N)

No.1~ No.8

가 .

* 3 : Q172CPU(N) 9

가 .

(4)

D704	PLC Ready			
D705				
D706	ON			
D707	/가 (SV22)			
D708	JOG			
D709	가	—	—	—
D710	JOG			
D711				
D712				
D713				
D714	1			
D715	No.			
D716	2			
D717	No.			
D718	3			
D719	No.			
D720	1			
D721	2			
D722	3			
D723	4			
D724	5			
D725	6			
D726	7			
D727	8			
D728	9			
D729	10			
D730	11			
D731	12			
D732	13			
D733	14			
D734	15			
D735	16			
D736	17			
D737	18			
D738	19			
D739	20			
D740	21			
D741	22			
D742	23			
D743	24			
D744	25			
D745	26			
D746	27			
D747	28			
D748	29			
D749	30			
D750	31			
D751	32			

D752	1		가				
D753	2						
D754	3						
D755	1 가						
D756	2 가						
D757	3 가						
D758	가				—	—	—
D759	PCPU						
D760							
D761							
D762							
D763							
D764							
D765							
D766							
D767							
D768							
D769							
D770							
D771							
D772							
D773							
D774							
D775							
D776							
D777							
D778							
D779							
D780							
D781							
D782							
D783							
D784							
D785							
D786							
D787							
D788							
D789							
D790							
D791							
D792							
D793							
D794							
D795							
D796							
D797							
D798							
D799							

* 1 : SV13/SV22			가	.
* 2 : Q172CPU(N)	No.1 ~	No.8	가	.
* 3 : Q172CPU(N)	9			가 .

3. 2. 1

- , CPU가 , ,
 SFC
 가 ()
 (, ,)가 ON/OFF ,
 「 4 」
- (1) (D0+20n, D1+20n)
 (a) / ,
 0
 (M3212+20n)
 ON/OFF
 • M3212+20n : OFF... 0
 • M3212+20n : ON.....
 0
- (b)
- (2) (D2+20n, D3+20n)
 (a)
 (b) "() = ()"
- (3) (D4+20n, D5+20n)
 가
- (4) (D6+20n)
 (a) (1.2)가
 가
- (b) (M3207+20n)
- (5) (D7+20n)
 (a) (1.3)가
 가
- (b) (M3207+20n)

- (6) (D8+20n)
 (a) (1.4)가 .
 가
 .
 (b) (M3208+20n) .
- (7) (D9+20n)
 ON (6.22.1)
 가 , CPU ()
 . ,
 (, .)
 가 131072[PLS] ,
 10 .
- (8) ON (D10+20n, D11+20n).....
 (a) , ON ()
 .
 (b) . () .
- (9) No. (D12+20n)
 (a) No. .
 (b) .
 ① JOG FFFF
 ② FFFF
 ③ FF00
 (c) FFFD가 .
 .
- (10) M (D13+20n)
 (a) M *가
 .
 M 가 "0" .
 (b) .
 (c) PLC Ready(M2000) "0" .
 * : M M .
 • M7.1
 • M 3.1

(11) (D14+20n)

PLC Ready(M2000) 300[%]가

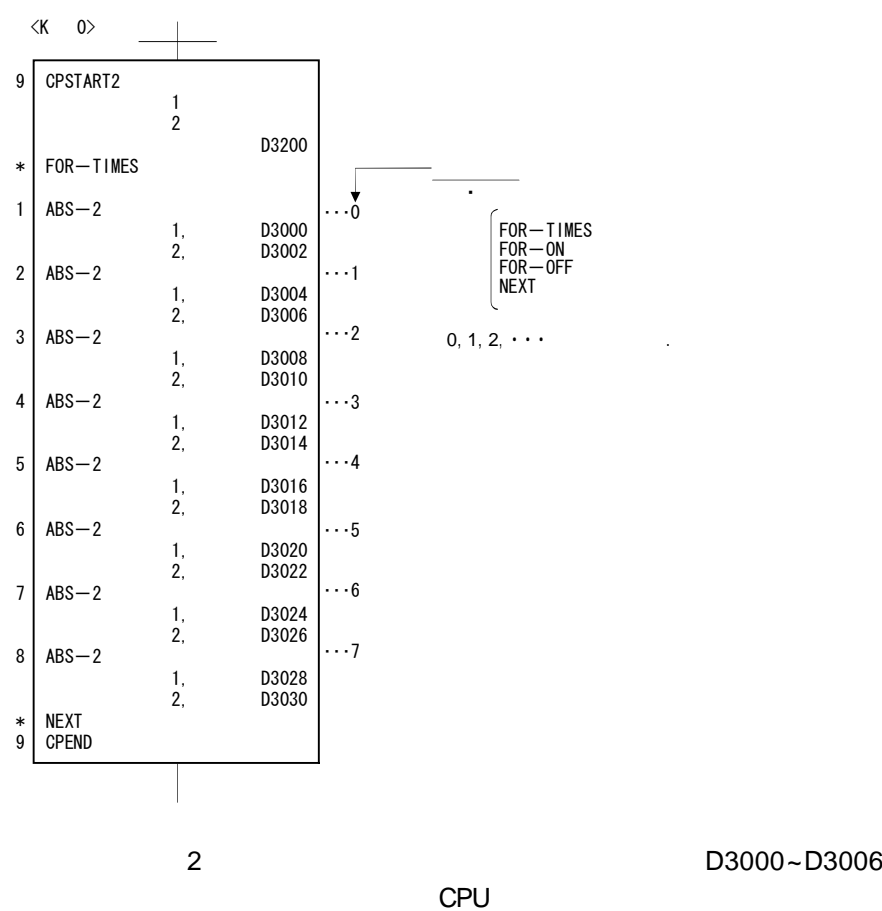
<ul style="list-style-type: none"> • • • SFC (CHGT) • PLC CPU (S(P).CHGT)

(12) (D15+20n)

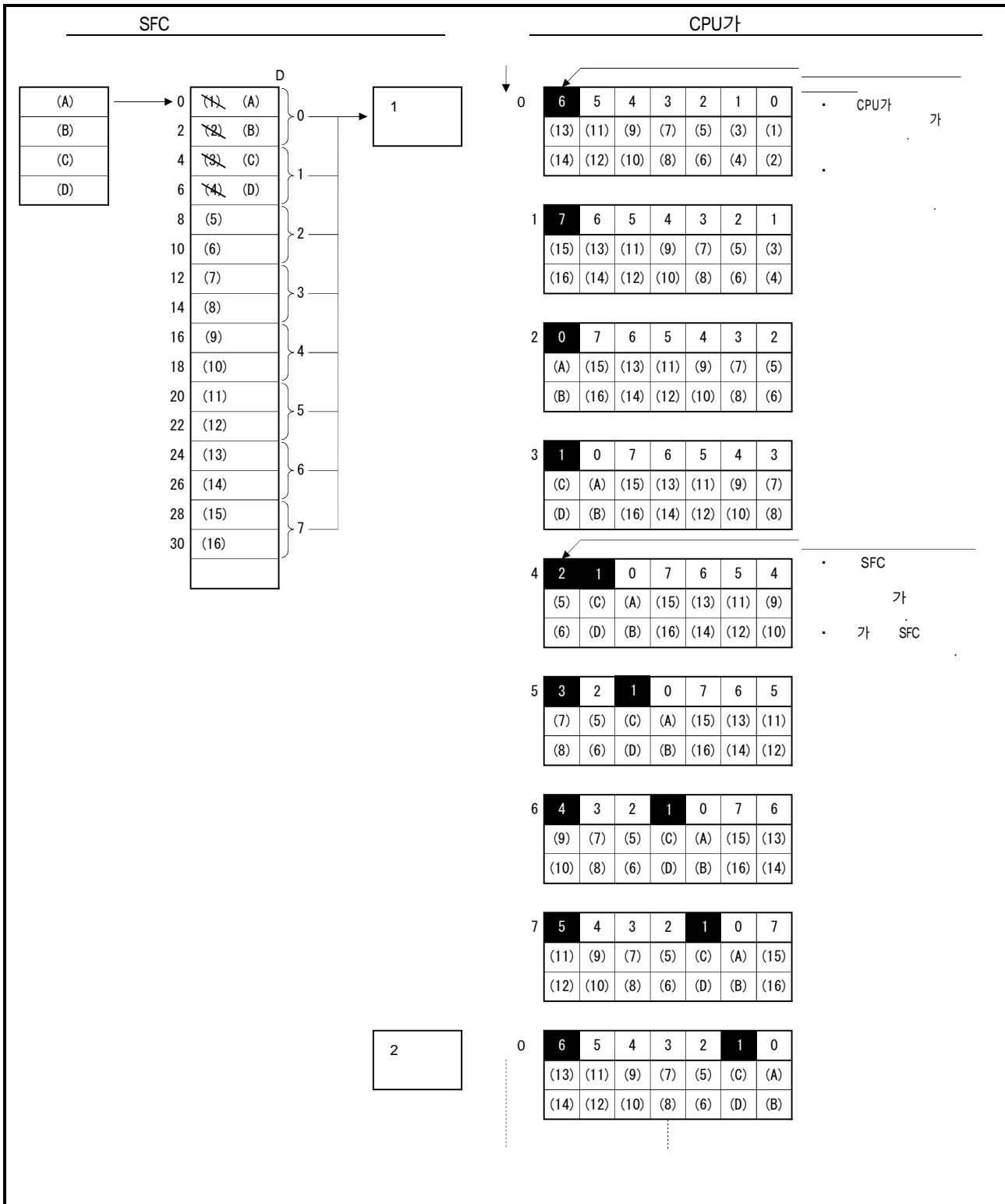
(FOR - TIMES, FOR - OFF)

가 CPU가
SFC

(가 SFC)
가



[CPU]



【 】

- (a) CPU 0~6 ((1)~(14))
 , 가 "6"
 "6" 0~6
 가
- (b) SFC 0~1 ((A)~(D))
 (가 SFC 가
 .) 가 "1"
 2~6 ((5)~(14)) 가
- (c) 0
 1 "7"
 , CPU 0 ((1)~(2)) , 7
 ((15)~(16))
- (d) ,
 가
 CPU 3
 SFC , CPU가 D8, D10
 , 2
 CPU
 가 가 SFC

· 가 8	·
· 가 8	,
SFC	
가	CPU가
·	
CPU가	가

(13) (D16+20n, D17+20n)

· (6.14)

(14) STOP (D18+20n, D19+20n).....

Q172LX

(STOP)

3.

3.2.2

JOG

3.1

	1	2	3	4	5	6	7	8
JOG	D641, D640	D643, D642	D645, D644	D647, D646	D649, D648	D651, D650	D653, D652	D655, D654
	9	10	11	12	13	14	15	16
	D657, D656	D659, D658	D661, D660	D663, D662	D665, D664	D667, D666	D669, D668	D671, D670
	17	18	19	20	21	22	23	24
	D673, D672	D675, D674	D677, D676	D679, D678	D681, D680	D683, D682	D685, D684	D687, D686
	25	26	27	28	29	30	31	32
	D689, D688	D691, D690	D693, D692	D695, D694	D697, D696	D699, D698	D701, D700	D703, D702

* : Q172CPU(N) 1~ 8 가

(1) JOG (D640+2n)

(a) JOG JOG

(b) JOG

	mm		inch		degree		PLS	
JOG	1~600000000	$\times 10^{-2}$ [mm/min]	1~600000000	$\times 10^{-3}$ [inch/min]	1~2147483647	$\times 10^{-3}$ [degree/min]	1~10000000	[PLS/s]

(c) JOG (OFF→ ON) JOG

JOG

JOG

(d) JOG

6.20

3.

3. 2. 3

(1) SET/RST (D704~D708, D755~D757)

PLC CPU ON/OFF D

가 0 1 가 ON . OFF

1 0 .

. (M2000~M2053 「 3.1.3

」 .)

No.			
1	PLC Ready	M2000	D704
2		M2040	D705
3	ON	M2042	D706
4	/가 (SV22)	M2043	D707
5	JOG	M2048	D708
6	1 가	M2051	D755
7	2 가	M2052	D756
8	3 가	M2053	D757

(2) JOG (D710~D711)

(a) JOG No.

	b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0	
D710	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	JOG
D711	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	
D712	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	JOG
D713	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	
* JOG 1/0 1 : 0 : * Q172CPU(N) 1~ 8 가																	

(b) JOG 6. 20. 3

(3) No. (D714~D719).....

(a) No. .

		b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
P1	D714	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
	D715	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17
P2	D716	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
	D717	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17
P3	D718	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
	D719	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17

* 1 : 1/0 .
0 :
* Q172CPU(N) 1~ 8 가 .

(b) 6.21 .

(4) 1 (D720~D751)

(a) 1 (1~10000)

1	No.		1	No.	
D720	1	1~10000*2	D736	17	1~10000*2
D721	2		D737	18	
D722	3		D738	19	
D723	4		D739	20	
D724	5		D740	21	
D725	6		D741	22	
D726	7		D742	23	
D727	8		D743	24	
D728	9		D744	25	
D729	10		D745	26	
D730	11		D746	27	
D731	12		D747	28	
D732	13		D748	29	
D733	14		D749	30	
D734	15		D750	31	
D735	16		D751	32	

* 1 : Q172CPU(N) 1~ 8 가 .

* 2 : SW6RN-SV13Q /22Q (Ver.00B) (1~100)가 .

(b) 6.21 .

(5) (D752~D754)

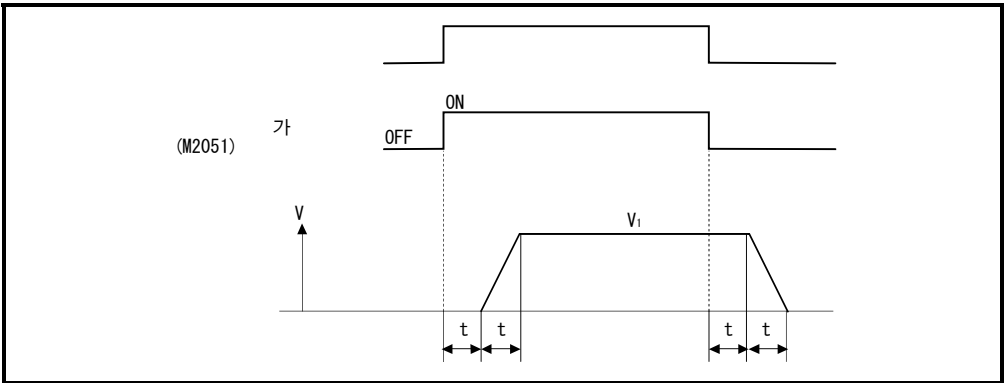
(a)

1(P1) : D752	0~59
2(P2) : D753	
3(P3) : D754	

(b)

(t) = (+1) X 56.8 [ms]

(c)



(V1) = (/ms) X (1)

(L) = $\left[\begin{matrix} 1 \\ 1 \end{matrix} \right] \times \quad \times \left[\begin{matrix} 1 \end{matrix} \right]$



(1)

- 1
- mm : 0.1[μm]
- inch : 0.00001[inch]
- degree : 0.00001[degree]
- PLS : 1[PLS]

(2)

56.8[ms]~3408[ms]

(6) (D792~D799)
CPU ON/

	b15 ~ b12	b11 ~ b8	b7 ~ b4	b3 ~ b0
D792	4	3	2	1
D793	8	7	6	5
D794	12	11	10	9
D795	16	15	14	13
D796	20	19	18	17
D797	24	23	22	21
D798	28	27	26	25
D799	32	31	30	29

→

• 0.....

• 2.....

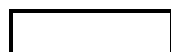
3.

3.3

CPU (#0~#8191)가 , #8000~#8063
SFC , #8064~#8191
SFC 「Q173CPU/Q172CPU
(SV13/SV22) (SFC)」

(1) (#8064~#8191)
「 , 「 , 「 가

No.					
1	#8064~#8067				
2	#8068~#8071	+0	*1		
3	#8072~#8075				
4	#8076~#8079		1 : MR-H-BN 5 : MR-J2-M		
5	#8080~#8083		2 : MR-J-B 6 : MR-J2-03B5		
6	#8084~#8087	+1	3 : MR-J2-B 65 : FR-V500		
7	#8088~#8091		4 : MR-J2S-B		
8	#8092~#8095		-5000~5000 (×0.1[%])	3.55ms	
9	#8096~#8099				
10	#8100~#8103	+2	-50000~50000 (×0.1[r/min])		
11	#8104~#8107	+3			
12	#8108~#8111	*1 : +0, +1 .			
13	#8112~#8115				
14	#8116~#8119				
15	#8120~#8123				
16	#8124~#8127				
17	#8128~#8131				
18	#8132~#8135				
19	#8136~#8139				
20	#8140~#8143				
21	#8144~#8147				
22	#8148~#8151				
23	#8152~#8155				
24	#8156~#8159				
25	#8160~#8163				
26	#8164~#8167				
27	#8168~#8171				
28	#8172~#8175				
29	#8176~#8179				
30	#8180~#8183				
31	#8184~#8187				
32	#8188~#8191				



* : (#8064~#8191) SW6RN-SV13Q /22Q (Ver.00D)

3.

3. 4 (SP. M)

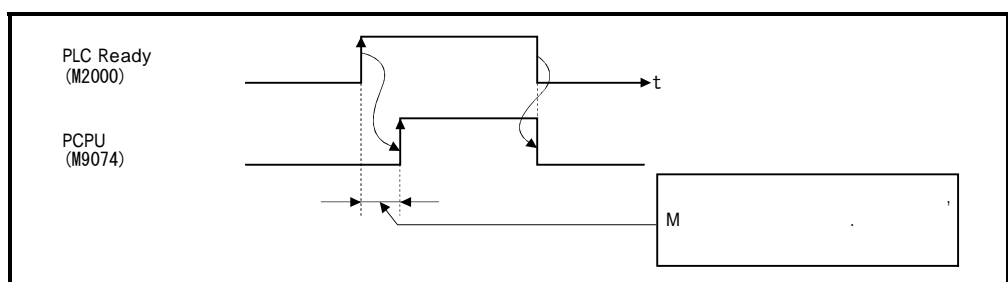
CPU M9000~M9255 256 가 , M9073~
M9079 7 , 3.2 가
. (M9073~M9079 2.1
.)

3.2

M9073	PCPU WDT		
M9074	PCPU		
M9075			
M9076			
M9077			
M9078			
M9079			

(1) PCPU WDT (M9073)
CPU "WDT " ON
CPU WDT
CPU WDT 가 ON CPU
M9073 ON CPU (D9184) (3.5)
CPU WDT

(2) PCPU (M9074)
CPU /
(a) PLC Ready (M2000) (OFF → ON) , ON
M
(b) PLC Ready (M2000)가 OFF OFF



(3) (M9075)

(a) 가

SFC

• OFF.....

• ON

(b)

(M9078) ON

(4) (M9076).....

ON/OFF

• OFF..... ON

• ON OFF

(1)	가	ON
(M2042)	OFF	OFF
(2)		

(5) (M9077).....

(a) (D714~D719) /

• OFF..... D714~D719가

• ON D714~D719가

(b) M9077 ON
(D9185~D9187)

(6) (M9078)

(a)

ON

(b) M9078 ON (D9182,
D9183)

(7) (M9079)

/

• OFF.....

• ON

3.

3. 5 (SP. D)

CPU , D9000~D9255 256 가 .
 , D9180~D9199 20 , .
 (D9180~D9201 , 2.2 .)

3. 3

D9180	가	—	—	—
D9181				
D9182				
D9183				
D9184	CPU WDT	CPU WDT		
D9185		가		
D9186				
D9187				
D9188				
D9189	No.			
D9190				
D9191		ON		
D9192				
D9193	/가	가		
D9194				
D9195				
D9196	PC			
D9197		ON		
D9198	가	—	—	—
D9199				
D9200				
D9201	LED			

(1) (D9182~D9183).....

가 , (M9078)가 ON ,
 / 가 .

	b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
D9182	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
D9183	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17

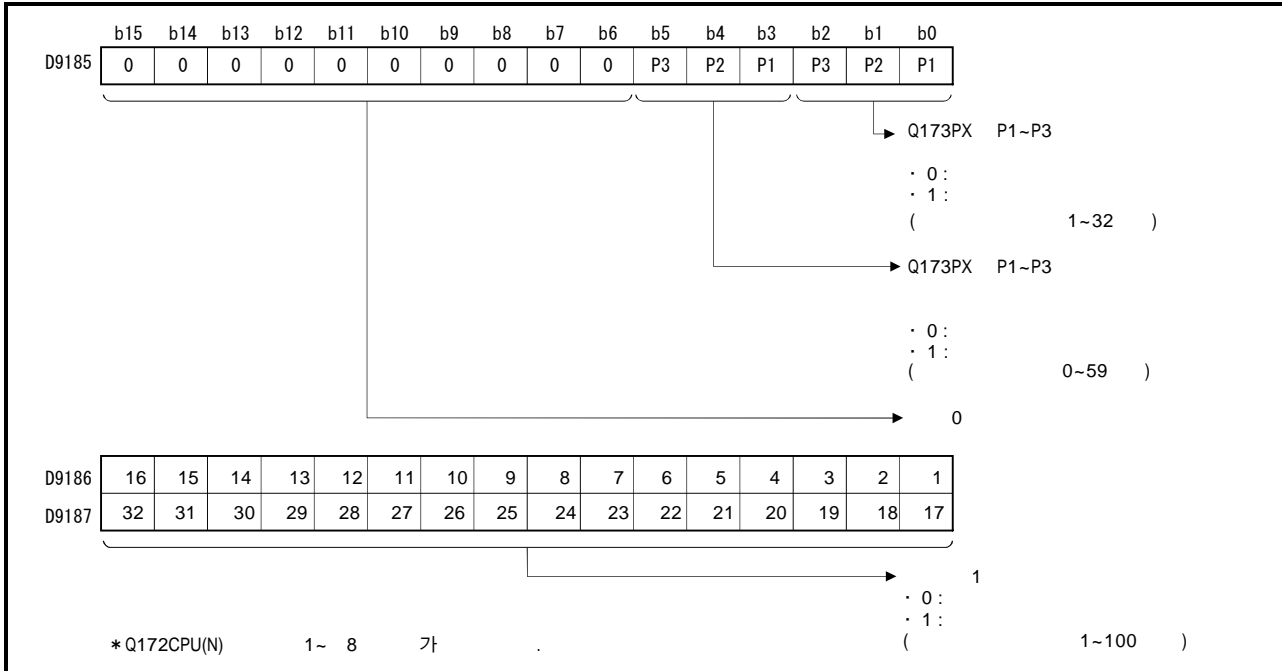
* Q172CPU(N) 1~ 8 가 .

· 0 :
 · 1 :

(2) CPU WDT (D9184).....
CPU

1	S/W 1	가가	• • • • • • • • • •
2			• • • • • • • • • •
3	Q WDT		• • • • • • • • • •
4	WDT		• • • • • • • • • •
30	H/W		• • • • • • • • • •
201~215	Q H/W 201 01 : Q 1 02 : Q 2 04 : Q 4 08 : Q 8 +200		• • • • • • • • • •
250~253	I/F H/W 250 SSCNET No. 0 : SSCNET 1 1 : SSCNET 2 2 : SSCNET 3 3 : SSCNET 4 SSCNET No. +250		• • • • • • • • • •
300	S/W 3		• • • • • • • • • •
301	8 CPSTART 가 가 14		• • • • • • • • • •
302	ROM , FLASH ROM 가		• • • • • • • • • •

- (3) (D9185~D9187).....
가 , 가 ,
D9185~D9187 , (M9077)
가 ON .



- (4) (D9188).....
[μs] .

- (5) No. (D9189).....
(a) 가 ,
(M9079)가 ON , 가 No.(0~4095)
가 .
(b) No.가 , 가
, No.가 .

- (6) (D9190).....
가 ,
(M9079)가 ON , 가 가
. , 2. 2 .

(7) (D9191~D9192).....
CPU ON/ , 가
→ , 가 . , →
 , .

	b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
D9191	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
D9192	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17

.....1

....0

* Q172CPU(N) 1~ 8 가 .

(a) /
..... 가 ()
..... 가)
OFF .
 ,
 ,

(No.)	1	0
	0	0

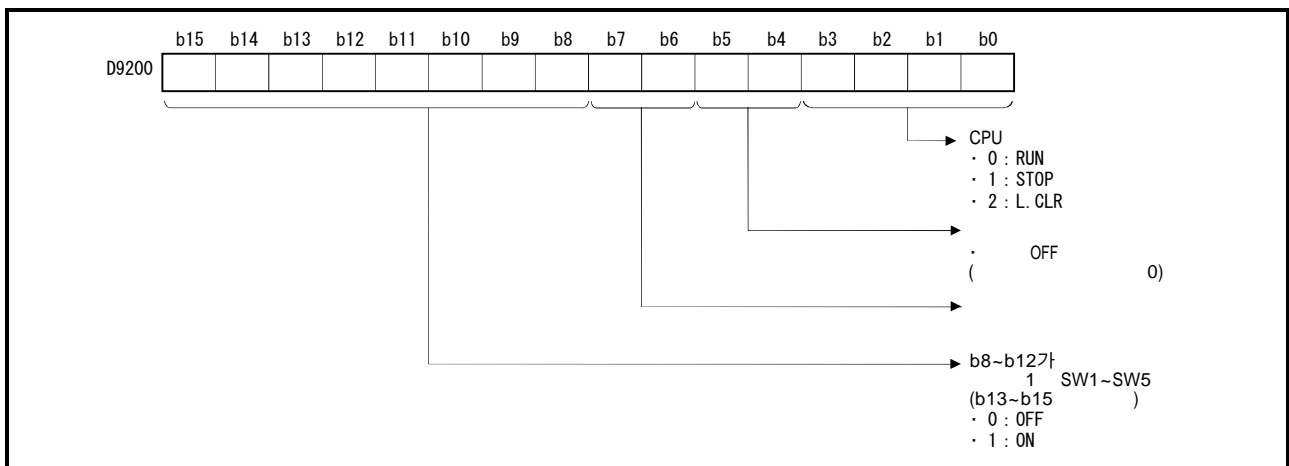
(8) PC (D9196).....
PC 가 , 가 .

PC	
D9196	00 : 01 : 02 : CRC 03 : 04 : 05 : (00 .)

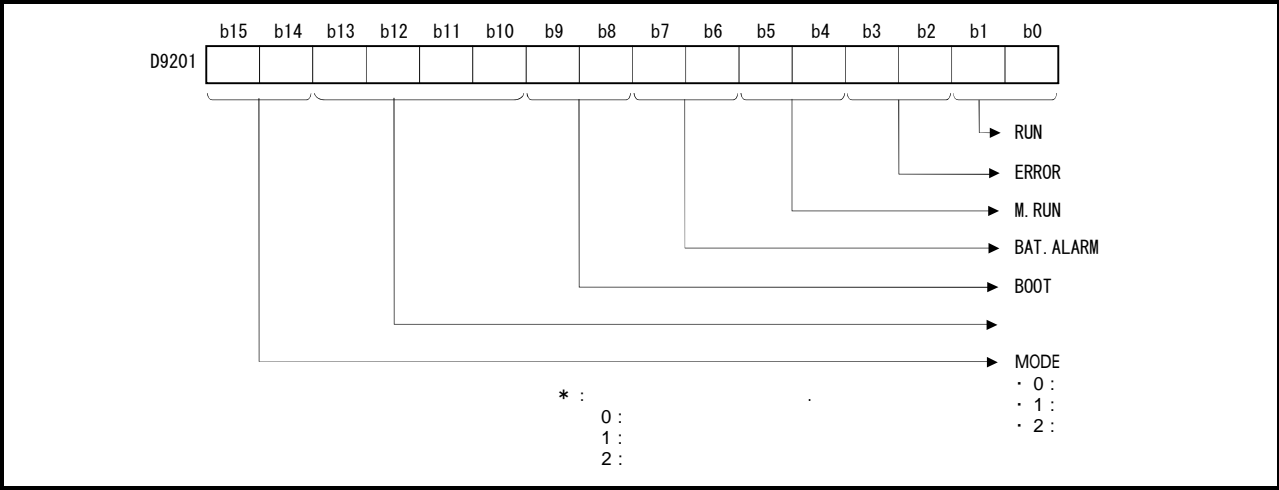
PC , 1. 5 .

(9) (D9197).....
[μs] .
「 」 , 가 .
0.8[ms]/1.7[ms]/3.5[ms]/7.1[ms]
가 .
) : MR-H-BN 0.8[ms] .
MR-H-BN , 0.8[ms] ,
1.7[ms] .

(10) CPU (D9200).....
가,



(11) LED (D9201).....
CPU LED가
0 , 1 , 2 .



4

4. 1

- CPU , CPU
(1) CPU . CPU ,
(2) CPU ,
(3) (, 「Q173CPU/Q172CPU (SV13/SV22) (SFC)」 .)

4. 2

- (1) ,
(2) ,
(3) , 4. 1 .

4. 1

No.																
			mm		inch		degree		PLS							
1			0	—	1	—	2	—	3	—	3	—	•	—		
2	1	1	1~2147483647[PLS]									20000	PLS	•	1	4. 2. 1
3	(A)	1	0. 1~ 214748364. 7		0. 00001~ 21474. 83647		0. 00001~ 21474. 83647		1~ 2147483647		20000	•		1		
4		*	0~6553. 5		0~0. 65535		0~0. 65535		0~65535		0	•			7. 2	
5		*	—214748364. 8 ~ 214748364. 7	μ m	—21474. 83648 ~ 21474. 83647	inch	0~359. 99999	degree	—2147483648 ~ 2147483647	PLS	2147483647	•	(SV13) —2147483648 ≤ () ×AP/AL ≤2147483647	4. 2. 2		
6		*	—214748364. 8 ~ 214748364. 7		—21474. 83648 ~ 21474. 83647		0~359. 99999		—2147483648 ~ 2147483647		0	•	(SV13) —2147483648 ≤ () ×AP/AL ≤2147483647			
7		*	0. 1~ 214748364. 7		0. 00001~ 21474. 83647		0. 00001~ 359. 99999		1~ 2147483647		100	•	(M2403+20n) ON [()-()] 1 ≤ () ×AP/AL ≤32767		4. 2. 3	

* : , 가 가 .

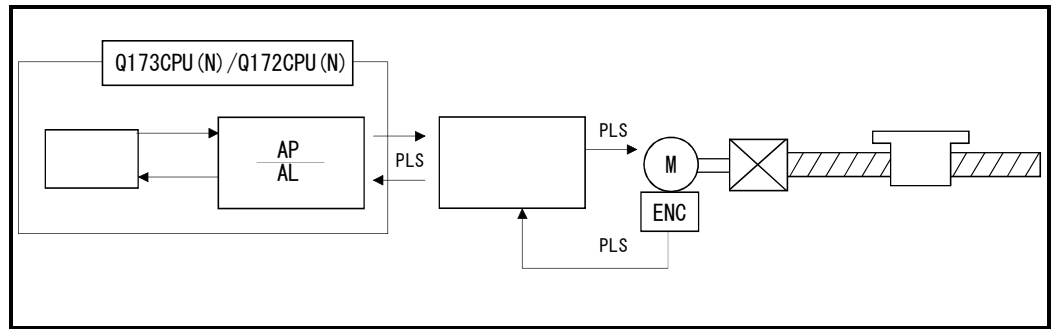
4.2.1 1

「 」, Q173CPU(N)/Q172CPU(N)
 "1", "1"

(1)	「 」, 1
(2)	Q172CPU(N), Q173CPU(N)/1
(3)	, (,), 「0」, 가 . (.)

, 1, 1

(a) 1 .
 1 (AP) • (AL) ,
 가 , () 가
 .
 , (ENC)
 .
 CPU



4. 1 CPU

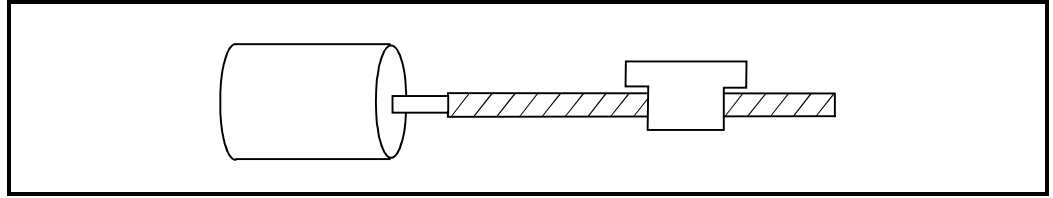
, 가
 1 (S4) [mm]/[inch] ,
 () ,[mm]/[inch]
 ,
 , [mm]/[inch]
 , AP, AL

1 = AP
 1 = AL

$$= \frac{AP}{AL} \dots (1)$$

(AP, AL 가 가 가 , 가
 () , AP, AL 가 가 .)

(20[mm]), HC-MFS(131072[PLS/rev]), ()



4. 2

, 가 1 (AP) , ()가 mm (AL)

$$AP (1) = 131072[PLS]$$

$$AL (1) = \times \\ = 20[mm]$$

, (1) .

$$\frac{AP}{AL} = \frac{131072[PLS]}{20[mm]}$$

, [mm] ,
0.1[μm]가 가 . AL , 0.1[μm] 가
, 20[mm](20.000[mm]) 20000.0[μm] .

$$\frac{AP}{AL} = \frac{131072[PLS]}{20000.0[\mu m]}$$

, 1 , 0.00015[mm] .
, 19[mm] , 124518.4[PLS]
, 0.4[PLS] 가 . CPU 124518[PLS]
CPU
, 가 .

4. 2. 2

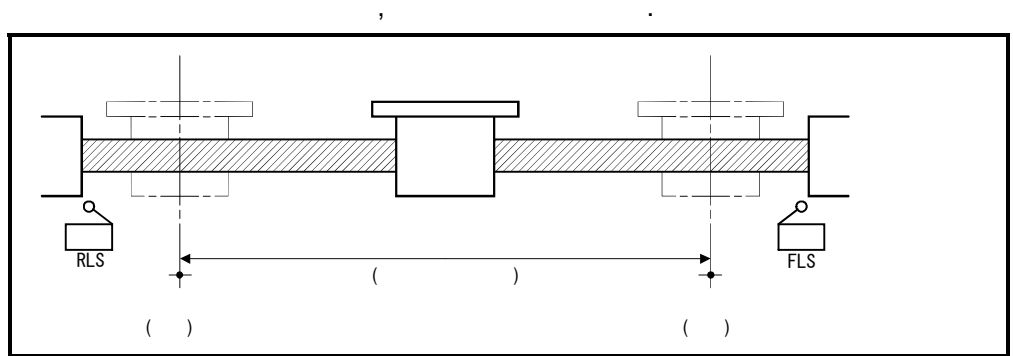
(1) , 가 .

$$0 \leq \frac{\times 1}{1} \frac{(AP)}{(AL)} (=A) \leq 65535 [PLS]$$

(2) , (2035) , () 가 , , .

$$A \leq \frac{[r/min] \times 1.2 \times [PLS] \times [ma]}{60[s] \times 1000[ms]} [PLS]$$

4. 2. 3



4. 3

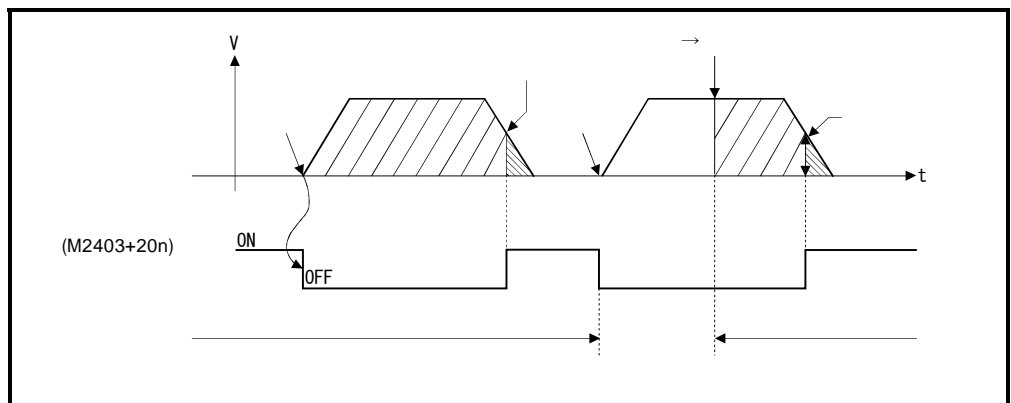
(1)

	/	
		<p>· , (:106)가 , 가</p> <p>· , (:207,208)가</p> <p>· 가 ,</p>
(I)		· 「0」 , (FLS,RLS,STOP,
(II)		가
()		· ,
JOG		· 가 가
		· , 가 ,

(1)		,	,	(FLS, RLS)	가	/
(2)	가 OFF	,	"	"	"	"

4. 2. 4

, ()
 [(-) ()]가 , (M2403
 +20n)가ON .




4.

4. 3 /

(1) , , ,

(2) / , .

	
●	/ , 「 」 , 「 」 , 「 , 「 」 ,

4. 3. 1

4.2~ 4.4 .



MR-H□BN, MR-H□BN4	SSCNET MR-H□BN (SH-3191)
MR-J2S-□B	SSCNET MR-J2S-□B (SH-030001)
MR-J2M-B	SSCNET MR-J2M-B (SH-030011)
MR-J2-□B	SSCNET MR-J2-□B (IB-67287)
MR-J2-03B5	SSCNET MR-J2-03B5 (SH-030004)

(1)

4. 2 ()

No.			(
-----	--	--	---	--	--	--	--

4.2 () ()

No.			()						
				가 (○ : 가 , × : 가)					
				MR- H-BN	MR- H-BN4	MR- J2-B	MR- J2S-B		MR- J2-Jr
8		.	 	○	○	○	○	○	—
9			0 : 1 : 2 : ()	○	○	○	—	○	4.3.8
			0 : 1 : 1 2 : 2 3 : 2 4 : 1	—	—	—	○	—	
10		() 가	1 :	○	○	○	—	○	4.3.9
			2 :						
			3 :						
			4 :						
			5 :						
			8 :						
			9 :						
			A :						
			B :						
			C :						
			1 : (15Hz)	—	—	—	○	—	
			2 : (20Hz)						
			3 : (25Hz)						
			4 : (30Hz)						
			5 : (35Hz)						
			6 : (45Hz)						
			7 : (55Hz)						
			8 : (70Hz)						
			9 : (85Hz)						
			A : (105Hz)						
			B : (130Hz)						
			C : (160Hz)						
			D : (200Hz)						
			E : (240Hz)						
F : (300Hz)									

(1) * , CPU , PLC
(M2000) OFF → ON ,
(2) MR-J2M-B , "MR-J2S-B"
 , MR-J2S-B

(2)

4.3

()

No.			()						
				가 (○ : 가 , × : 가)					
				MR- H-BN	MR- H-BN4	MR- J2-B	MR- J2S-B		MR- J2-Jr
1		가 " " 1, 2, " 1, 2" " " CPU ON, , PLC (M2000) Q173CPU(N)/Q172CPU(N)	0~100.0[]	○	○	○	—	○	4.3.7
			0~300.0[]	—	—	—	○	—	
2	1	1 1	4~1000[rad/s]	○	○	○	—	○	4.3.2
			4~2000[rad/s]	—	—	—	○	—	
3	1	1 , 가	20~5000[rad/s]	○	○	○	—	○	4.3.3
			20~8000[rad/s]	—	—	—	○	—	
4	2	2 , 가	1~500[rad/s]	○	○	○	—	○	4.3.2
			1~1000[rad/s]	—	—	—	○	—	
5	2	가 2 , 가	20~8000[rad/s]	○	○	○	—	○	4.3.3
			20~20000[rad/s]	—	—	—	○	—	
6			1~1000[ms]	○	○	○	○	○	4.3.4
7	()		00 : 01 : 1125[Hz] 02 : 563[Hz] 03 : 375[Hz] 04 : 282[Hz] 05 : 225[Hz] 06 : 188[Hz] 07 : 161[Hz]	○	—	○	—	○	4.3.10
			00 : 08 : 141[Hz] 01 : 1125[Hz] 09 : 125[Hz] 02 : 563[Hz] 10 : 113[Hz] 03 : 375[Hz] 11 : 102[Hz] 04 : 282[Hz] 12 : 94[Hz] 05 : 563[Hz] 13 : 87[Hz] 06 : 375[Hz] 14 : 80[Hz] 07 : 282[Hz] 15 : 75[Hz]	—	○	—	—	—	

() ()

No.			()						
				가 (○ : 가 , × : 가)					
				MR- H-BN	MR- H-BN4	MR- J2-B	MR- J2S-B		MR- J2-Jr
7*	()		00 : 10 : 281.3[Hz] 01 : 4500[Hz] 11 : 264.7[Hz] 02 : 2250[Hz] 12 : 250[Hz] 03 : 1500[Hz] 13 : 236.8[Hz] 04 : 1125[Hz] 14 : 225[Hz] 05 : 900[Hz] 15 : 214.3[Hz] 06 : 750[Hz] 16 : 204.5[Hz] 07 : 642.9[Hz] 17 : 195.7[Hz] 08 : 562.5[Hz] 18 : 187.5[Hz] 09 : 500[Hz] 19 : 180[Hz] 0A : 450[Hz] 1A : 173.1[Hz] 0B : 409.1[Hz] 1B : 166.7[Hz] 0C : 375[Hz] 1C : 160.1[Hz] 0D : 346.2[Hz] 1D : 155.2[Hz] 0E : 321.4[Hz] 1E : 150[Hz] 0F : 300[Hz] 1F : 145.2[Hz]	—	—	—	○	—	4.3.10
	()	0 : (-40db) 1 : ↑ (-14db) 2 : ↓ (-8db) 3 : (-4db)	—	—	—	○	—		
8		가 , 100[%] 가 가 1[s] (100[%] 가) <div></div> 「2 : ()」	0~100[%]	○	○	○	○	○	4.3.6
9		<div></div> MR-J2S-B	0~32767[PLS]	○	○	○	○	○	4.3.5
10		(MBR)가 OFF가	0~1000[ms]	○	○	○	○	○	4.3.11

* : MR-J2S- B , 2

4.3 () ()

No.			()					詳細 説明項	
				가 (○ : 가 , × : 가)					
				MR- H-BN	MR- H-BN4	MR- J2-B	MR- J2S-B		MR- J2-Jr
11 [*]	(1)	CH1.CH2	0 : (±)	○	○	—	—	—	
			1 : (±)						
			2 : (±)						
			3 : (±)						
			4 : (±)						
			5 : (<input checked="" type="checkbox"/> T) (±)						
			6 : 1/1 (±)						
			7 : 1/4 (±)						
			8 : 1/16 (±)						
			9 : 1/32 (±)						
			A : 1/64 (±)						
12 [*]	(2)	CH1.CH2	0 : (±)	—	—	○	—	—	
			1 : (±)						
			2 : (±)						
			3 : (+)						
			4 : (±)						
			5 : (<input checked="" type="checkbox"/> T) (±)						
			6 : 1/1 (±)						
			7 : 1/16 (±)						
			8 : 1/64 (±)						
			9 : 1/256 (±)						
			A : 1/1024 (±)						
		CH1.CH2	0 : (±8V/)	—	—	—	○	—	
			1 : (±8V/)						
			2 : (+8V/)						
			3 : (+8V/)						
			4 : (±8V/)						
			5 : (±8V/)						
			6 : (±10V/128)						
			7 : (±10V/2048)						
			8 : (±10V/8192)						
			9 : (±10V/32768)						
			A : (±10V/131072)						
			B : (+8V/400V)						

* : MR-J2S- B , 2

4.3 () ()

No.									
			()						
			가 (○ : 가 , × : 가)						
			MR- H-BN	MR- H-BN4	MR- J2-B	MR- J2S-B	MR- J2-Jr		
13	1 ()	1() , ()	0 : () 1 : ()	—	—	○	○	○	4.3.13
14	1 ()	() "1 : 9.0[KHz]" 20dB	0 : 2.25KHz	○	—	—	—	—	
			2 : 6.375KHz	—	○	—	—	—	
			3 : 9KHz	○	○	—	—	—	
15	1 ()	·	0 : 2 1 : 4 ()	○	○	○	○	○	4.3.14
16	2 ()	· 2 · 가 , , 가	0 : 1 : () " " "2")	—	—	○	○	○	
17	2 ()		0 : 1 :	○	○	○	○	○	
18	2 ()		가 · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · ·						

* : MR-J2S- B , 2

(3)

4. 4

()

No.			()	가 (○ : 가 , × : 가)					詳細 説明項
				MR- H-BN	MR- H-BN4	MR- J2-B	MR- J2S-B	MR- J2-Jr	
1	1	1	-9999~9999 -999~999	○	○	—	—	—	4. 3. 15項
2	2	2	-9999~9999 -999~999	○	○	—	—	—	
3	(1)		0 : 1 : 2 : 3 : (+) 4 : 5 : $F\Delta T$ 6 : 1/1 7 : 1/4 8 : 1/16 9 : 1/32 A : 1/64	○	○	—	—	—	4.3.16
4	(2)								
5	()		0 : 1.77[ms] 1 : 3.55[ms] 2 : 7.11[ms] 3 : 14.22[ms] 4 : 28.44[ms]	○	○	—	—	—	
6		(zsp)	0~10000[r/min]	○	○	○	○	○	4.3.17
7		(52)	0~1000[PLS] 0.1~100.0[rev]	○	○	○	—	○	4.3.18
8	5 (PI-PID)	PI-PID	0 : PI 가 1 : 2 : PID 가	○	○	○	○	○	4.3.19
9	5 ()	0400h	0 : 1 :	○	○	—	—	—	
10*	6 ()		0 : 9600[bps] 1 : 19200[bps] 2 : 38400[bps] 3 : 57600[bps]	—	—	—	○	—	—
11*	6 ()		0 : 1 : (888[μs])						
12*	6 ()		0 : 1 :						
13	PI-PID	PI () PID 가 「0001h」	0~50000[PLS]	○	○	○	○	○	4.3.20
14		PI() 1000 , P() 가 , 1000 , P() 가	0~1000	○	○	○	○	○	4.3.22

* : MR-J2S- B , 2

4. 4 () ()

No.			()						
				가 (○ : 가 , × : 가)					
				MR- H-BN	MR- H-BN4	MR- J2-B	MR- J2S-B		MR- J2-Jr
15*		가 (A ,B) 1 (4) . . A , B 1/4 가 . , 1.3Mpps (4) 가 .	0~65535	—	—	—	○	—	—

* : MR-J2S- B , 2

(1) " " 1, 2, " " 1, 2, " " 가 , CPU
 , , PLC (M2000) , 가 .

2613	(1)	" " , M2000 OFF →ON, .
2614	(1)	
2615	(2)	
2616	(2)	
2617	()	

4. 3. 2

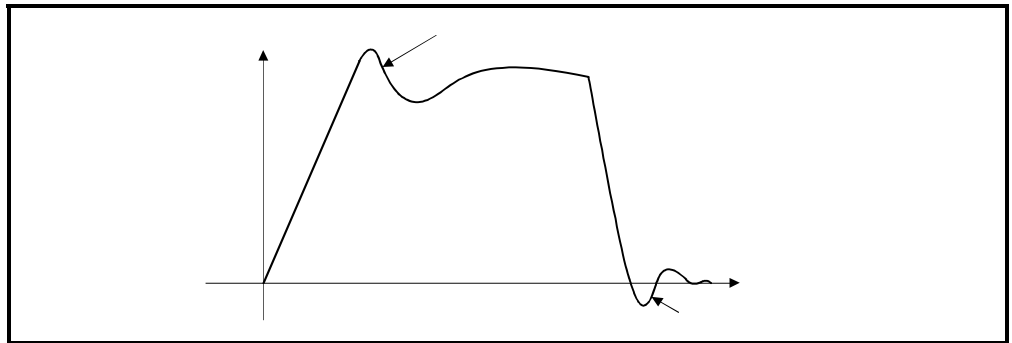
1, 2

(1)

(a)

(b)

, , 가



(2)

2

(a)

(b)

2

$$2 = \frac{2}{1 +} \times \frac{1}{10}$$

(1) 1 , 가 ,

(2) ()가 . 1 .(

1 ,

.)

4.

4. 3. 3

1, 2

(1) 1

(a)

(b)

(2) 2

(a) , 가 , () .

(b) 2 , 4. 5 .

4. 5 2

(GD_L^2/GD_M^2)	1	3	5	10	20	30	
[ms]	800	1000	1500	2000	2000	2000	1~9999 가 (20~5000)

(1)	1	,	가	,
(2)	()	1	.	
(3)	()	1	1	,
	.			

4. 3. 4

(1) , .

(2) , , 가 가

(3) , 4.6 .

4. 6

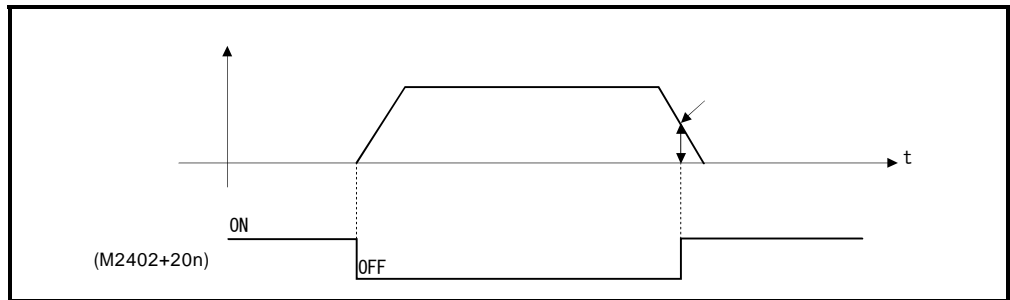
(GD_L^2/GD_M^2)	1	3	5	10	20	30	
[ms]	20	30	40	60	100	200	1~9999 가 (1~1000)

4.

4. 3. 5

(1) , .

(2) 가, 가 , (M2402+20n)가 ON .



4. 3. 6

..... 0~100[%]

4. 3. 7

(1) .

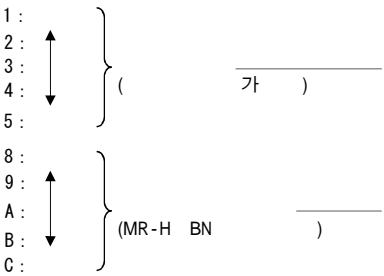
= _____

(2) , 가 .

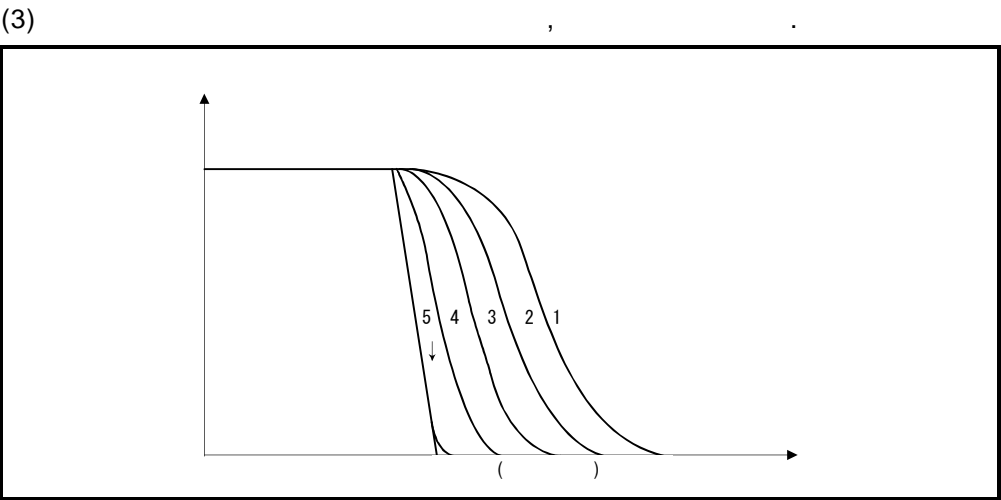
4. 3. 8

4. 3. 9

(1) 1,2, . . . ,5 ,
 , 8~C



(2) ,
 ,
 ,
 가 5 , 1



(4) ,

4.

4. 3. 10

	[Hz]
0	
1	1125
2	750
3	562
4	450
5	375
6	321
7	281

4. 3. 11

4. 3. 12

가 2

4. 3. 13 1

(1) 가

(2)

0 0

0 : 2.25kHz ()
3 : 9kHz()

0 : 2
1 : 4

1 ()

(3) (MR-J2S- B/MR-J2- B)
(EMI)

0 :
1 : (ON)

4. 3. 14 2

(1)

0 :
1 :

, , 가
가 가
CPU SFC

(2)

0 :

.

.

.

가

1 :

가

"0

"

가

(3)

(MR-J2S- B/MR-J2- B)

0 :

1 :

(4)

(MR-J2S B/MR-J2- B)

가

, MR-H BN

0 :

1 :

가

, MR-

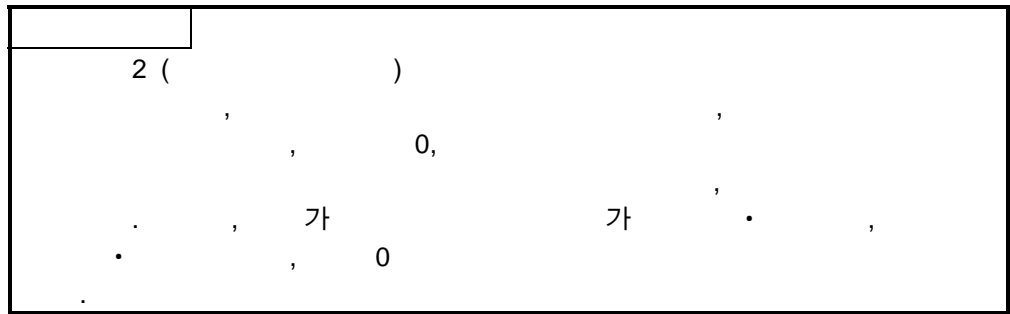
, 가

J2S- B/MR-J2- B

[900] (

가)

4.

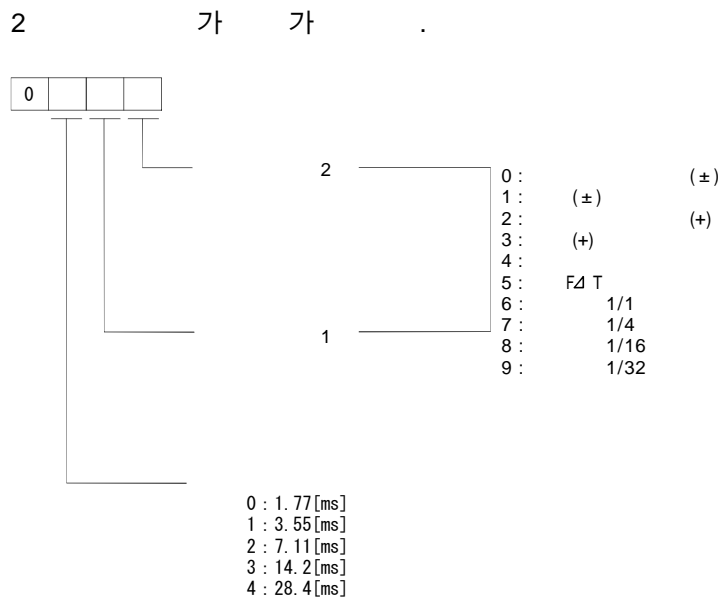


4. 3. 15 1, 2

1, 2

4. 3. 16

(1) , .
, .
(2) .



4.

4. 3. 17

0

4. 3. 18

4. 3. 19 5

(1) PI-PID

PI PID, PID PI

(2)

4. 3. 20 PI-PID

, PI-PID ()
5 PI-PID
가

4. 3. 21

(MR-H BN)

가 가

4. 3. 22

PI() , 1000 P
() 가 , 1000 , P() 가

4.

4. 4

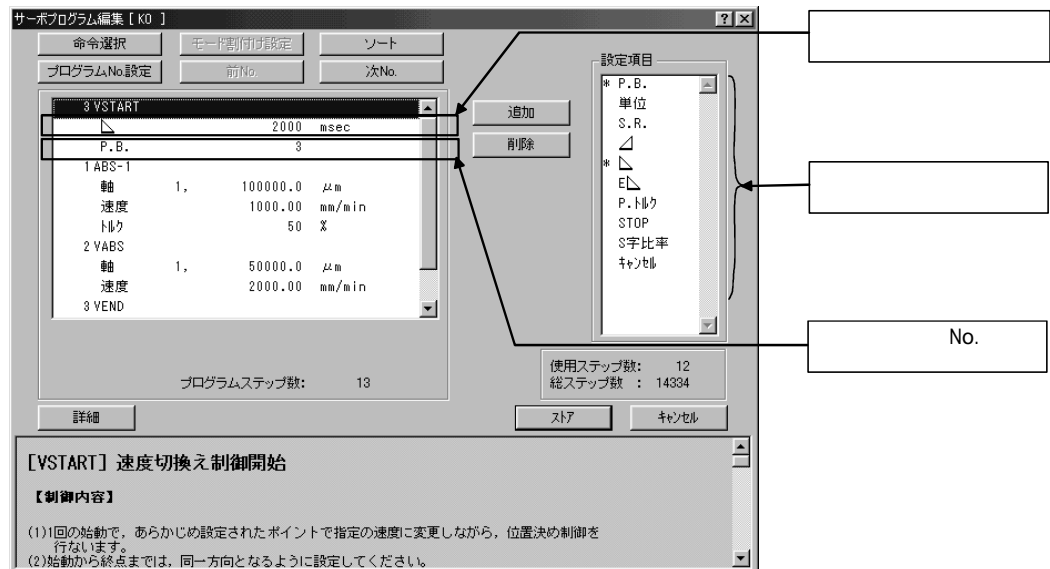
- (1) , 가
- (2) , 63 가 .
- (3) , .
- (4) , 4.8 .

4. 8

No.													
		mm		inch		degree		PLS					
1		0	—	1	—	2	—	3	—	3	—	· · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · · ·	

(1)	, , JOG ,
(2)	, 가
(5. 3)

(1) , , JOG
No. ,
No.1

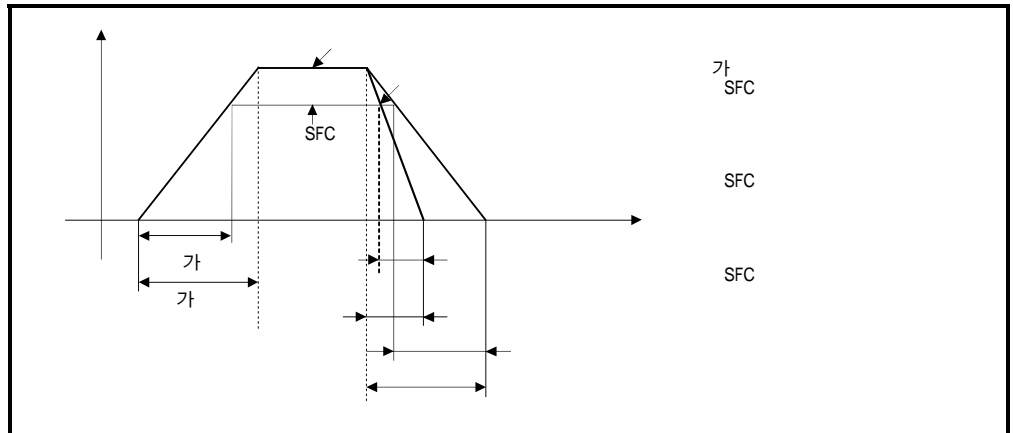


(2) ・ JOG No. ,
・ 「JOG」 (, 6.22.1
・ JOG JOG
・ JOG)



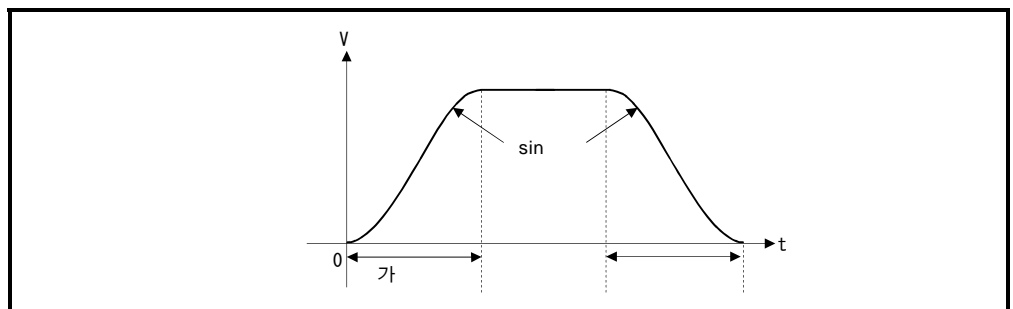
4. 4. 1

, 가 , , / .
 가 , , .
 , 가 , , 가



4. 2. 2 S

S 가 가 , S 가 .
 (S 가 , 6.1.7 .)
 S , 0~100[%] .
 , 가 , S 100[%]
 , (D9190) .
 S , 가 가 .
 S , sin .



4. _____

MEMO

[illegible]

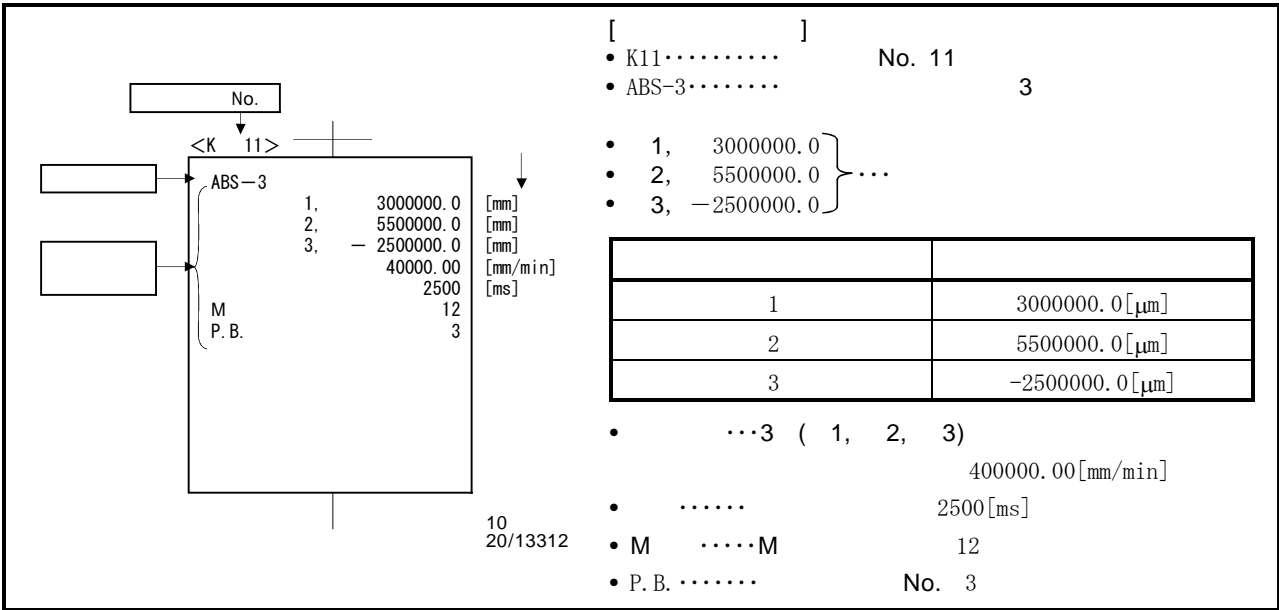
CPU

5. 1

5. 1. 1

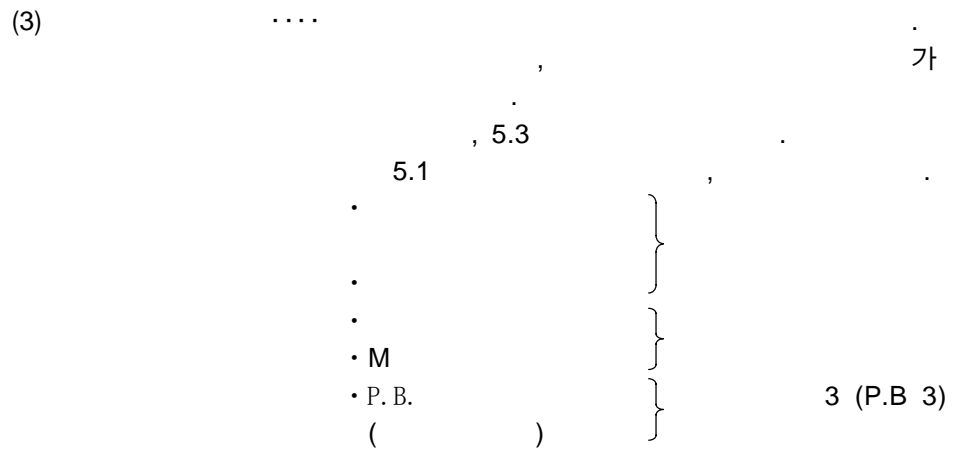
No.,

No.,

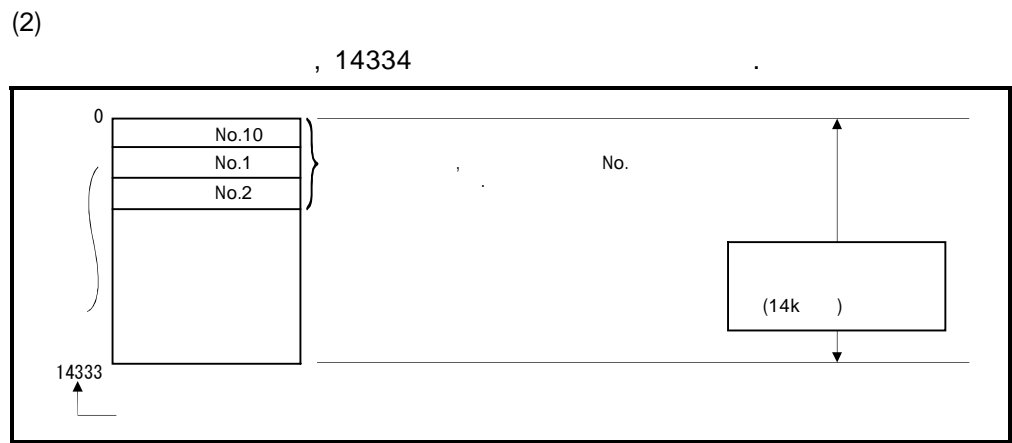
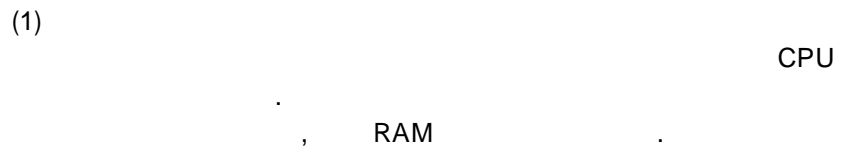


5. 1

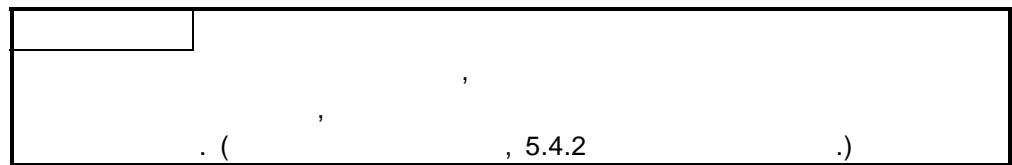
- (1) No..... SFC , 0~4095
- (2) , 5.2



5. 1. 2



5. 2



5.

5.2

(1)

5.1

				③																④																⑤																⑥																⑦																⑧																																																																																															
				No.																M																OSC																*1																가																STOP																S																No. ()																FIN																WAITING/OFF																															
				가																1																2																1																2																1																2																1																2																1																2															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															
				1																2																1																2																1																2																1																2																1																2																															

가

•

•

5. 2

*1 :
*2 : (B)

5.2 ()

															OSC		*1																
			No.	/			M										No.	가						STOP	S						FIN가	WAIT TION/OFF	
가	○	○	○	○	○	○	—	○	○	○	○	—	—	—	○	—	○	○	○	○	—	—	○	○	○	○	○	○	—	○	○	○	
	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	2	1	1	1	1	1	1	1	1	1	2	2	1	2	1	2
	1	—	2	2	1	1	1	2	2	2	1	2	2	2	1	1	2	1	1	1	1	1	2	1	^{#2} 1/ 1(B)	—	2	^{#2} 1(B)	—	^{#2} 1(B)	1	^{#2} 1(B)	

	<div>ABH</div>		△	○	○	○	△	△		○				△	△	△	△	△	△	△	△		△				△	△					10~27	
		<div>INH</div>	△	○	○	○	△	△		○		○			△	△	△	△	△	△	△	△		△				△	△					
	<div>ABH</div>	<div>↶</div>	CW 180°	△	○	○	○	△	△			○		○				△	△	△	△	△	△	△		△				△	△			9~26
		<div>↷</div>	CW 180°	△	○	○	○	△	△			○		○				△	△	△	△	△	△	△		△				△	△			
		<div>↶</div>	CCW 180°	△	○	○	○	△	△			○		○				△	△	△	△	△	△	△		△				△	△			
		<div>↷</div>	CCW 180°	△	○	○	○	△	△			○		○				△	△	△	△	△	△	△		△				△	△			
		<div>↶</div>	CW 180°	△	○	○	○	△	△			○		○				△	△	△	△	△	△	△		△				△	△			
		<div>↷</div>	CW 180°	△	○	○	○	△	△			○		○				△	△	△	△	△	△	△		△				△	△			
		<div>↶</div>	CCW 180°	△	○	○	○	△	△			○		○				△	△	△	△	△	△	△		△				△	△			
		<div>↷</div>	CCW 180°	△	○	○	○	△	△			○		○				△	△	△	△	△	△	△		△				△	△			
	<div>ABH</div>	<div>↶</div>	CW	△	○	○	○	△	△			○	○				△	△	△	△	△	△	△		△				△	△			10~27	
		<div>↷</div>	CCW	△	○	○	○	△	△			○	○				△	△	△	△	△	△	△		△				△	△				
		<div>↶</div>	CW	△	○	○	○	△	△			○	○				△	△	△	△	△	△	△		△				△	△				
		<div>↷</div>	CCW	△	○	○	○	△	△			○	○				△	△	△	△	△	△	△		△				△	△				

:
 *1 :
 *2 : (B)

5. 2 ()

															OSC	*1																				
			No.	/		M										No.	가					S	T	O	P	S	No.()					FIN 가	W A I T I O N / O F F			
가	○	○	○	○	○	○	—	○	○	○	○	—	—	—	○	—	○	○	○	○	—	—	○	○	○	○	○	○	○	○	○	○				
			1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	2	1	1	1	1	1	1	1	1	1	1	2	2	1	2	1	2		
			1	—	2	2	1	1	1	2	2	2	1	2	2	2	1	1	2	1	1	1	1	1	2	1	^{*2} 1/ 1(B)	—	2	^{*2} 1(B)	—	^{*2} 1(B)	1	^{*2} 1(B)		
	1	FEED-1	1	△	○	○	○	△	△										△	△	△	△	△	△	△		△						4~17			
	2	FEED-2	2	△	○	○	○	△	△										△	△	△	△	△	△	△	△		△					5~19			
	3	FEED-3	3	△	○	○	○	△	△										△	△	△	△	△	△	△	△	△		△					7~21		
()		VF	()	△	○		○	△												△	△	△	△	△	△	△		△						3~15		
		VR	()	△	○		○	△												△	△	△	△	△	△	△	△		△							
()		VVF	()	△	○		○	△	△												△	△	△	△	△	△	△		△						3~16	
		VVR	()	△	○		○	△	△												△	△	△	△	△	△	△	△		△						
		VPF	·	△	○	○	○	△	△	△											△	△	△	△	△	△	△		△						4~18	
		VPR	·	△	○	○	○	△	△	△											△	△	△	△	△	△	△		△							
		VPSTART	·	○																										△					2~4	
		VSTART		△																△	△	△	△	△	△	△	△		△						1~13	
		VEND																																	1	
		ABS-1			○	○	○	△	△	△																				△					4~9	
		ABS-2			○	○	○	△	△	△																				△					5~10	
		ABS-3			○	○	○	△	△	△																				△					7~12	
		INC-1			○	○	○	△	△	△																				△					4~9	
		INC-2			○	○	○	△	△	△																				△					5~10	
		INC-3			○	○	○	△	△	△																				△					7~12	
		VABS				○	○	△	△																											4~6
	VINC				○	○	△	△																												
		PFSTART		△	○	○	○	△													△	△	△	△	△	△	△		△			△			4~16	
		CPSTART1	1	△	○		○														△	△	△	△	△	△	△		△			△			3~15	
		CPSTART2	2	△	○		○														△	△	△	△	△	△	△	△		△			△		3~17	
		CPSTART3	3	△	○		○														△	△	△	△	△	△	△	△		△			△			4~17
		CPSTART4	4	△	○		○															△	△	△	△	△	△	△	△		△			△		

:
 *1 :
 *2 : (B)

5.2 ()

													OSC				*1																		
			No.		/			M												가				STOP	S					No. ()		FIN 가	WAIT TION / OFF		
가			○	○	○	○	○	○	—	○	○	○	○	—	—	—	○	—	○	○	○	○	—	—	○	○	○	○	○	○	○	○			
			1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	2	1	1	1	1	1	1	1	1	2	2	1	2	1	2	
			1	—	2	2	1	1	1	2	2	2	1	2	2	2	1	1	2	1	1	1	1	1	1	2	1	*2 1/1(B)	—	2	*2 1(B)	—	*2 1(B)	1	*2 1(B)
ABS	ABS — 1			○	○			△	△																			△		△	△	2~10			
	ABS — 2			○	○			△	△																			△		△	△	3~11			
	ABS — 3			○	○			△	△																			△		△	△	4~12			
	ABS — 4			○	○			△	△																			△		△	△	5~13			
	ABS ↗			○	○			△	△	○																		△		△	△	5~14			
	ABS ↖			○	○			△	△	○																		△		△	△	4~13			
	ABS ↗			○	○			△	△	○																		△		△	△				
	ABS ↖			○	○			△	△	○																		△		△	△				
	ABS ↗			○	○			△	△	○																		△		△	△				
	ABS ↖			○	○			△	△		○																	△		△	△	5~14			
	ABS ↗			○	○			△	△		○																	△		△	△				
	ABH ↗			○	○			△	△	○		○																△		△		9~14			
	ABH ↖			○	○			△	△	○		○																△		△		8~13			
	ABH ↗			○	○			△	△	○		○																△		△					
	ABH ↖			○	○			△	△	○		○																△		△					
	ABH ↗			○	○			△	△	○		○																△		△					
	ABH ↖			○	○			△	△		○	○																△		△		9~14			
	ABH ↗			○	○			△	△		○	○																△		△					
	ABH ↖			○	○			△	△		○	○																△		△					
INC	INC — 1			○	○			△	△																			△		△	△	2~10			
	INC — 2			○	○			△	△																			△		△	△	3~11			
	INC — 3			○	○			△	△																			△		△	△	4~12			
	INC — 4			○	○			△	△																				△		△	△	5~13		
	INC ↗			○	○			△	△	○																		△		△	△	5~14			
	INC ↖			○	○			△	△	○																		△		△	△	4~13			
	INC ↗			○	○			△	△	○																		△		△	△				
	INC ↖			○	○			△	△	○																		△		△	△				
	INC ↗			○	○			△	△	○																		△		△	△				
	INC ↖			○	○			△	△		○																		△		△	△	5~14		
	INC ↗			○	○			△	△		○																		△		△	△			
	INC ↖			○	○			△	△		○																		△		△	△			
	INC ↗			○	○			△	△		○																		△		△	△			

:
 *1 :
 *2 : (B)

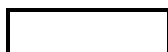
()

*1 :
*2 : (B)

This image shows a full page of white paper with horizontal ruling lines. The lines are evenly spaced and run across the width of the page, providing a template for writing or drawing. There are no margins, text, or other markings present.

					mm	inch	degree	PLS
M	No.	가 , STOP		1	1~64			
		No.		—	1~32			
				—	-214748364.8 ~214748364.7 [μm]	-21474.83648 ~21474.83647	0~359.99999	-2147483648 ~2147483647
				—	0~±2147483647			
				—	0~214748364.7 [μm]	0~21474.83647	0~21474.83647	0~2147483647
				—	0.01~ 6000000.00 [mm/min]	0.001~ 600000.000 [inch/min]	0.001~ 2147483.647 [degree/min]	1~10000000 [PLS/s]
				0[ms]	0~5000[ms]			
M		M		0	0~255			
		가			1~500[%]			
		가		[%]				
				—	-214748364.8 ~214748364.7 [μm]	-21474.83648 ~21474.83647	0~359.99999	-2147483648 ~2147483647
					0~±2147483647			
				—	0.1 ~429496729.5 [μm]	0.00001 ~42949.67295	0~359.99999	1~4294967295
				—	0.1 ~214748364.7 [μm]	0.00001 ~21474.83647	0.00001 ~21474.83647	1~2147483647
				—	-214748364.8 ~214748364.7 [μm]	-21474.83648 ~21474.83647	0~359.99999	-2147483648 ~2147483647
					0~±2147483647			

	SFC ()								
					가		*4 (D9190)		가
	mm	inch	degree	PLS					
	1~64				○	1	1	○	
	—				×	—	—		○
	−2147483648 ~2147483647 (×10 ^{−1} [μ m])	−2147483648 ~214748647 (×10 ^{−5} [inch])	0~35999999 (×10 ^{−5} [degree])	−2147483648 ~2147483647	○	2	n03*1		
	0~±214783647								
	0~2147483647 (×10 ^{−1} [μ m])	0~2147483647 (×10 ^{−5} [inch])	0~2147483647 (×10 ^{−5} [degree])	0~2147483647					
	1~600000000 (×10 ^{−2} [mm/min])	1~600000000 (×10 ^{−3} [inch/min])	1~2147483647 (×10 ^{−3} [degree/min])	1~10000000 [PLS/s]			○	2	4
	0~5000[ms]				○	1	5	○	
	0~255				○	1	6	○	
	1~500(%)				○	1	7	○	
	−2147483648~ 2147483647 (×10 ^{−1} [μ m])	−2147483648 ~2147483647 (×10 ^{−5} [inch])	0~35999999 (×10 ^{−5} [degree])	−2147483648 ~2147483647	○	2×2	n08*1		
	0~±2147483647								
	1~4294967295 (×10 ^{−1} [μ m])	1~4294967295 (×10 ^{−5} [inch])	0~35999999 (×10 ^{−5} [degree])	1~4294967295	○	2	n09*1		○
	1~2147483647 (×10 ^{−1} [μ m])	1~2147483647 (×10 ^{−5} [inch])	1~2147483647 (×10 ^{−5} [degree])	1~2147483647	○				
	−2147483648 ~2147483647 (×10 ^{−1} [μ m])	−2147483648 ~2147483647 (×10 ^{−5} [inch])	0~35999999 (×10 ^{−5} [degree])	−2147483648 ~2147483647	○	2×2	n10*1		
	0~±2147483647				○				



*1 :n03, n08, n09, n10 n , No.(1~32)

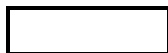
*2 :

*3 : 가 "0"

*4 : 가 , 가

			mm	inch	degree	PLS	
		3	0	1	2	3	
		200.000 [PLS/s]	0.01 ~6000000.00 [mm/min]	0.001 ~600000.000 [inch/min]	0.001 ~2147483.647 [degree/min]	1~10000000 [PLS/s]	
	가	1000[ms]	1~65535[ms]				
		1000[ms]	1~65535[ms]				
		1000[ms]	1~65535[ms]				
	S	0 [%]	0~100[%]				
		300[%]	1~500[%]				
	STOP	0	0 : 1 :				
		100[PLS]	0~10000.0[μm]	0~1.00000	0~1.00000	0~100000	
		FOR-TIMES NEXT	—	1~32767			
	No.	No.	—	0~4095			
	()		—	0.01 ~6000000.00 [mm/min]	0.001 ~600000.000 [inch/min]	0.001 ~2147483.647 [degree/min]	1~10000000 [PLS/s]
		ON	—	X, Y, M, B, F			
		ON	—	X, Y, M, B, F			
	FIN가	, FIN ON	—	1~5000[ms]			
	WAIT-ON/OFF	ON/OFF	—	X, Y, M, B, F			

	SFC ()				가		*4 (D9190)		가
	mm	inch	degree	PLS					
	0	1	2	3	○	1	11	○	
	1~600000000 ($\times 10^{-2}$ [mm/min])	1~600000000 ($\times 10^{-3}$ [inch/min])	1~2147483647 ($\times 10^{-3}$ [degree/min])	1~10000000 [PLS/s]	○	2	12		
	1~65535[ms]				○	1	13		
	1~65535[ms]				○	1	14		
	1~65535[ms]				○	1	15		
	0~100[%]				○	2	21		
	1~500[%]				○	1	16		
	0 : 1 :				○	1	—	K1	
	1~100000 ($\times 10^{-1}$ [μ m])	1~100000 ($\times 10^{-5}$ [inch])	1~100000 ($\times 10^{-5}$ [degree])	1~100000 [PLS]	○	2	17		
	1~32767				○	—	18		
	0~4095				○	—	19		○
	1~600000000 ($\times 10^{-2}$ [mm/min])	1~600000000 ($\times 10^{-3}$ [inch/min])	1~2147483647 ($\times 10^{-3}$ [degree/min])	1~10000000 [PLS/s]	○	2	4	○*2	○*3
	—				—	—	—		
	—				—	—	—		
	1~5000[ms]				○	1	13	1000[ms]	
	—				—	—	—		



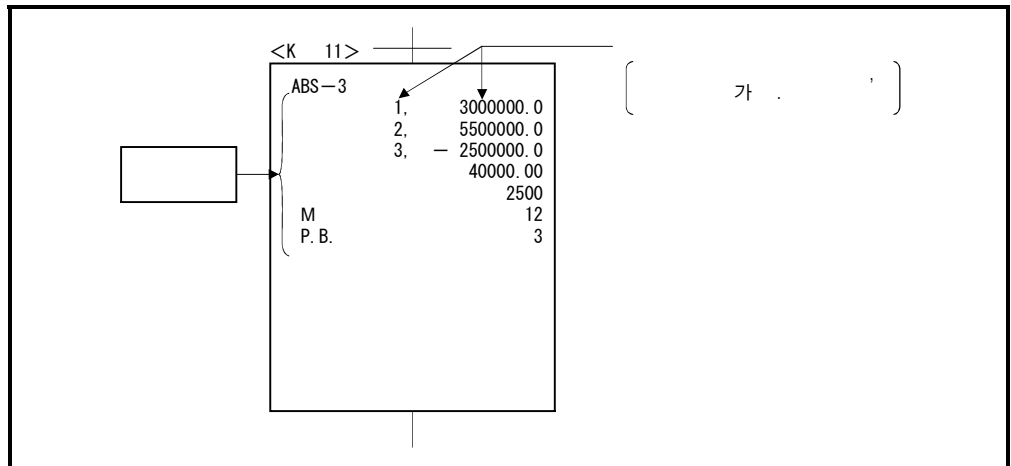
*2 : ,
 *3 : 가 "0" .
 *4 : 가 , 가

5. 4

- (1) 5.4.1
- (2) 5.4.2
- " , " " "
- 가 .

5. 4. 1

가 .

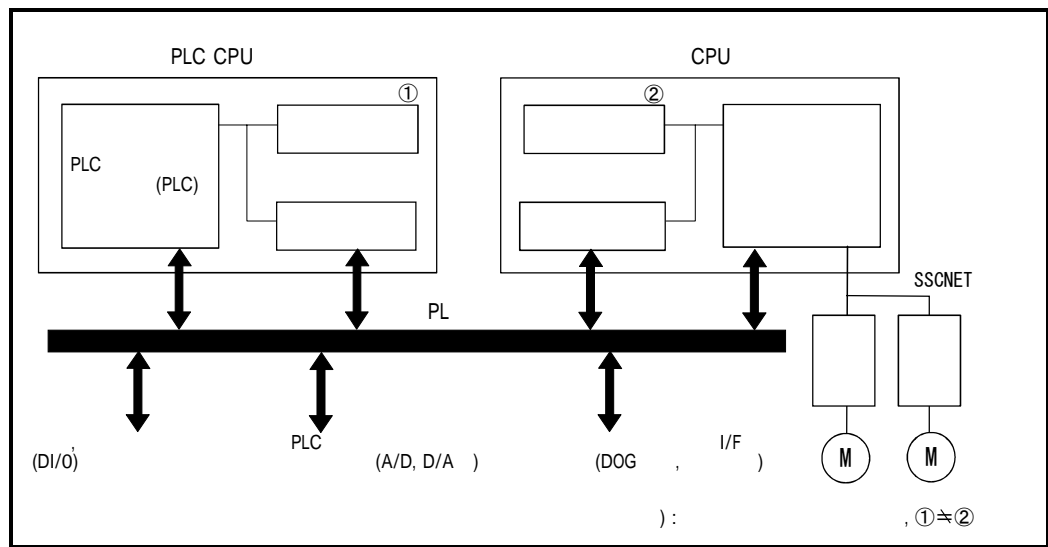


5. 3

5. 4. 2

(D, W, #)

*1 ,
(D, W, #)
SFC () , ()
CPU , PLC CPU
CPU, PLC CPU

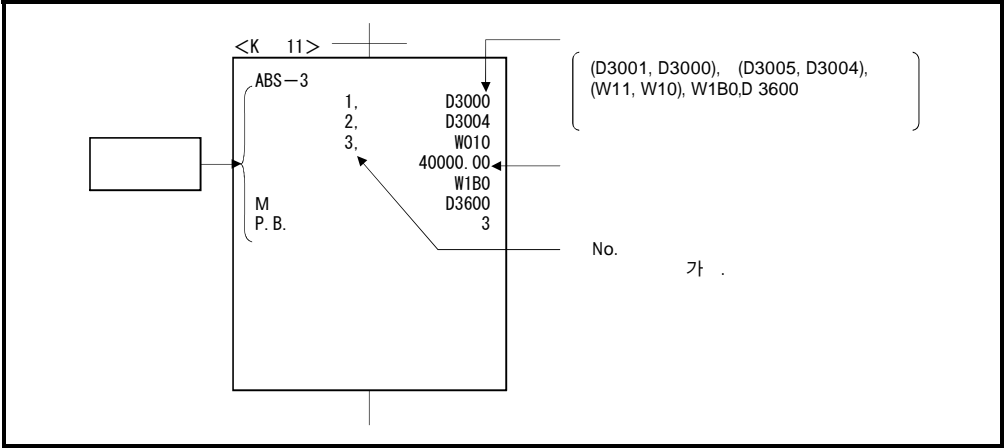


*1 : CPU

(1)

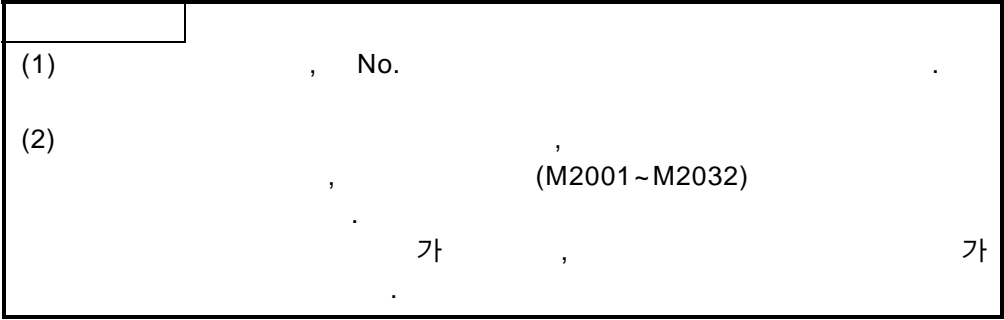
(#) (D) (W),
가 . (가 ,
) 가 ,

	가
D	800~8191
W	0~1FFF
#	0~7999



5. 4

(2) , CPU가 ,
.
, 가 .



6.

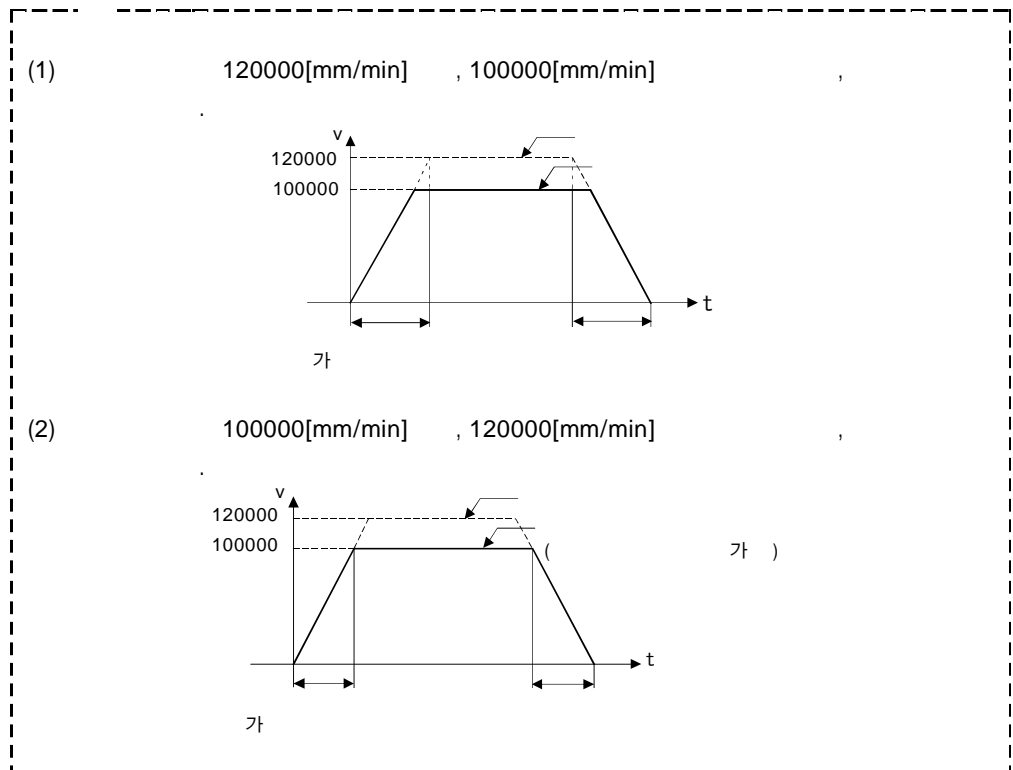
6

6. 1

6.2

6. 1. 1

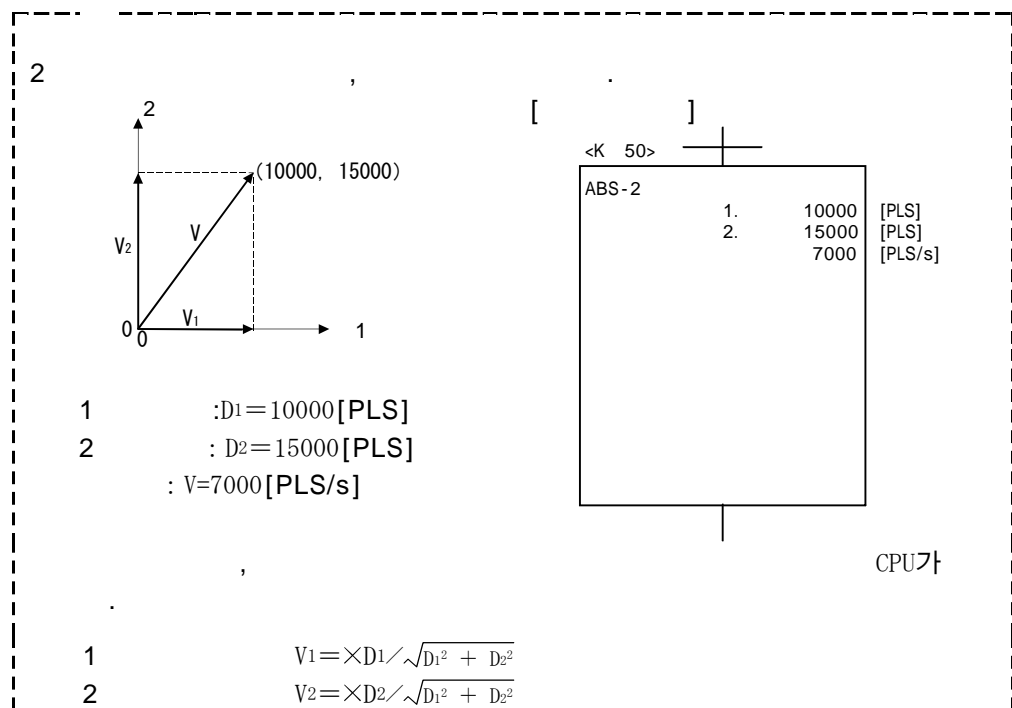
, 5

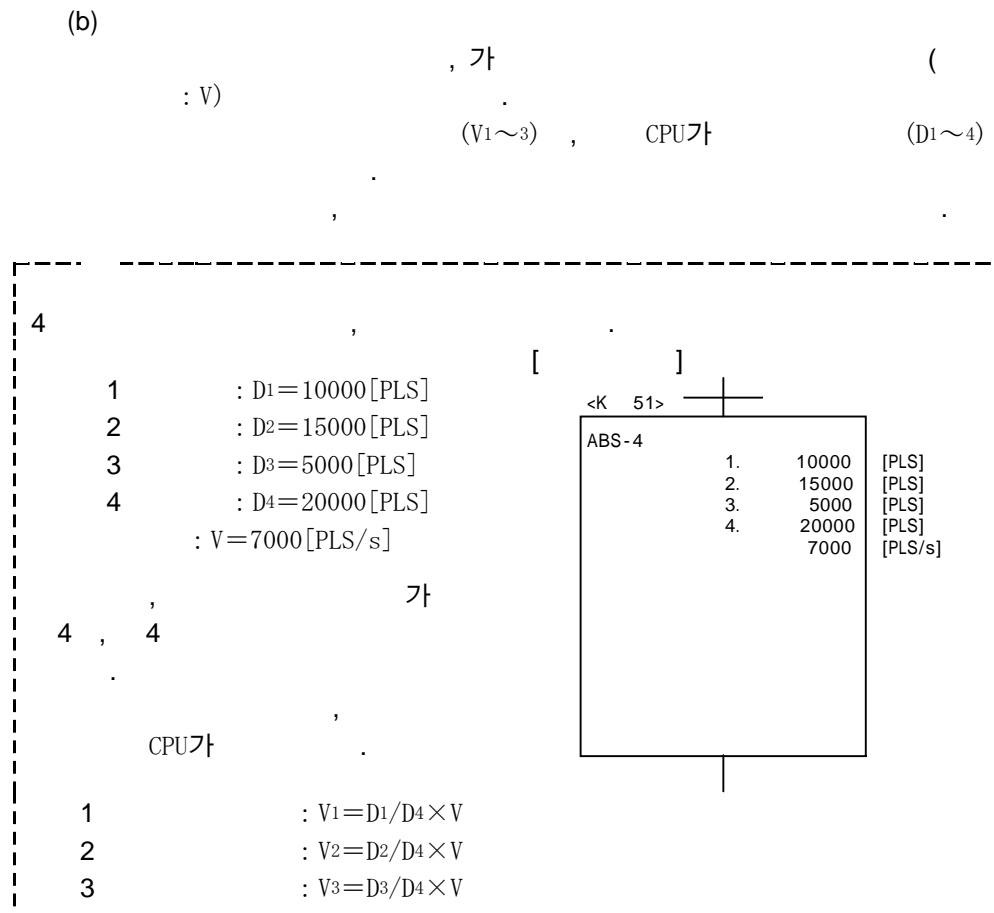


6

6. 1. 2

- CPU
- (1) 1 , 가 .
- (2) 2~4 , 3가 .
-
-
-
- CPU
- (a) (V₁~2) , (V)
- , CPU가 (D1~4) .





- 가 ,
- ① [mm] [inch]
- a) 가[mm]
- : [inch] [mm] [(inch
-) × 25.4]
- : , 가 ,
- b) 가[inch]
- : [mm] [inch] [(mm
-) ÷ 25.4]
- : , 가
- ② 가
- : , [PLS]
- : , 가 ,
- 가
- [PLS/s]

(1)

2, , 가

1 : 100 [PLS]

2 : 200 [PLS]

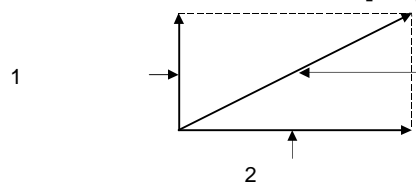
: 50 [PLS/s]

: 55 [PLS/s]

INC-2			
1.	100	[PLS]	
2.	200	[PLS]	
	50	[PLS/s]	

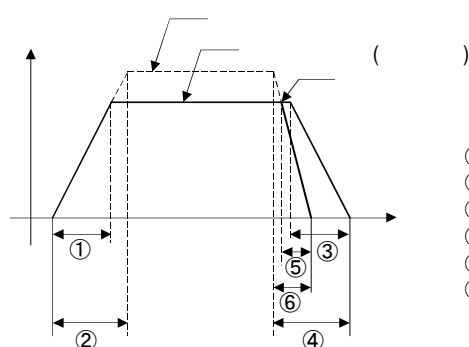
$$2^2, 2^2, \dots, 2^2 \text{ 가}$$
$$\frac{1}{25} [\text{PLS/s}] : 100/200 \times 50 =$$

2 : 50[PLS/s]

$$: \sim \sqrt{25^2 + 50^2} = 55.9 [\text{PLS}]$$


(2)

가 , ,
• 가 , , ,
.



① 가
② 가
③
④
⑤
⑥

(c)

(V₁~3) , CPU가 (:V) ,
(D₁~4)
No. ,

4

1 : D₁=10000 [PLS]
 2 : D₂=15000 [PLS]
 3 : D₃=5000 [PLS]
 4 : D₄=20000 [PLS]
 : V=70000 [PLS/s]
 : 4

4 , 4

CPU가

1 : V₁=D₁/D₄×V
 2 : V₂=D₂/D₄×V
 3 : V₃=D₃/D₄×V

< K 52>

ABS-4

1.	10000	[PLS]
2.	15000	[PLS]
3.	5000	[PLS]
4.	20000	[PLS]
	70000	[PLS/s]
	4	

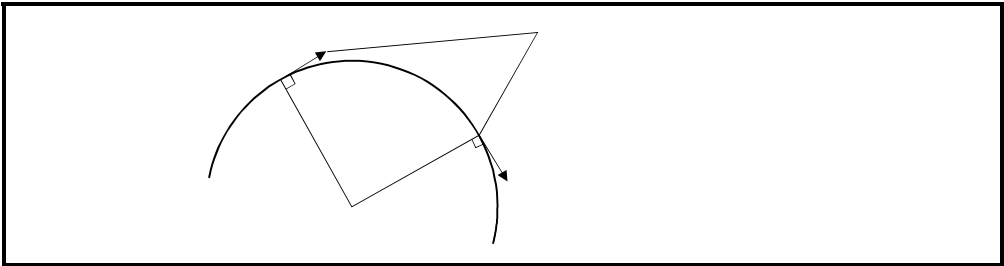
(1)

(2)

(3)

가
가
가
가
가
가

(3) , 가 가 .



6. 1. 3 1

1 , .
(, .)

6. 1. 4

(1) .
,
가 , .

	mm	inch	degree	PLS	
	가[mm],[inch] .		가 [degree] .	가 [PLS] .	
([40])	가				. 가 . 가 , . <div>PLS>degree>inch>mm</div> < > 1000[PLS] 10,000[inch] , 10,000 [inch] 10,000[PLS] .

(2)

<div></div>	mm	inch	degree	PLS
mm	①	②	③	③
inch	②	①	③	③
degree	③	③	①	③
PLS	③	③	③	①

- ① :
- ② : [mm] [inch]
- ③ :

(a) (①)

/ , ,

,

• “degree” ,

“degree” .

(b) [mm] [inch] (②)

• 가 [mm] , [inch] [mm]

[([inch]) × 25. 4] / , ,

• 가 [inch] , [mm] [inch]

([mm]) ÷ 25. 4] / , ,

,

(c) 가 (③)

• 가 ,

a) , [PLS]

b) , ,

[PLS/s]

[PLS] , [PLS/s] ,

,

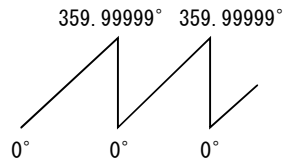
• 3 , 2

No.

6. 1. 5 가 "degree"

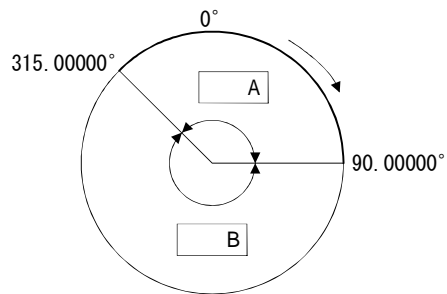
가 "degree"

- (1) "degree" , 0~360 가 .



- (2) / , 0° ~359.99999°

- (a) , →



- ① A , .
 a) 315.00000°
 b) 90.00000°
 ② B , .
 a) 90.00000°
 b) 315.00000°

- (b) 「 () = (,) 」 .

가 .

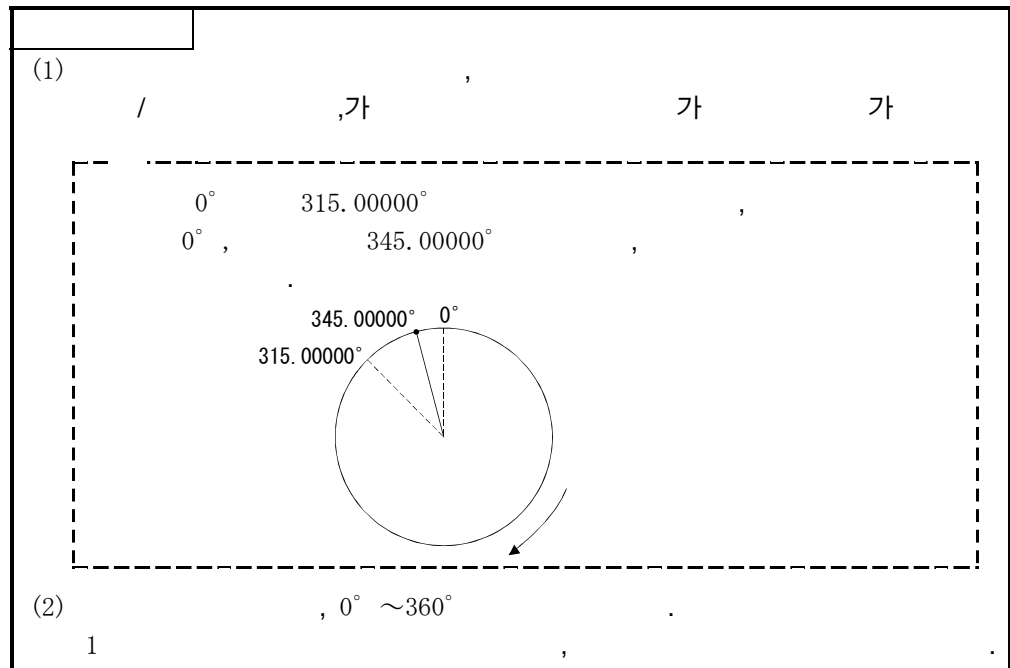
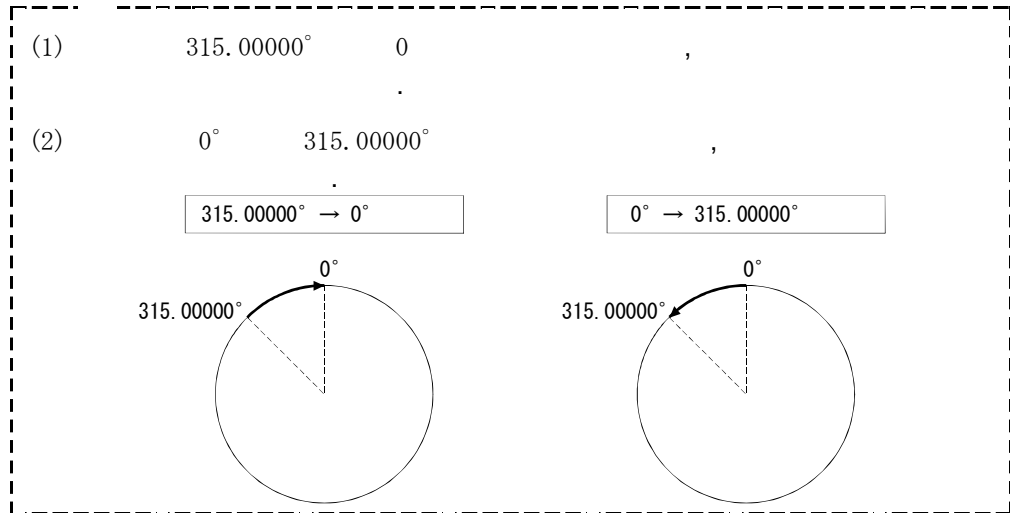
(1)	가
(2)	, / ,
(3)	, .

(3)

가 "degree"

(a) (ABS□)

가



(b) (INC□)

- ①
②

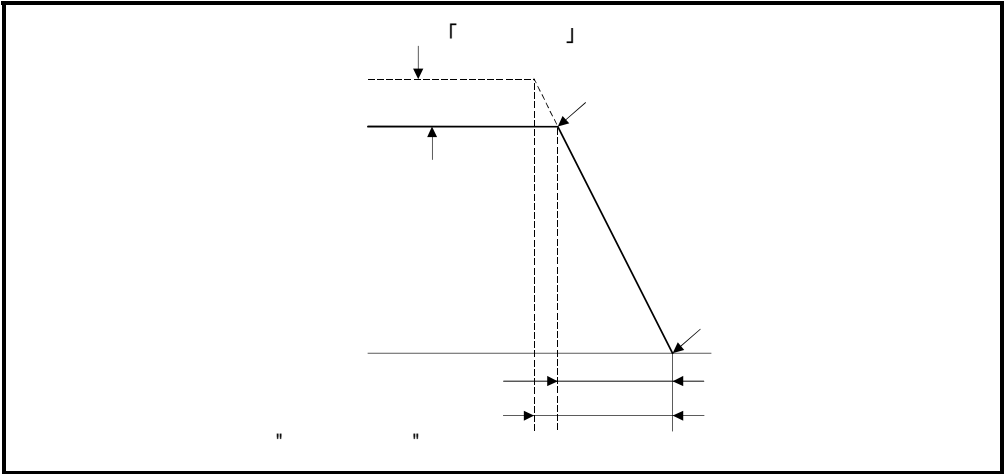


6. 1. 6

(1)
(a)

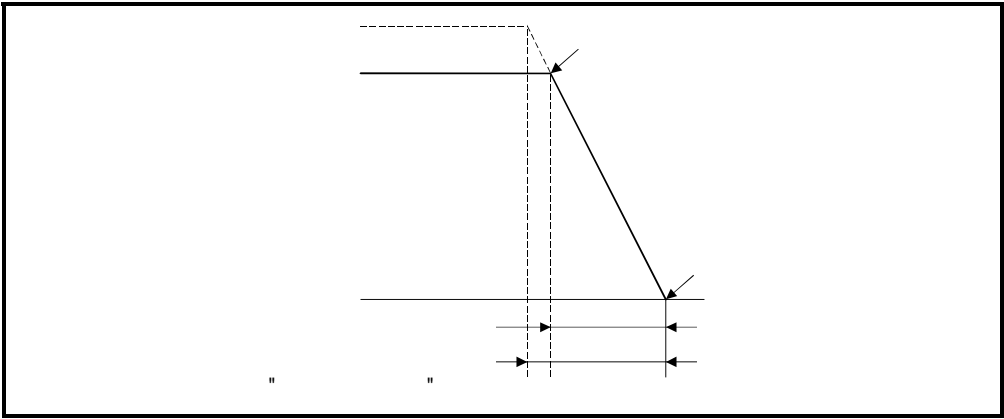
①

(1)



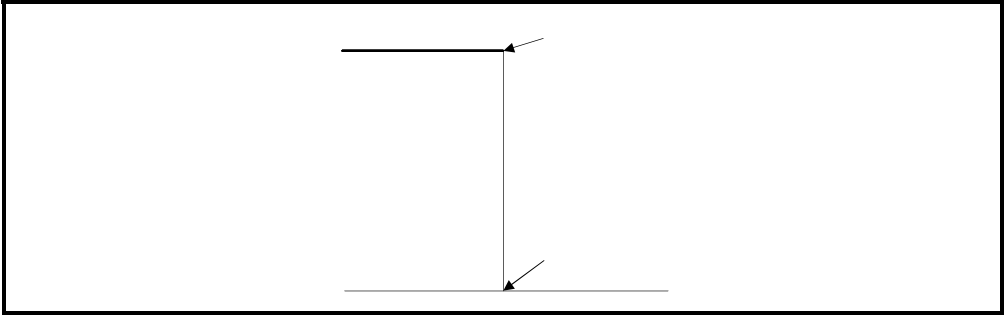
②

(2)



③

(3)



④

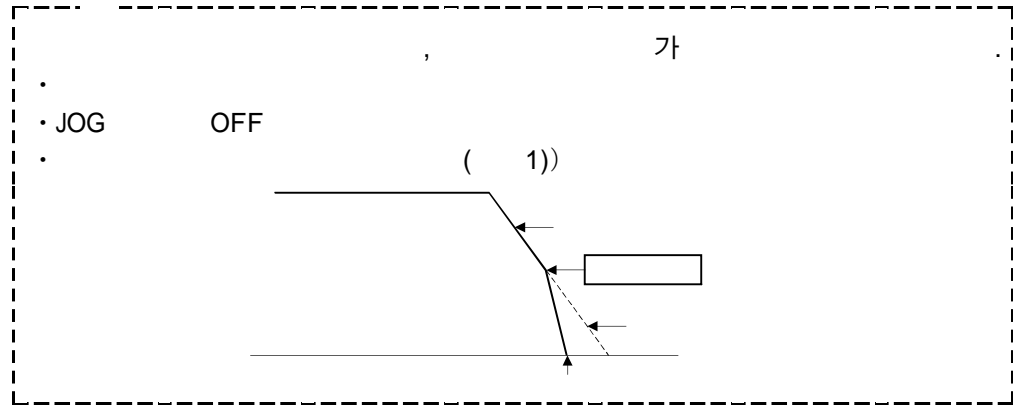
(4)

(+1) × 56.8[ms]

() TEL : 02-3660-9531

(b)

$$(1) < (2) < (3)$$



(c)

1 2 ()

1 2

No.								
					JOG			
1	Q172LX (STOP) ON		1 2 [“STOP ”]				4	1 CPU가 .
2	M3200+20n ON		1					
3	M3201+20n ON		2					
4	Q172LX FLS OFF		1 2 [“STOP ”]					
5	Q172LX RLS OFF							
6	M2408+20n ON		3					
7	PLC M2000 OFF		1				4	
8	*2		1					
9	*2		2					
10	CPU STOP		1					
11	CPU		3*1				—	
12	PCPU WDT		3*1				M9073(PCPU WDT —)ON	
13	CPU WDT		1				—	
14	CPU OFF		3*1				—	
15			3				가 OFF가	
16	OFF			3*1				가 . ()
17	0	*3	1			-	-	

*1 : H/W

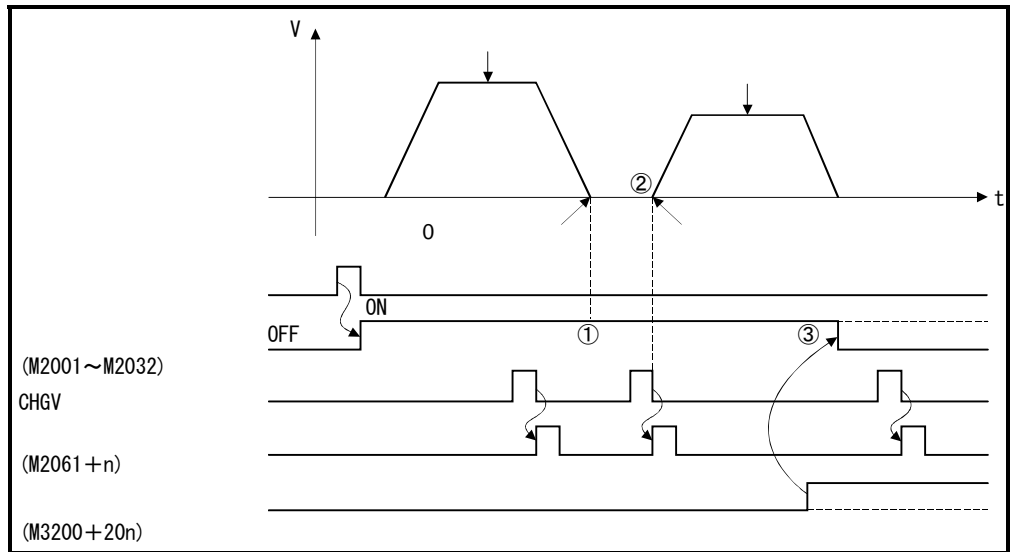
*2 :

*3 : 0

가

(2)

- (a) , (0) ,
가 .
 , Q172LX (STOP) ON,
(M3200 + 20n) ON, (M3201 + 20n) ON ,
VSTART 가 .
- (b) CHGV 0 , 0



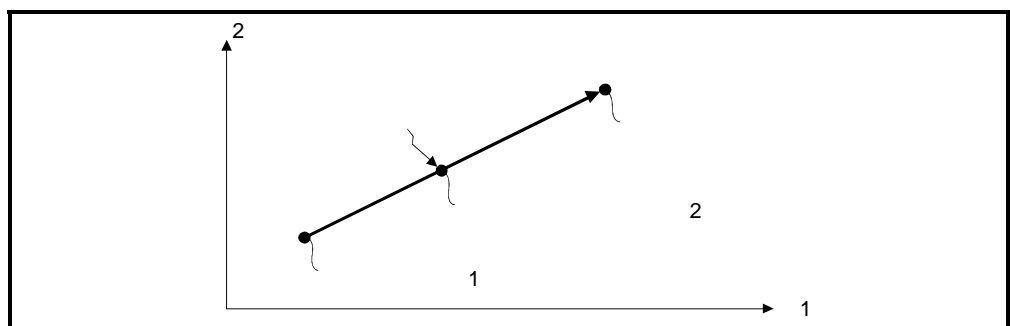
- ① 0 , M2001 ~ M2032
ON .
- ② , ,
- ③ , M3200 + 20n ON , M2001 ~
M2032 OFF ,

(3)

Q172LX (STOP) ON, (M3200 + 20n) ON,
(M3201 + 20n) ON , No.

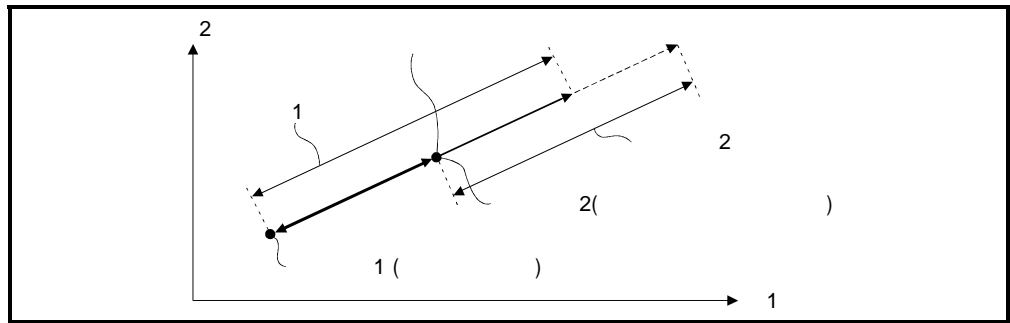
- (a) 1 , 2 /3
① ABS□

가 가



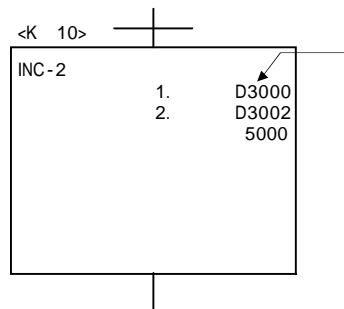
6.

② INC□



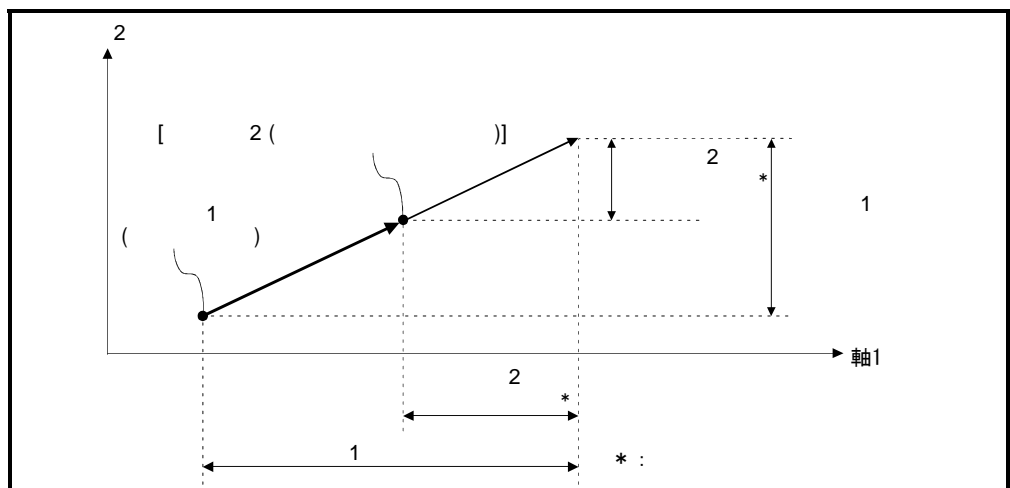
2 INC□ (+ SFC
) 가

【 】



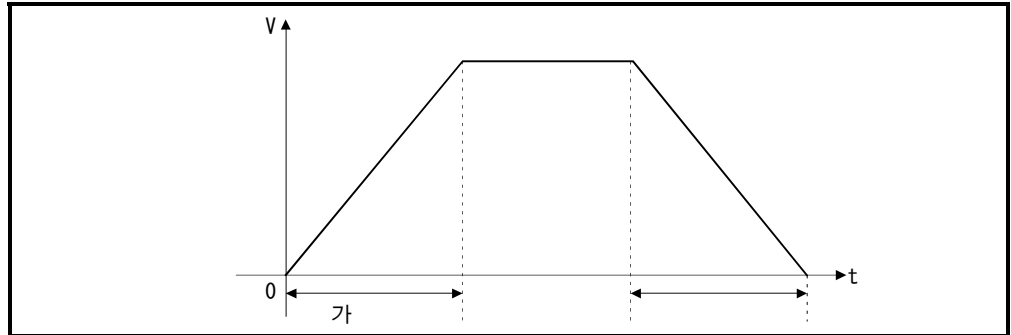
【 SFC 】

1. CPU
- 2.
- 3.
- 4.
- 5.

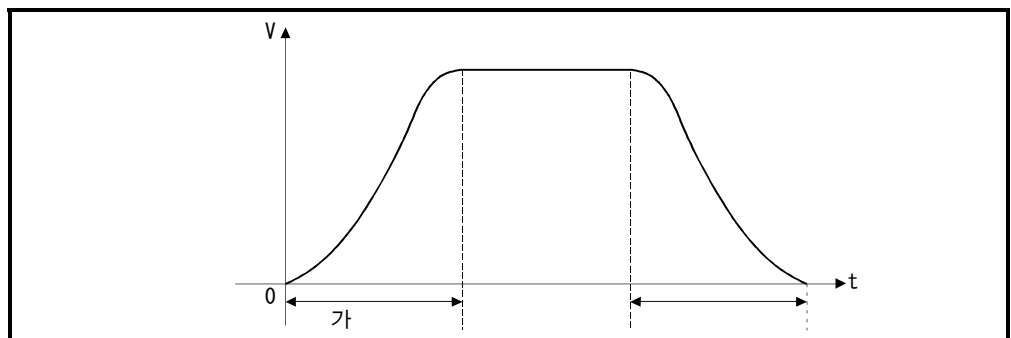


6.1.7 가

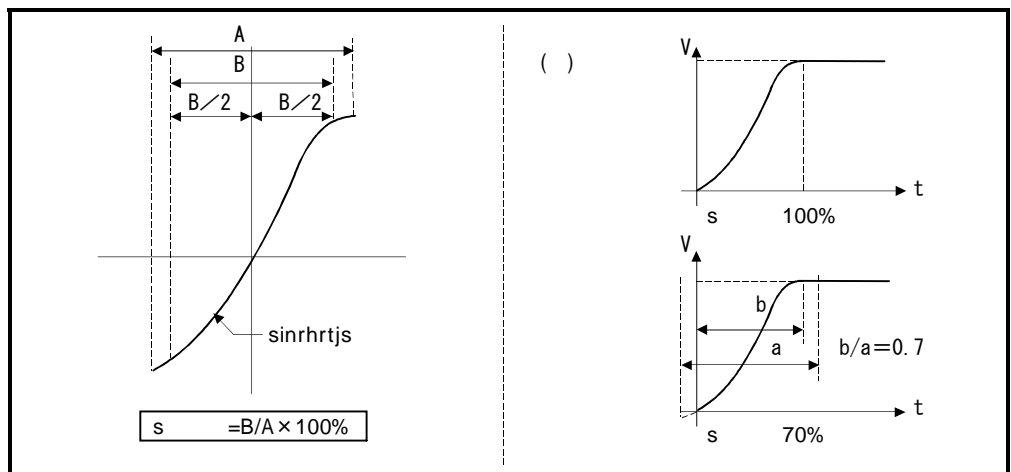
가 , 2 가 .
 (1) 가 , 가 , 가 , .



(2) S 가 S , 가 sin S , (4.4.2)



S , sin 가



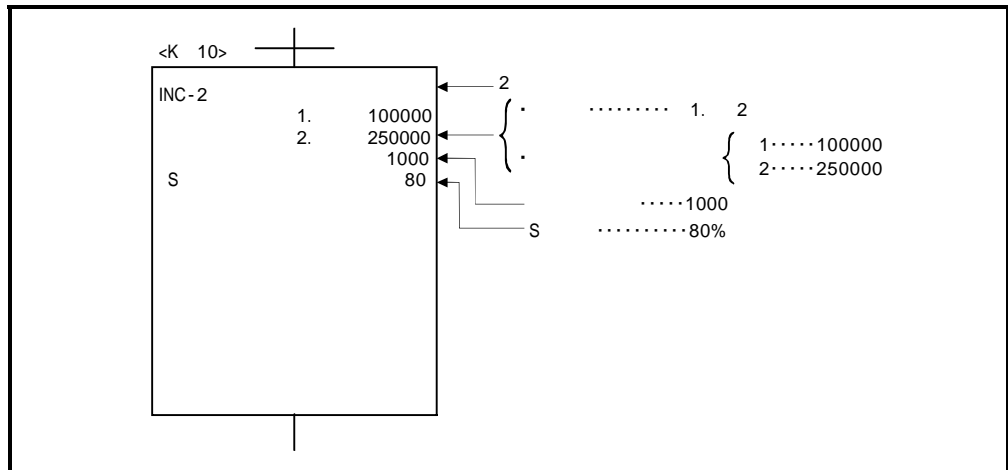
S

2 가

(a)

S

(0~100)

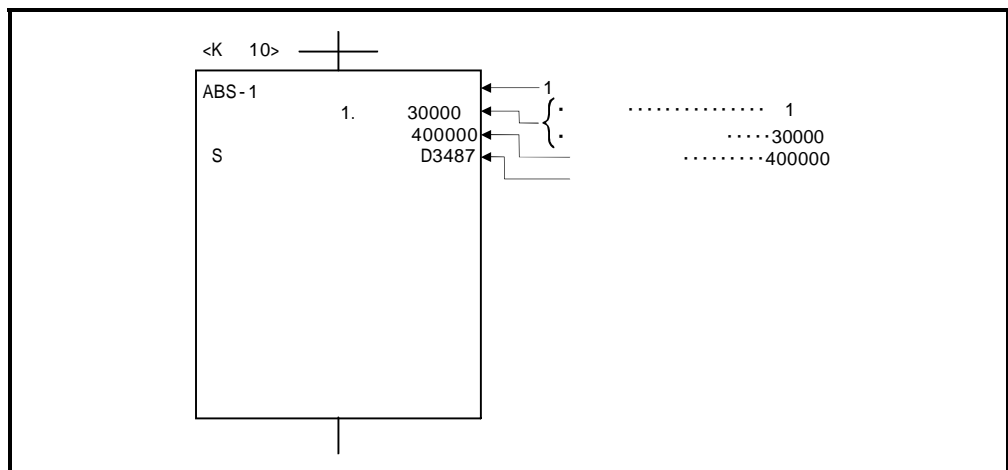


(b)

S

가

	가
D	800~8191
W	0~1FFF
#	0~7999



○ :
△ :

ABS-1 ()

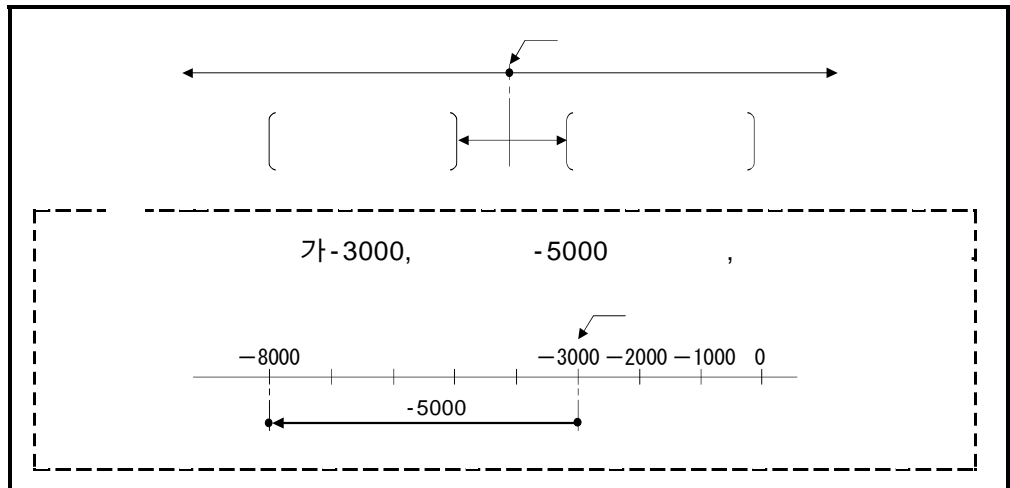


INC-1 ()

(1)

(2)

(+/-)
 (가)
 ()



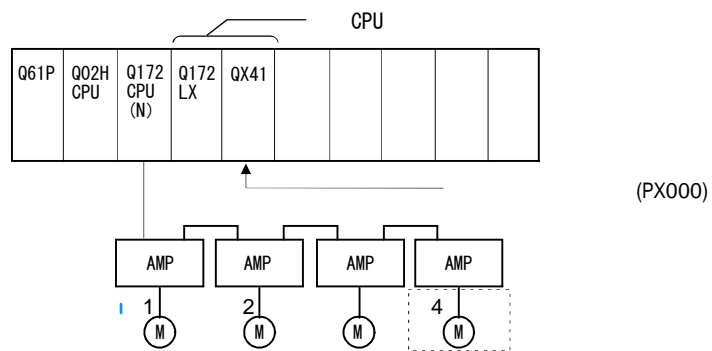
6.2

[]

No.0

(1)

4 1



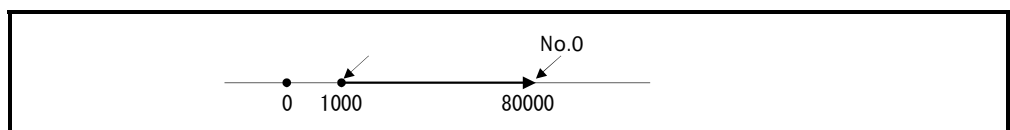
(2)

No.0

No.0

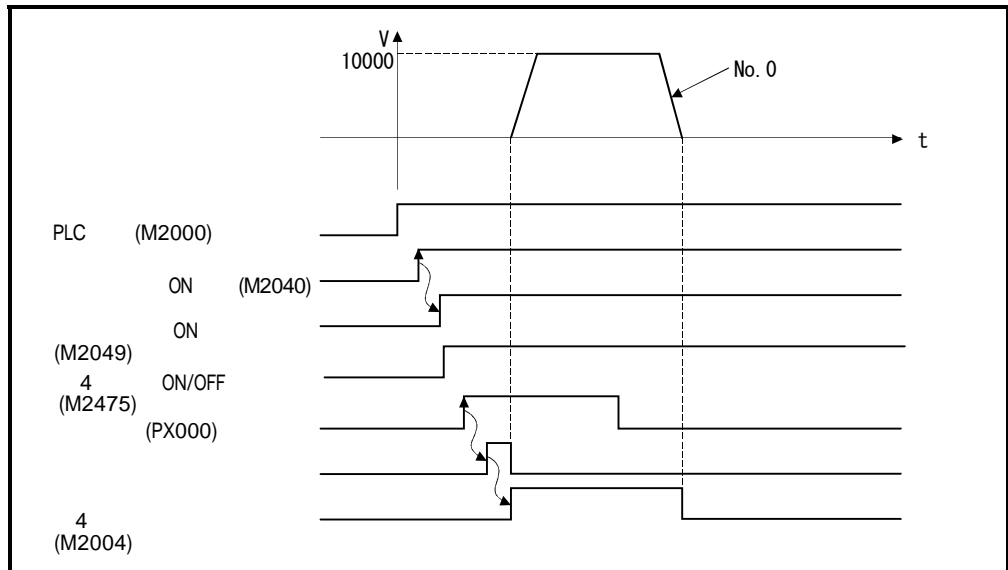
4

가



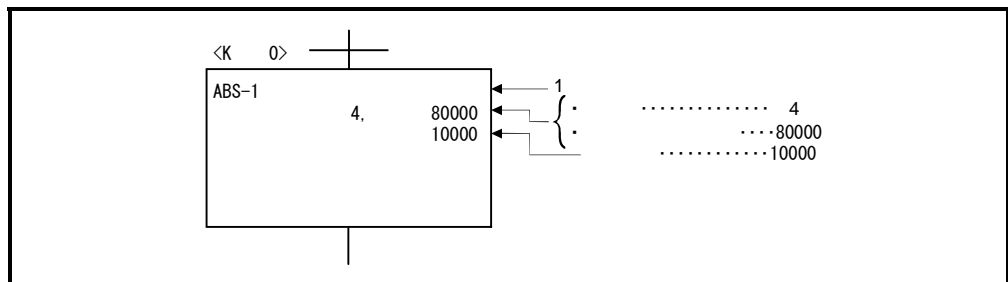
(3)

No.0



(4)

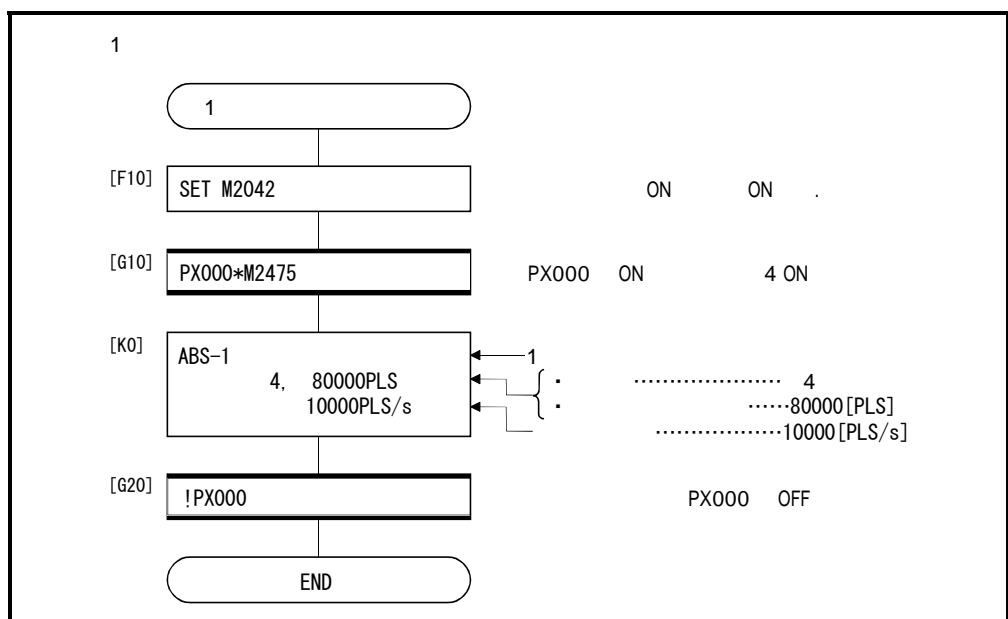
No.0



(5)

SFC

SFC



* : SFC /PLC

6.

6.3 2

2 2 , ABS-2 () INC-2 ()

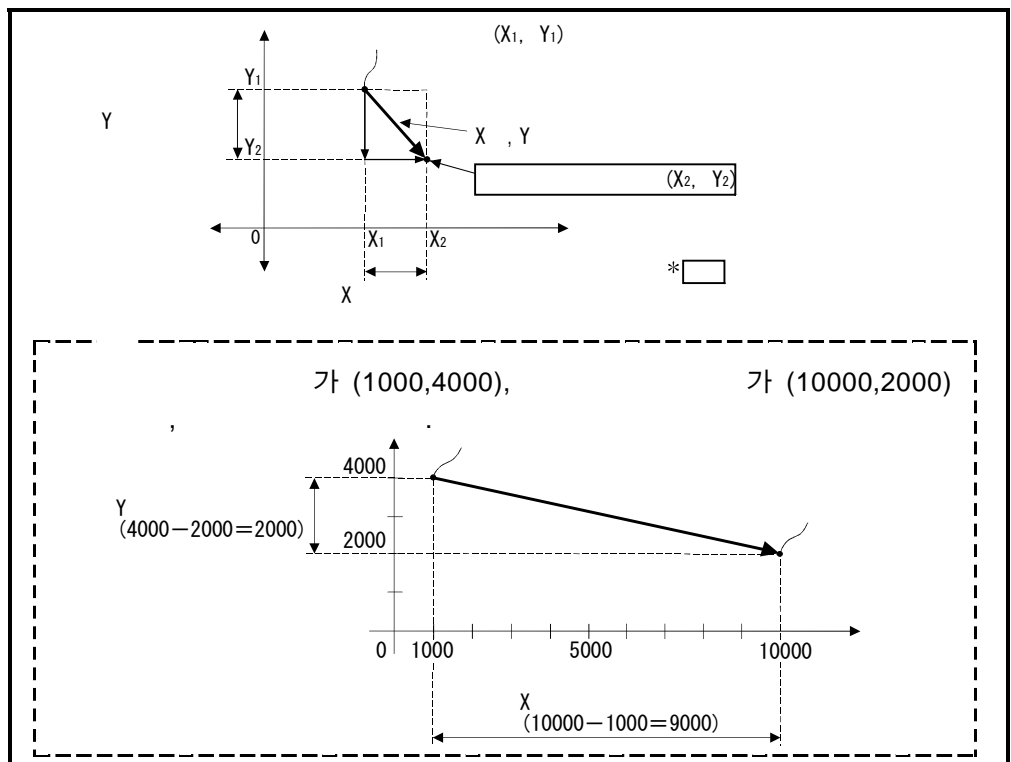
			No.		/			M						가			STOP	S	WAIT TION /OFF				
ABS - 2		2	△	○	○	○	△	△						△	△	△	△	△	△	△	△		가
INC - 2																							

○ :
△ :

[]

ABS-2 ()

- (1) (X_1, Y_1) ,
 (X_2, Y_2) 2 .
- (2) , ()



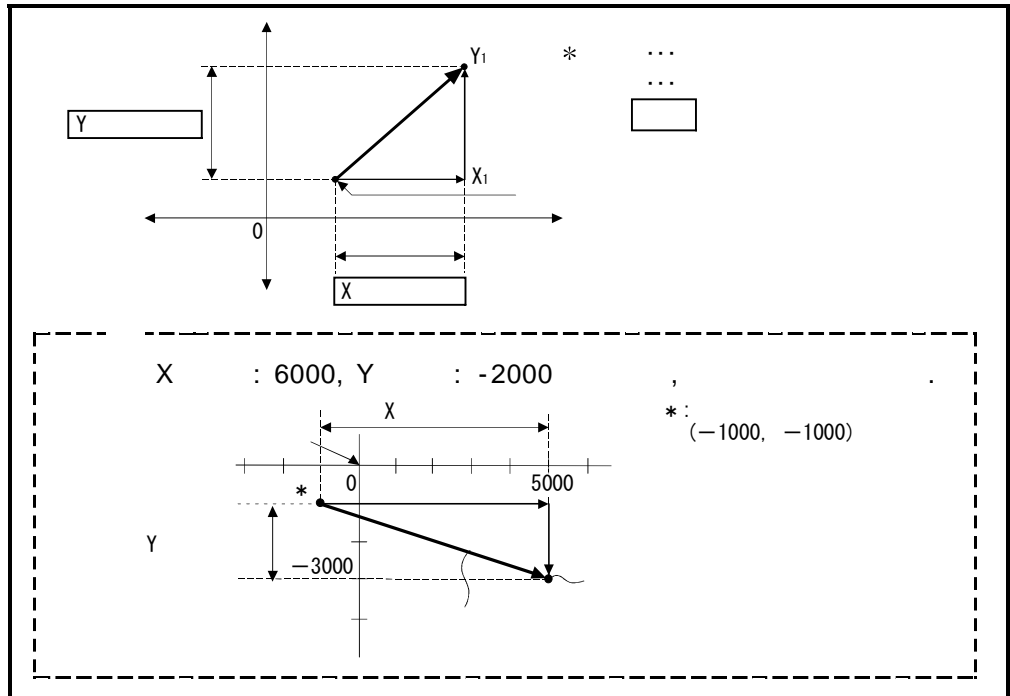
6.3

() TEL : 02-3660-9531

6 - 19

INC-2 ()

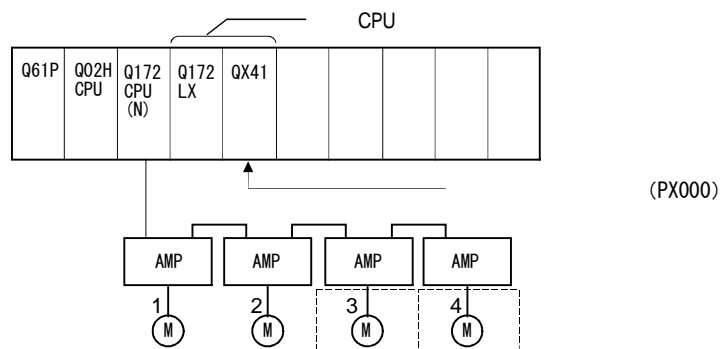
- (1) , .
- (2) , .
- (가)
 - ()



6.4

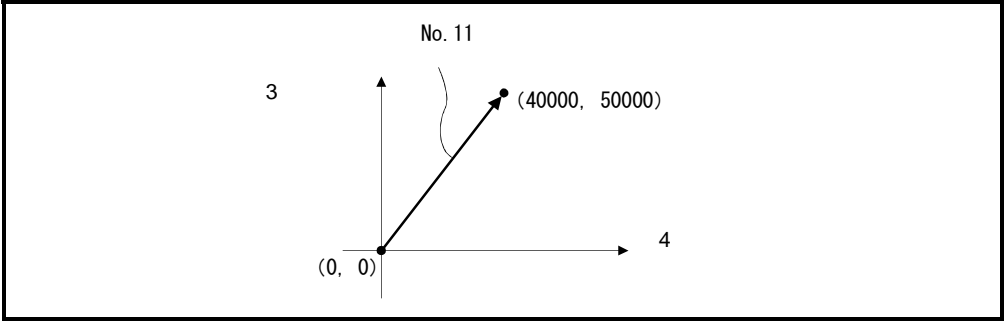
[]

- 2 , .
- (1) 3 4 2 .



(2)

3, 4
3, 4



(3)

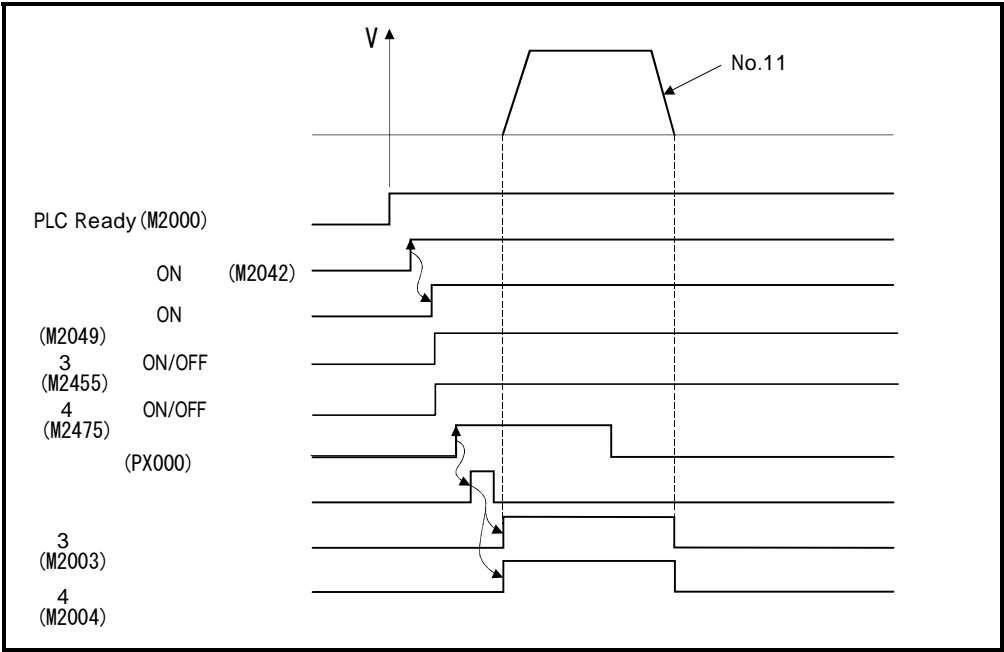
(a)

	No.
	No.11
	30000

(b) PX000 (OFF→ON)

(4)

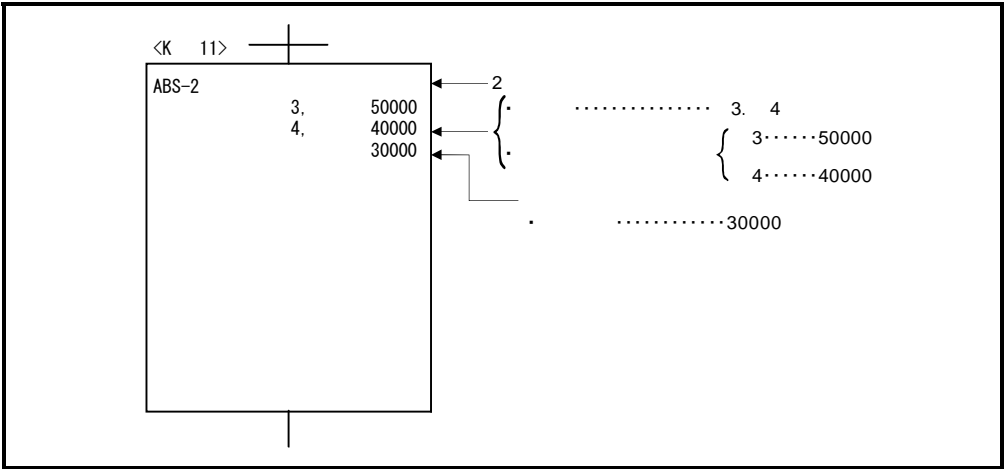
2



(5)

2

No. 11

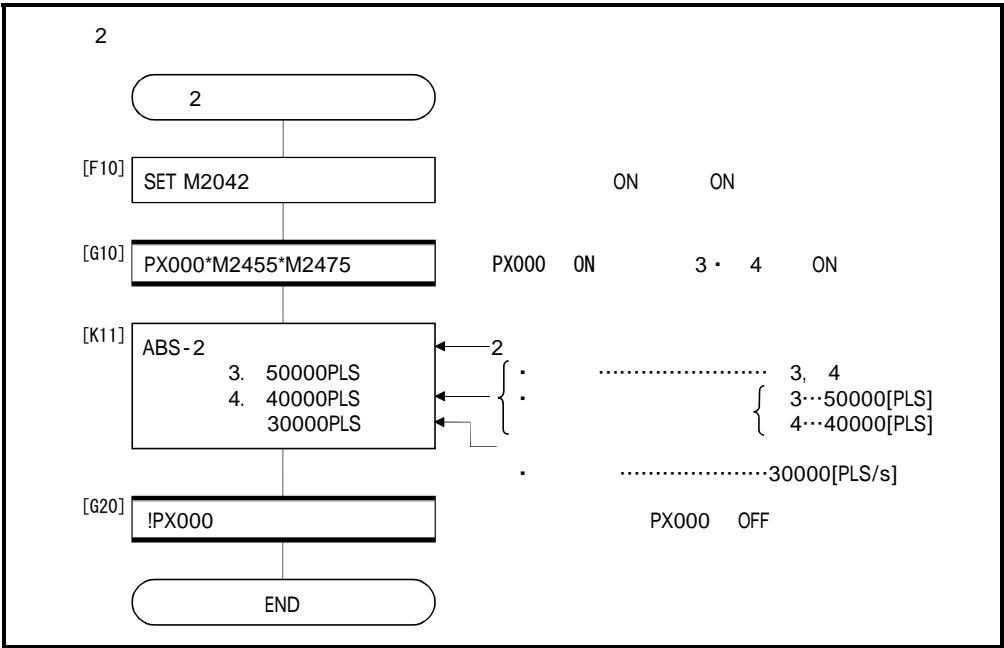


(6)

SFC

SFC

2



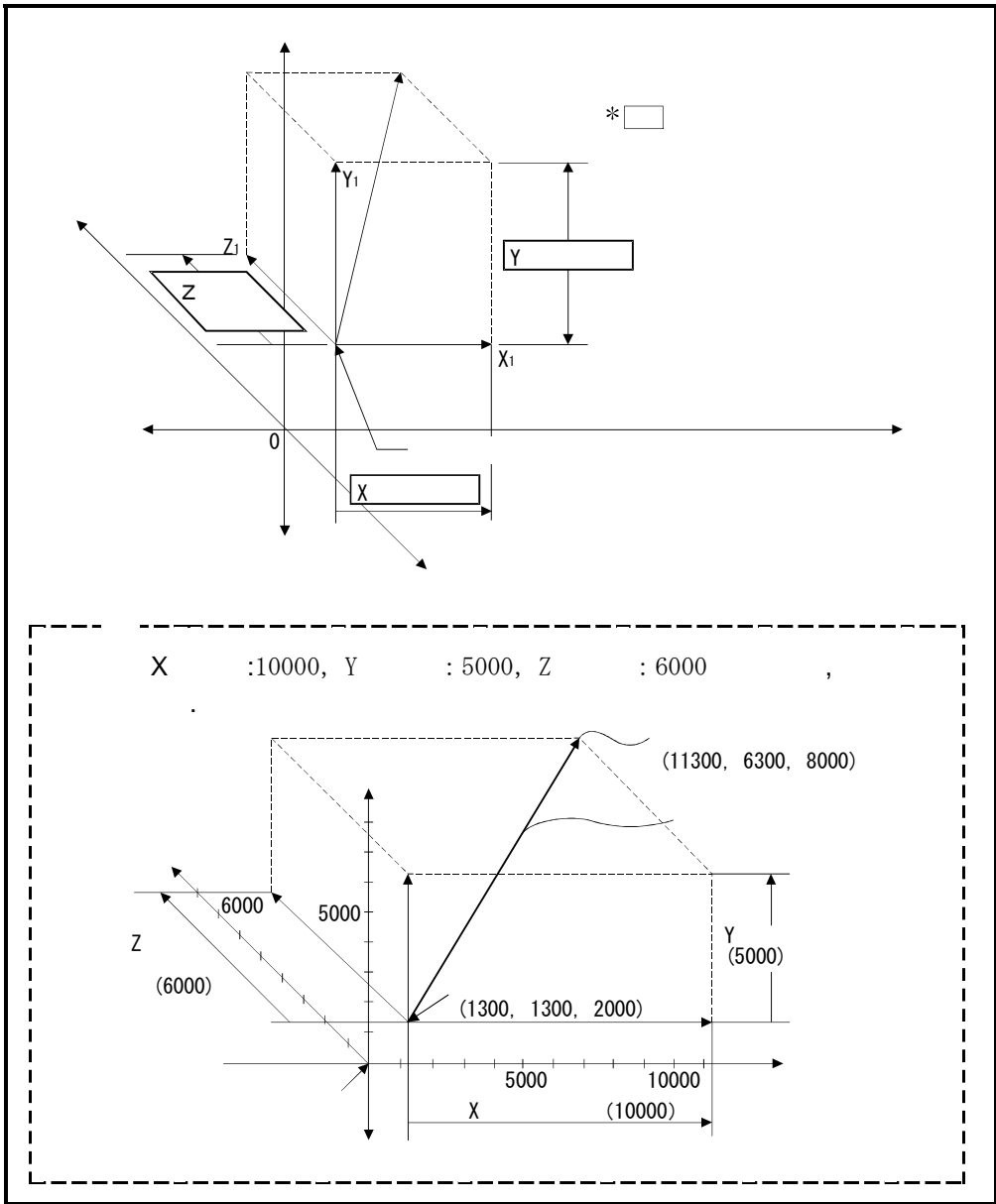
* : SFC

/

INC-3 ()

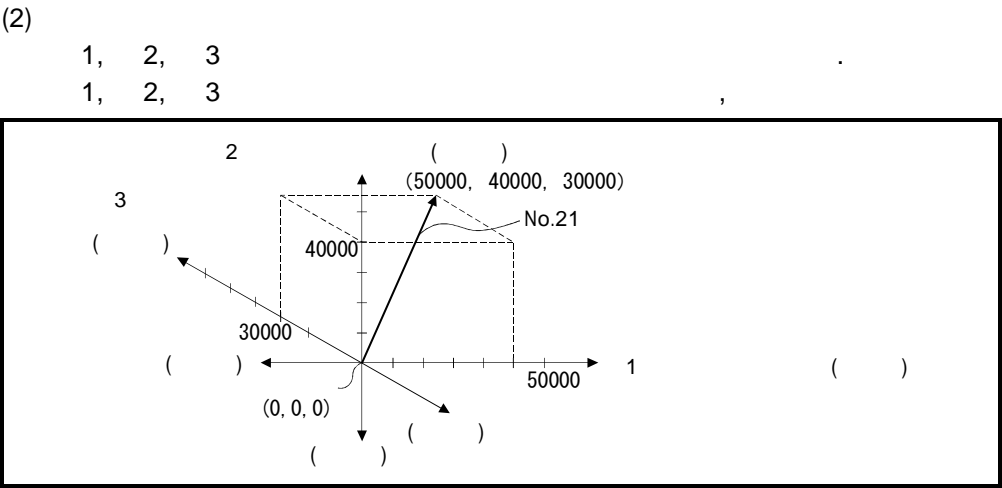
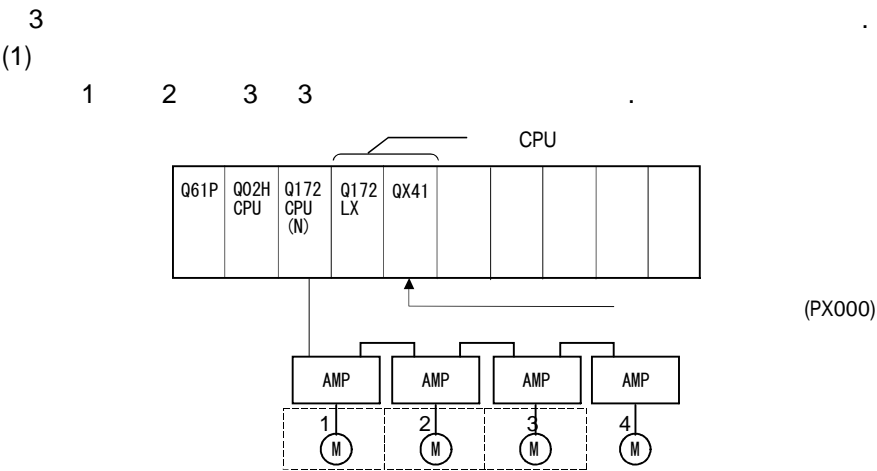
(1) ,
 흥남 .

(2) ,
 .
 . : (가)
 . : ()



6.6

[]



(3)

(a)

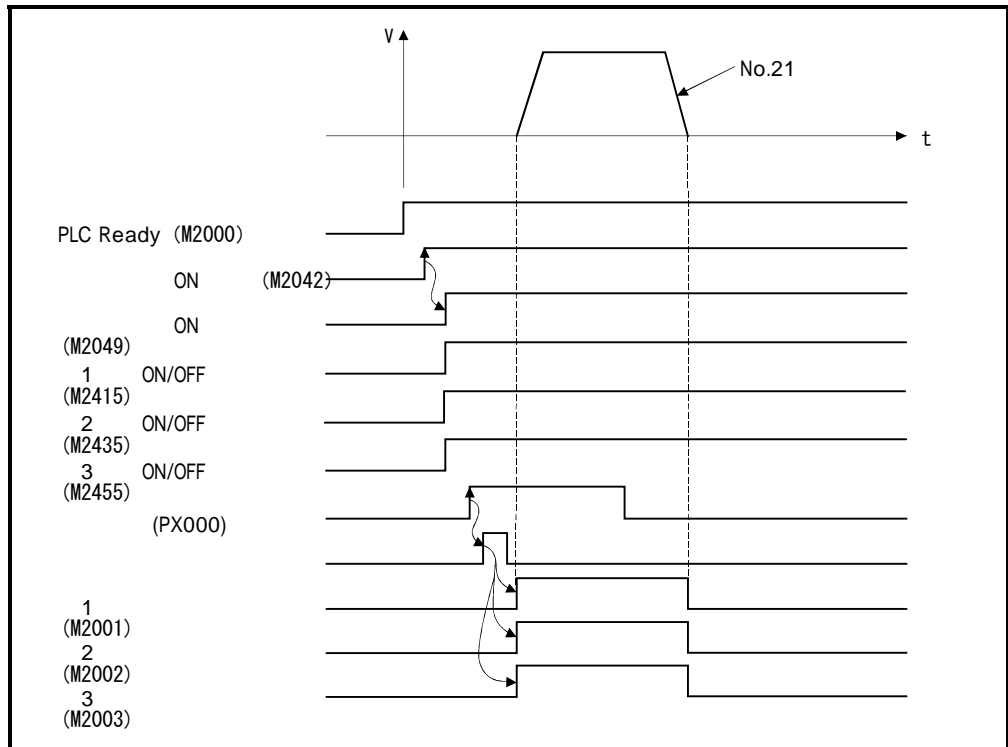
	No.
	No. 21
	1000

(b)

..... PX000 (OFF →ON)

(4)

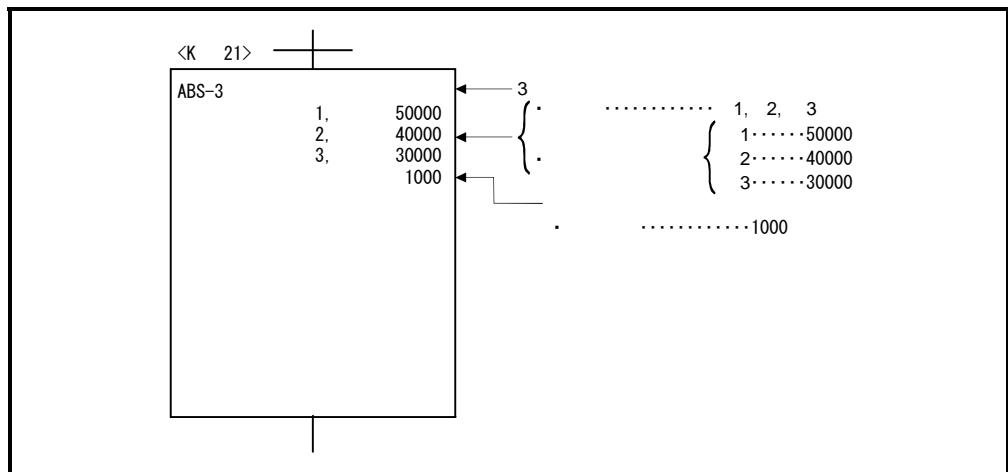
3



(5)

3

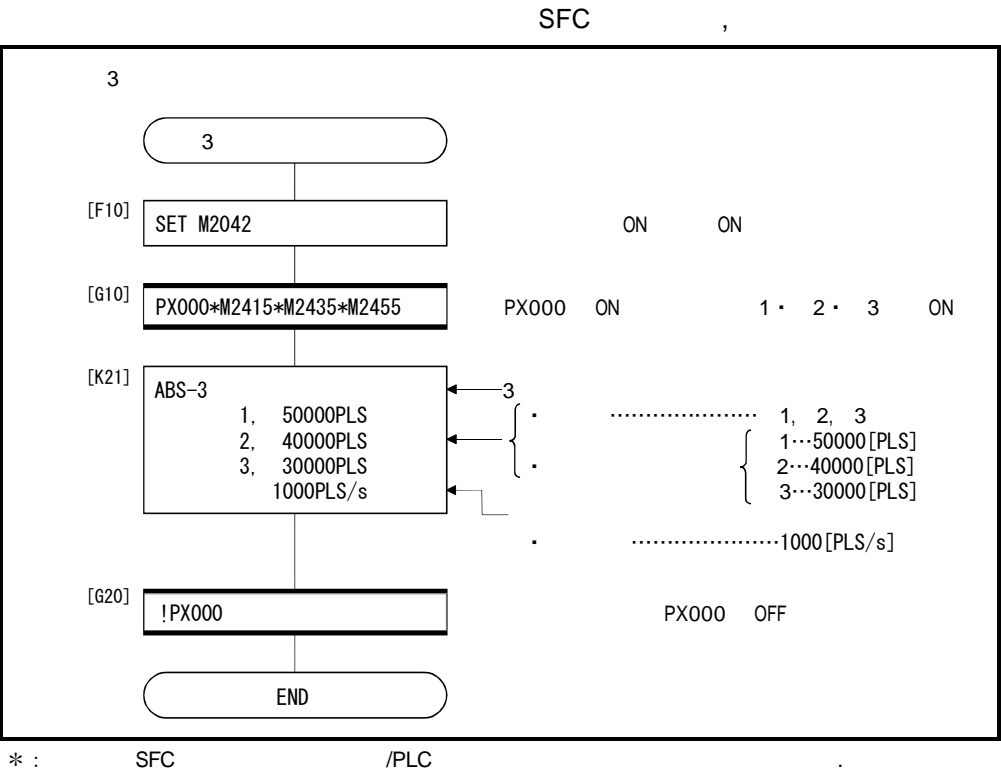
No.21



*:

SFC

(6) SFC



4 ,

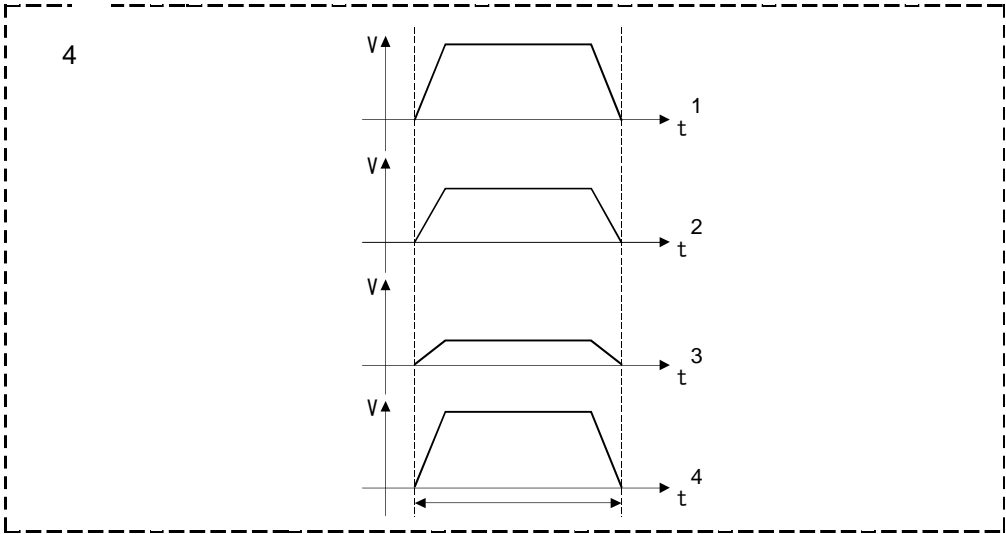
4

			No.	/				M						가				S T O P		S	W A I T I O N / O F F	
ABS - 4		4	△	○	○	○	△	△					△	△	△	△	△	△	△	△		가
INC - 4																						

○ :
△ :

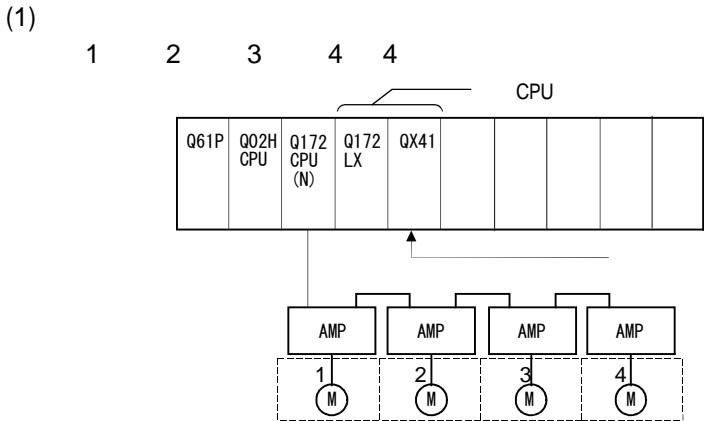
[]

4



[]

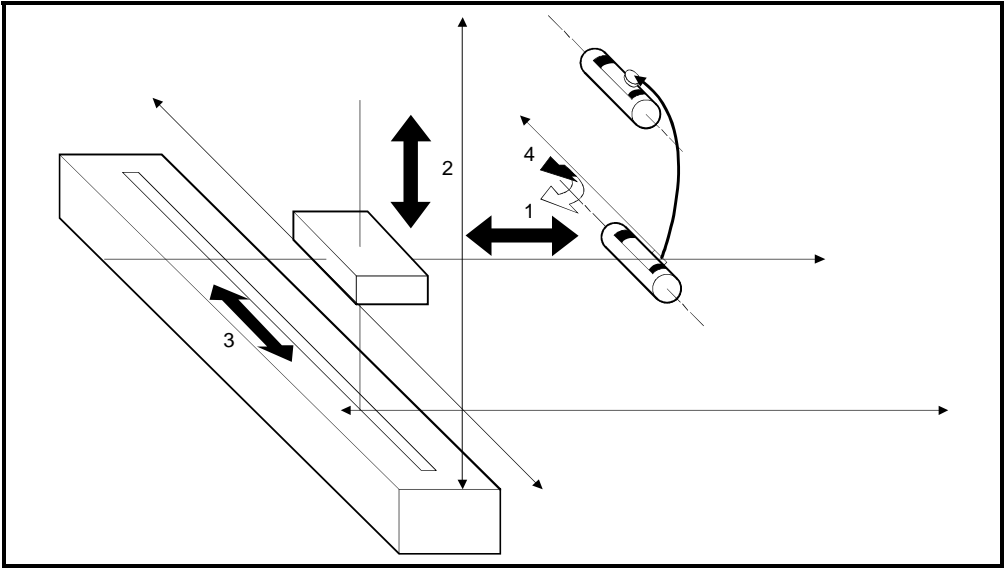
4



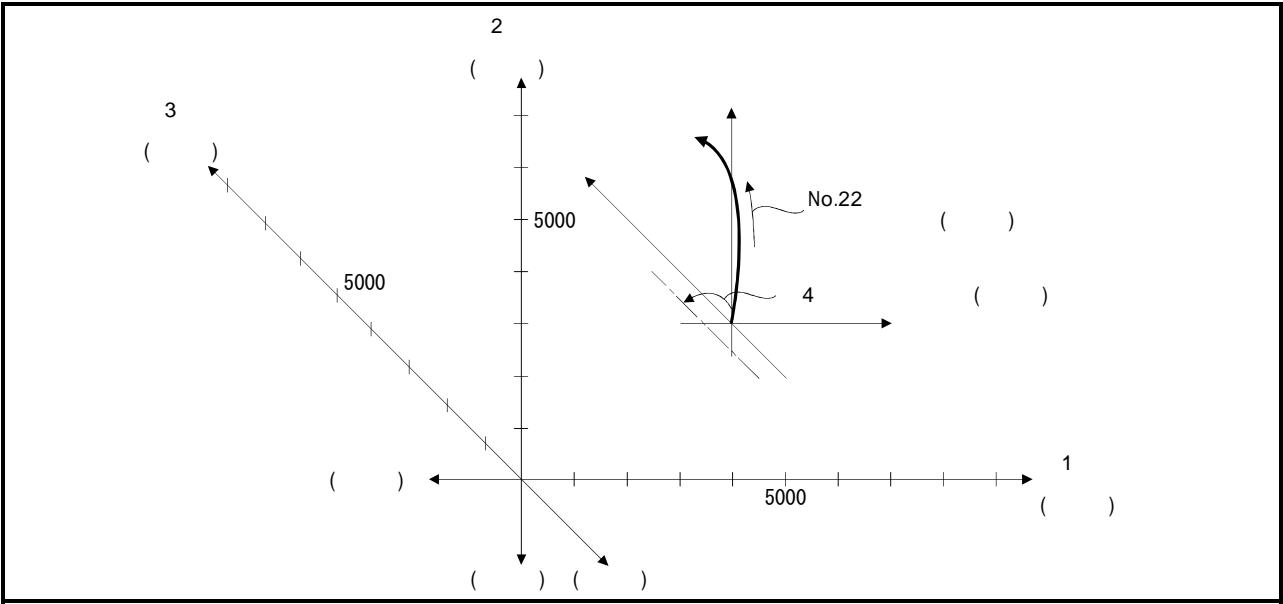
(PX000)

(2)

- 1, 2, 3, 4
- 1, 2, 3, 4



6.7



6.8 4

(3)

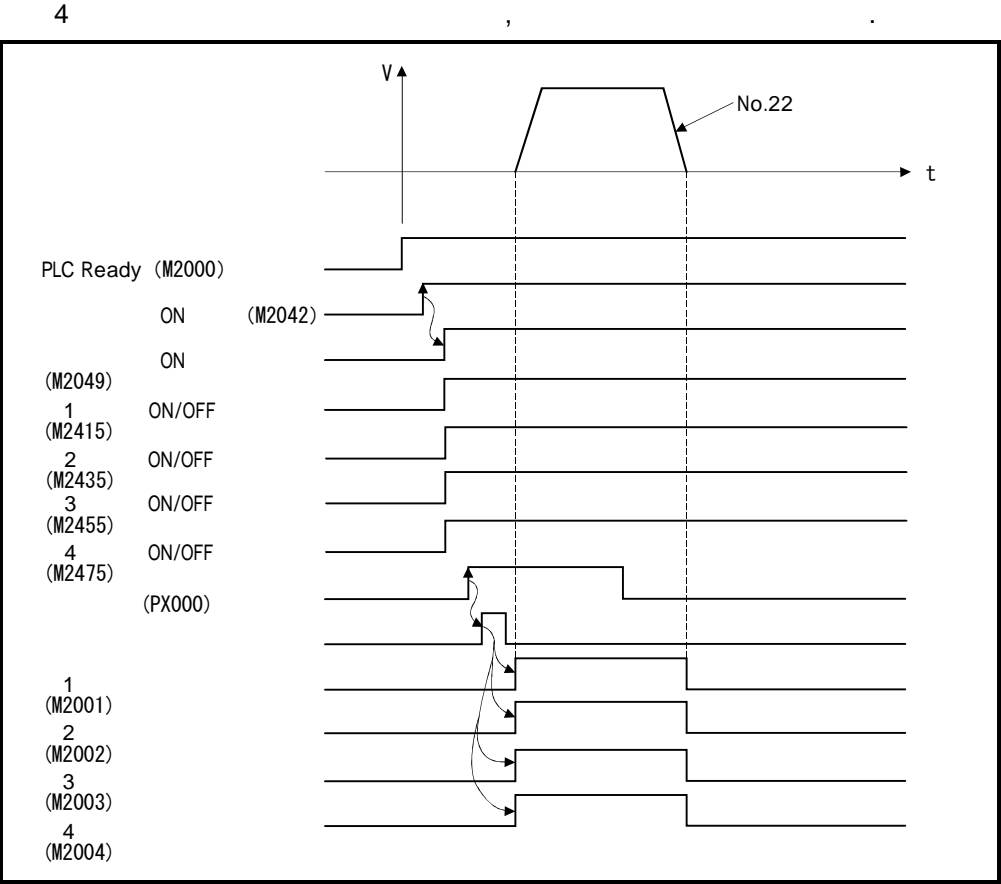
(a)

	No.
	No. 22
	10000

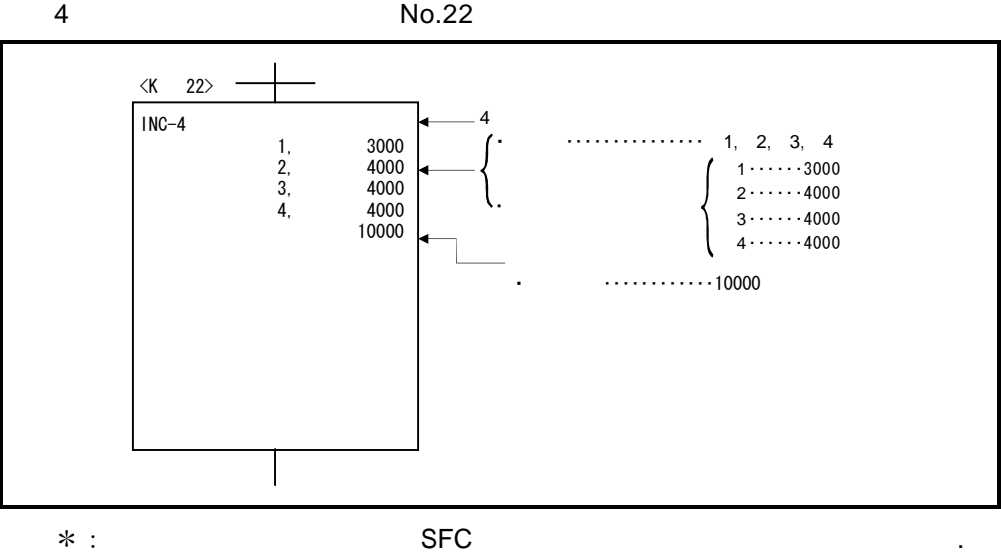
(b)

..... PX000 (OFF→ON)

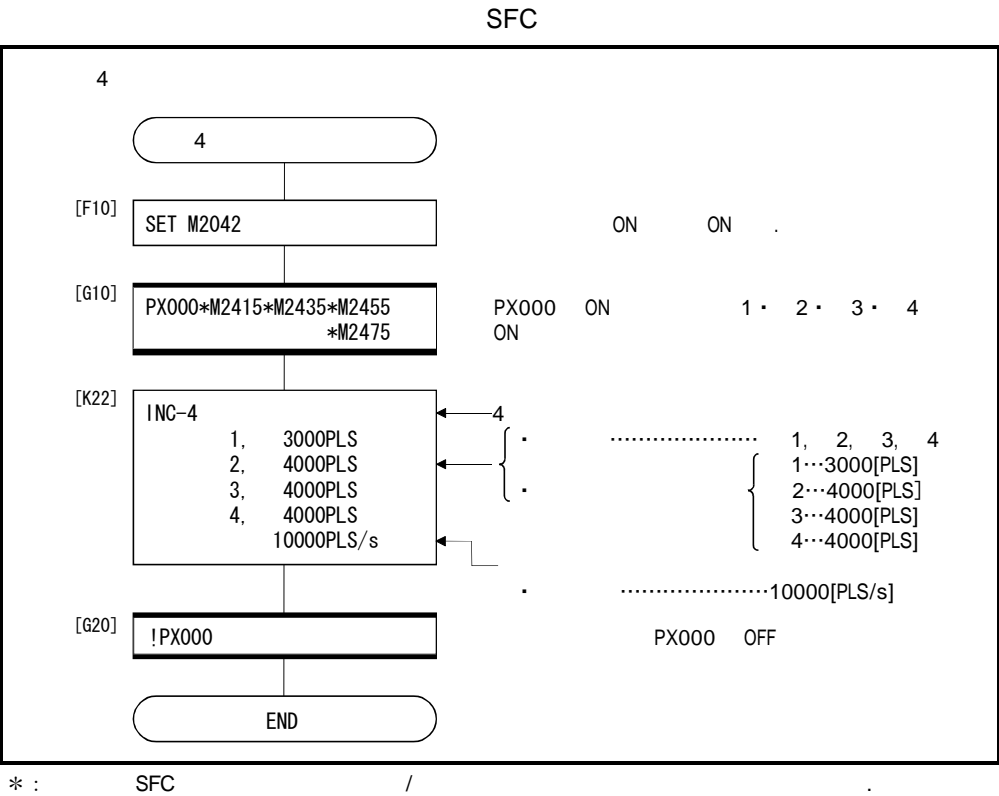
(4)



(5)



(6) SFC





6. 6

()

ABS () INC

()

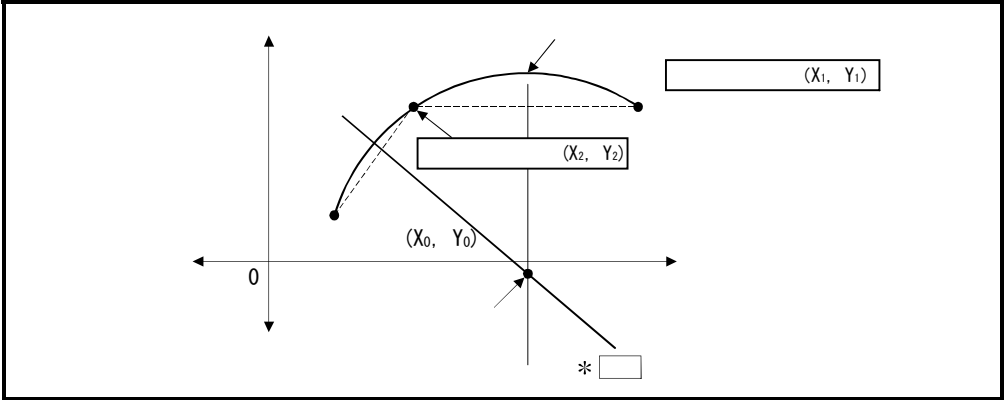
			No.		/			M						가			S T O P	S	W A I T I O N / O F F				
ABS 		2	△	○	○	○	△	△		○				△	△	△	△	△	△	△	△		가
INC 																							

○ :
△ :

[]

ABS ()

- (1) () ,
- (2) () , 가 .

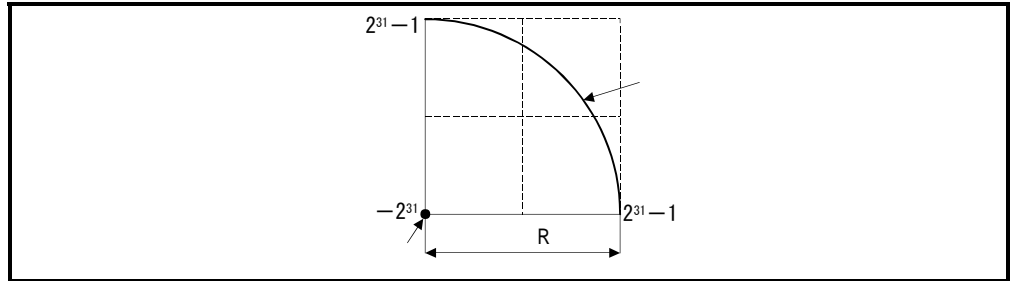


6.9

6.

(3) , , $-2^{31} \sim 2^{31}-1$

(4) , $2^{32}-1$.

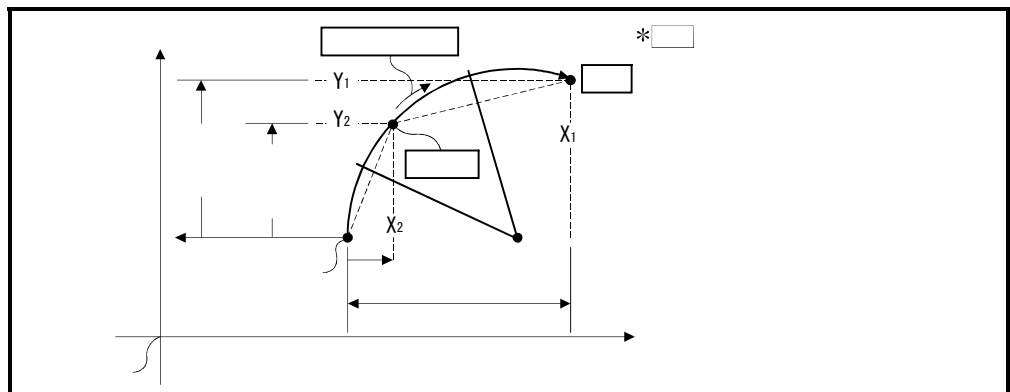


6.10

INC \angle ()

(1) ,

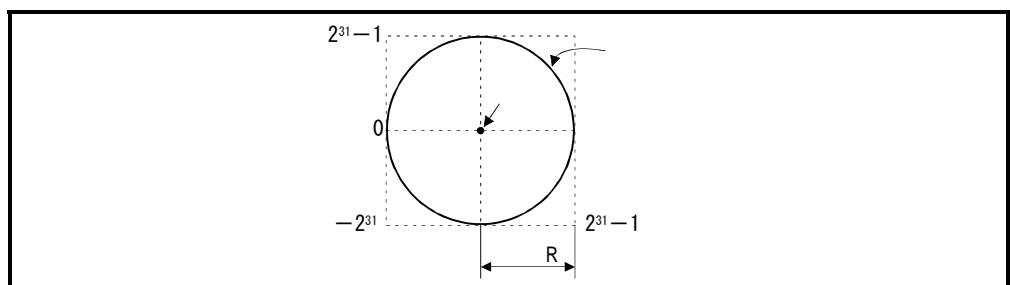
(2) () ,
가 .



6.11

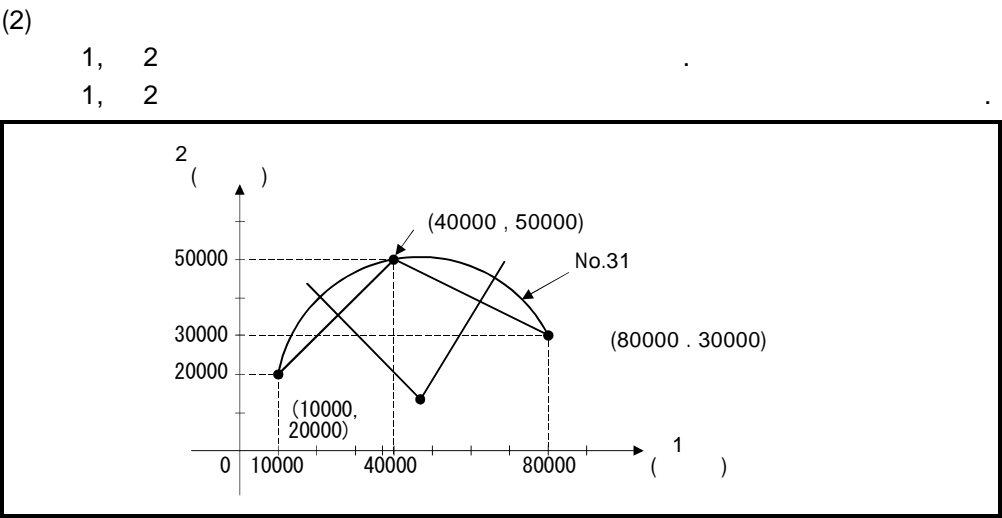
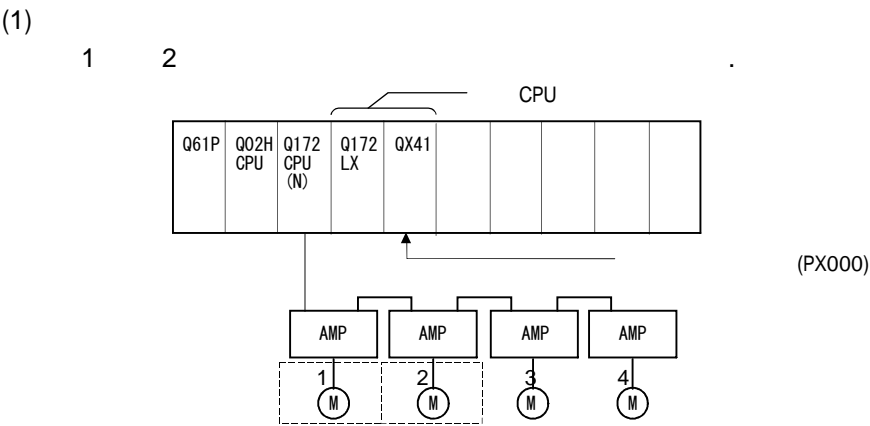
(3) , , $0 \sim \pm(2^{31}-1)$.

(4) , $2^{31}-1$.
 $2^{31}-1$, , 가
[107]가 .



6.12

[]



(3)

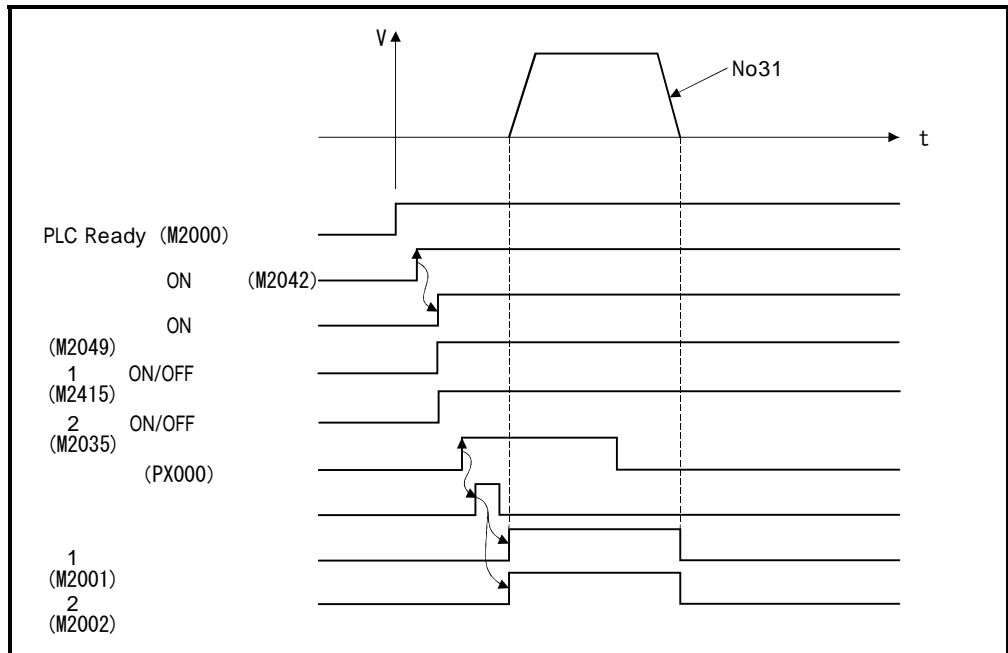
(a)

	No.
	No. 31
	1000

(b)

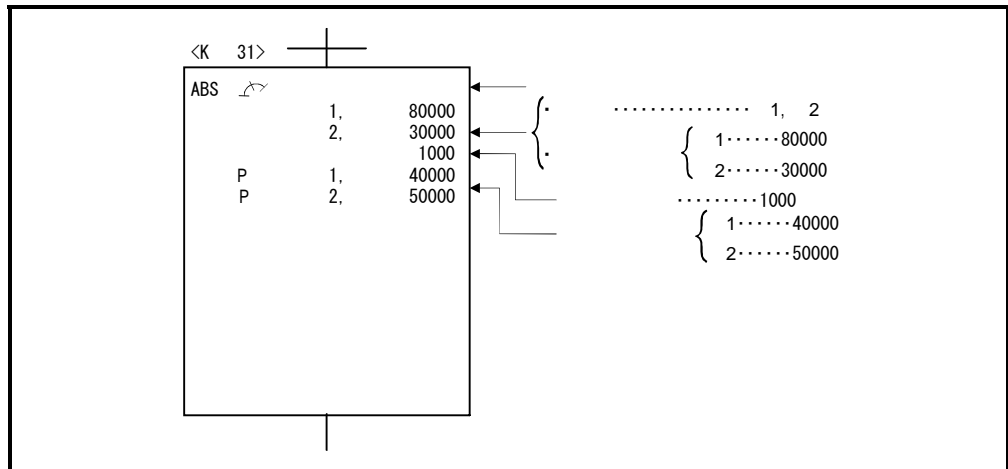
..... PX000 (OFF →ON)

(4)



(5)

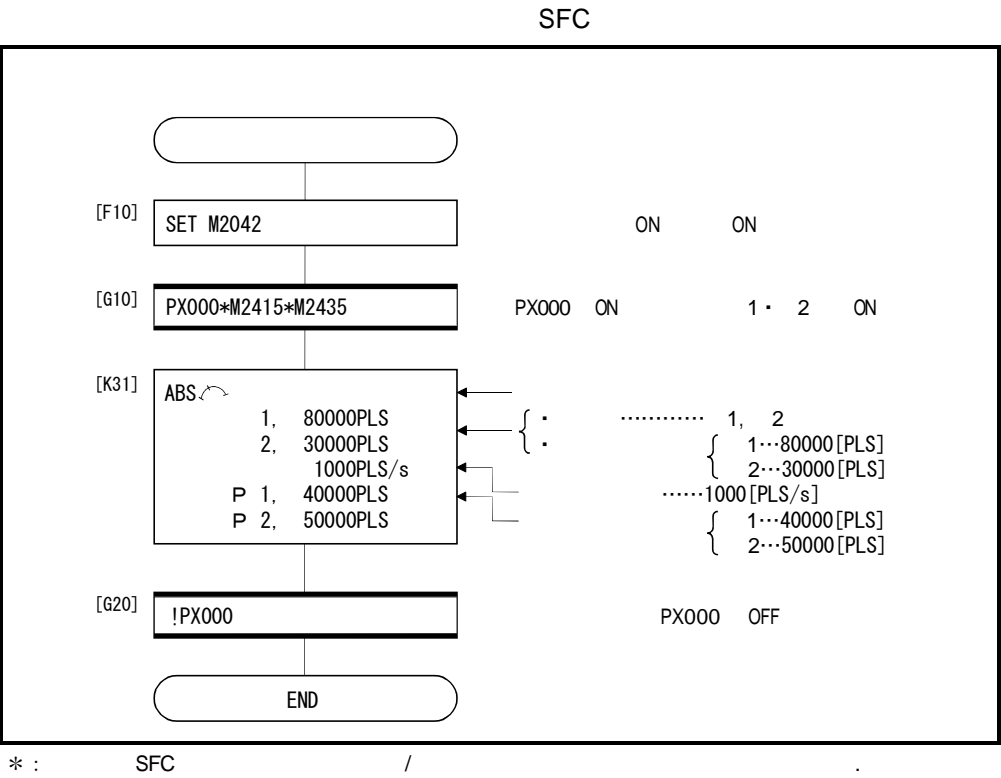
No.31



* :

SFC

(6) SFC



6.

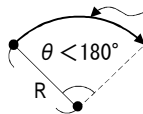
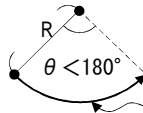
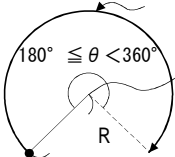
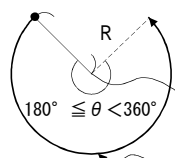
6.7

ABS↶, ABS↷, ABS↶↷, ABS↷↶ () INC↶, INC↷, INC↶↷, INC↷↶ ()

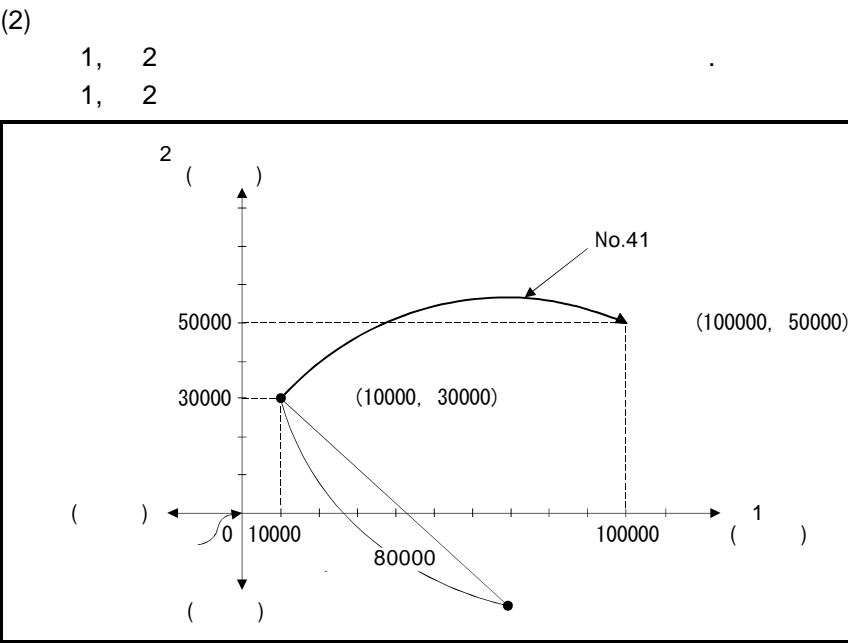
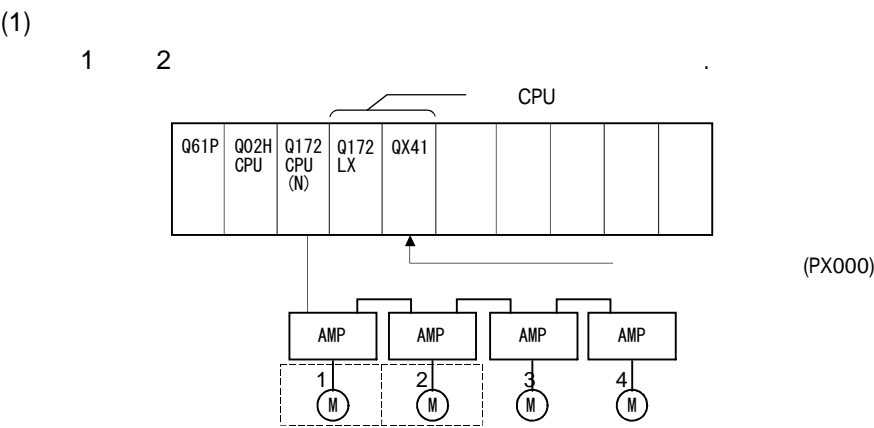
			No.	/			M					가			S T O P	S	W A I T I O N / O F F				
ABS ◀		2	△	○	○	○	△	△		○		△	△	△	△	△	△	△	△	가	
ABS ▶																					
ABS ◀▶																					
ABS ▶◀																					
INC ◀																					
INC ▶																					
INC ◀▶																					
INC ▶◀																					

○ :
△ :

[]

		가	
ABS ↶		$0^{\circ} < \theta < 180^{\circ}$	
INC ↶			
ABS ↶			
INC ↶			
ABS ↷		$180^{\circ} \leq \theta < 360^{\circ}$	
INC ↷			
ABS ↷			
INC ↷			

[]



(3)

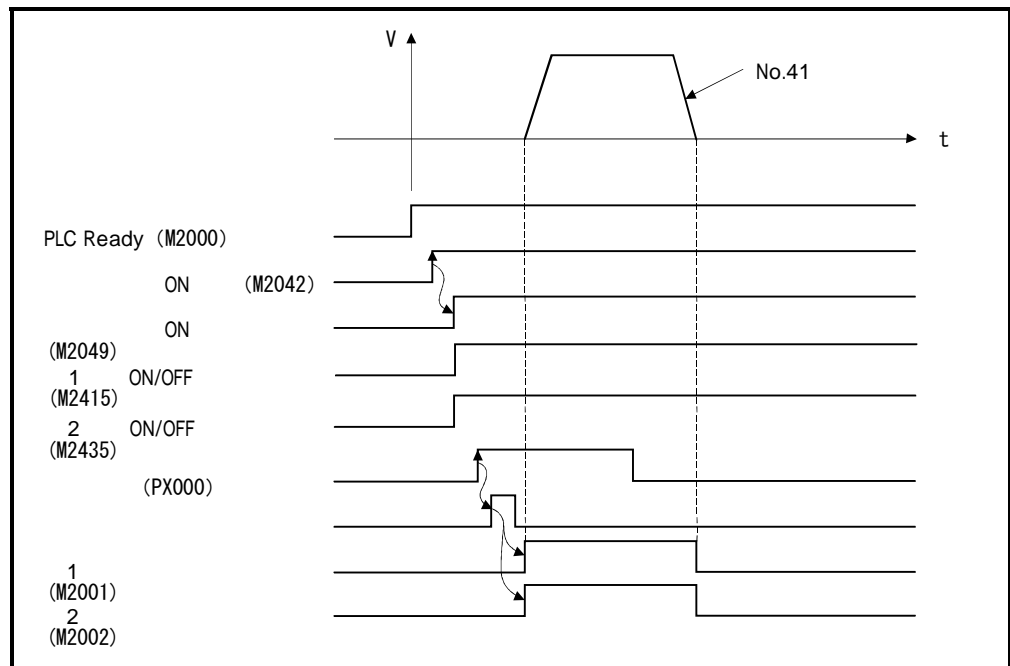
(a)

	No.
	No.41
	1000

(b)

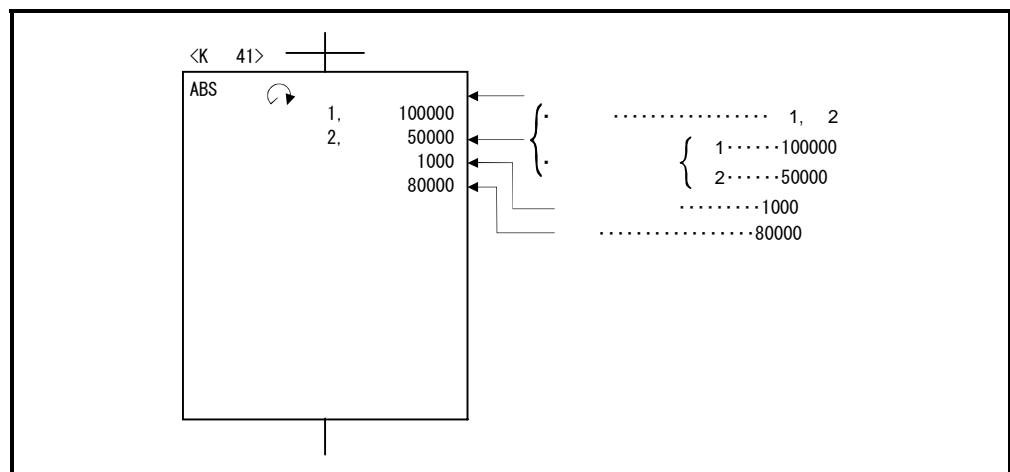
..... PX000 (OFF →ON)

(4)



(5)

No.41

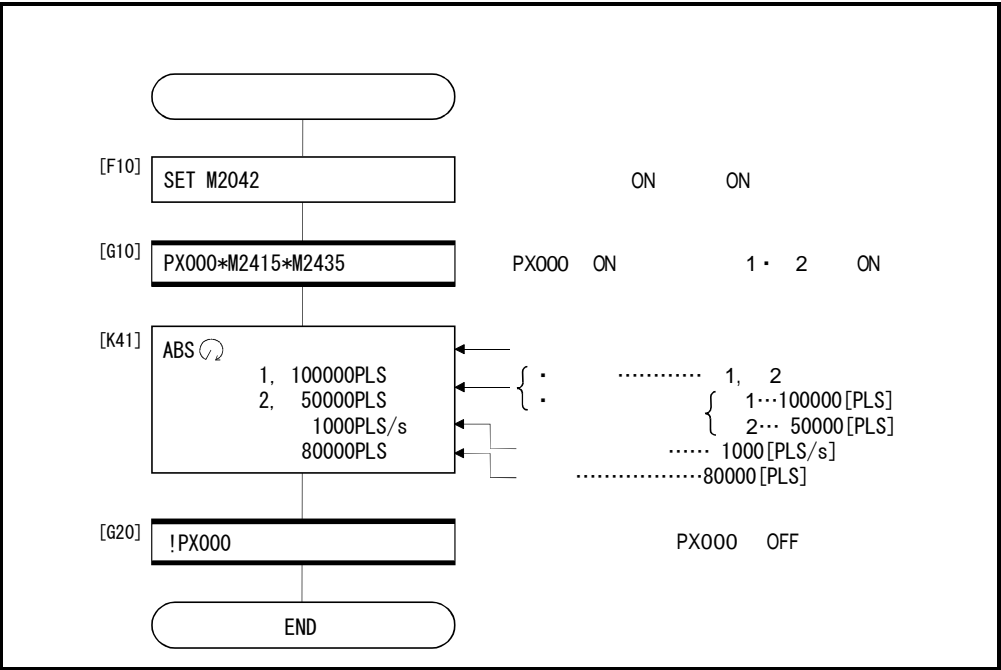


* :

SFC

(6) SFC

SFC







* : SFC /

6.

6. 8

INC ↺, INC ↻, (, ABS ↺ABS ↻, ()

			No.		/			M							가				S T O P		S			W A I T I O N / O F F	
ABS 		2	△	○	○	○	△	△					○	△	△	△	△	△	△	△	△	△		가	
ABS 																									
INC 																									
INC 																									

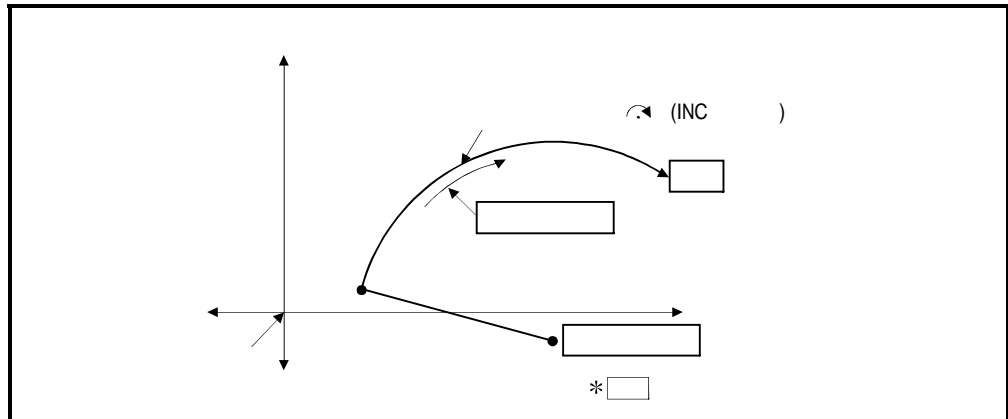
○ :
△ :

[]

		가		
ABS ↻		$0^{\circ} < \theta < 360^{\circ}$		
INC ↻				
ABS ↺				
INC ↺				

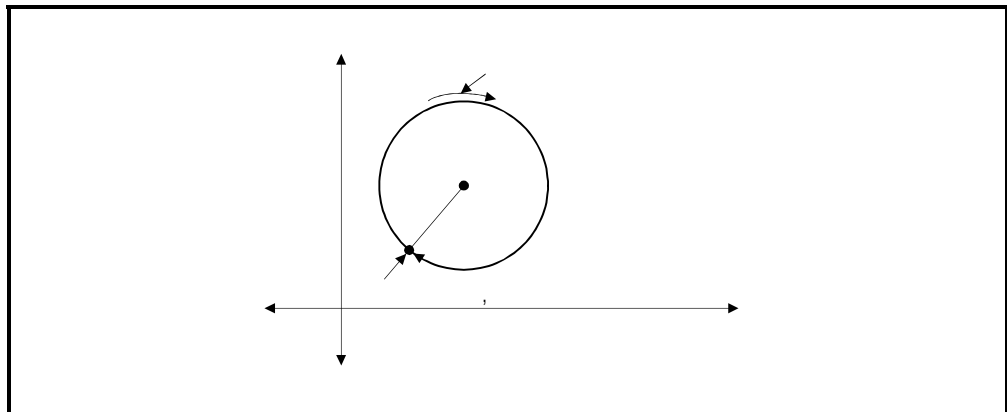
INC ↺, INC ↻ ()

(1) (0,0)



6.20 INC ↺

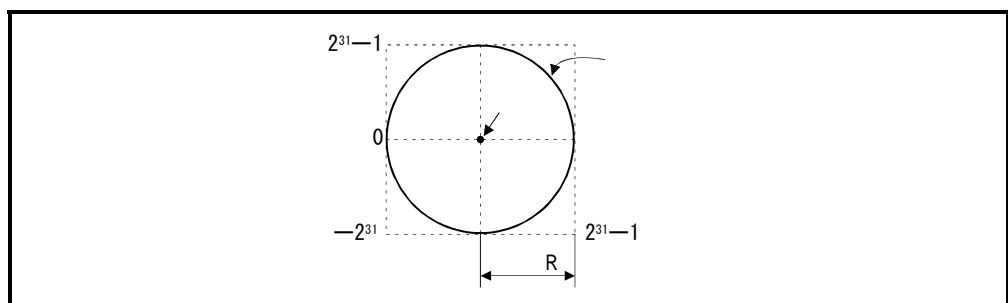
(2) , 가 .



6.21

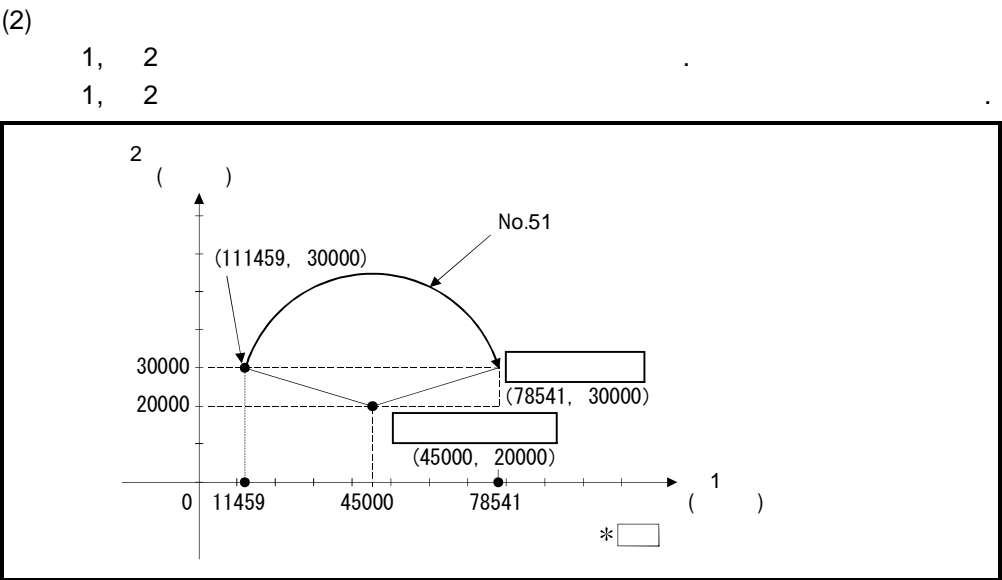
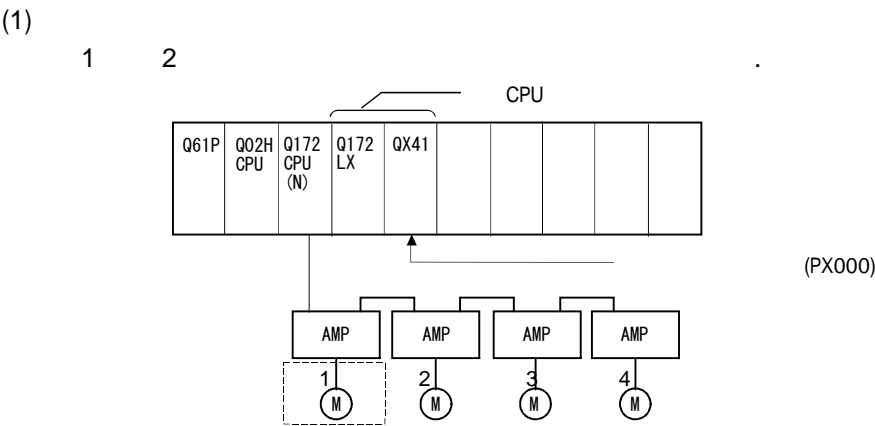
(3) , $0 \sim \pm(2^{31}-1)$.

(4) , $2^{31}-1$.
 $2^{31}-1$, , 가
 [109]가 .



6.22

[]



(3)

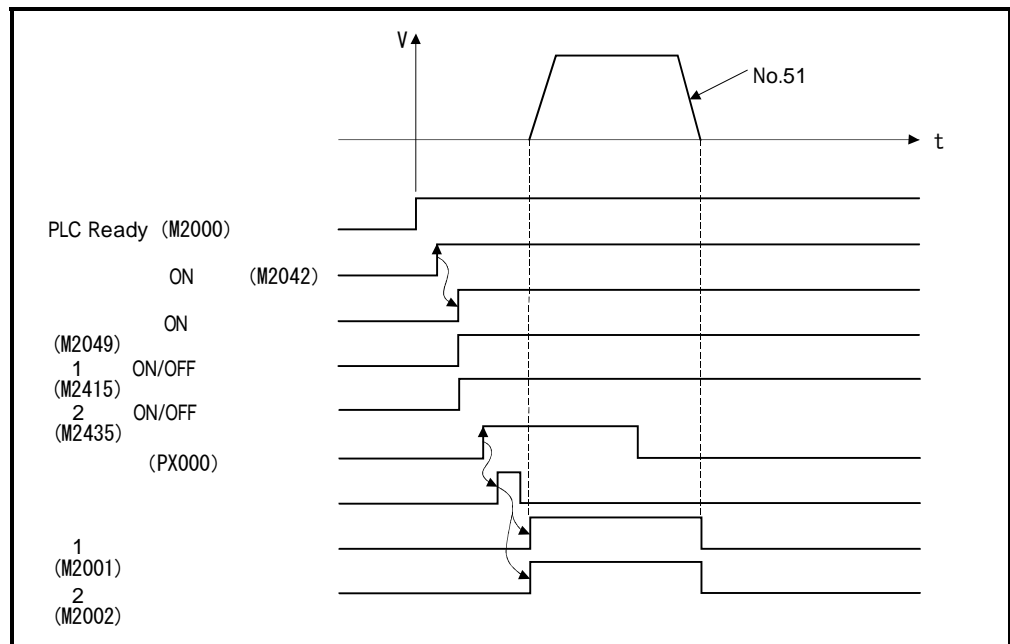
(a)

	No.
	No. 51
	1000

(b)

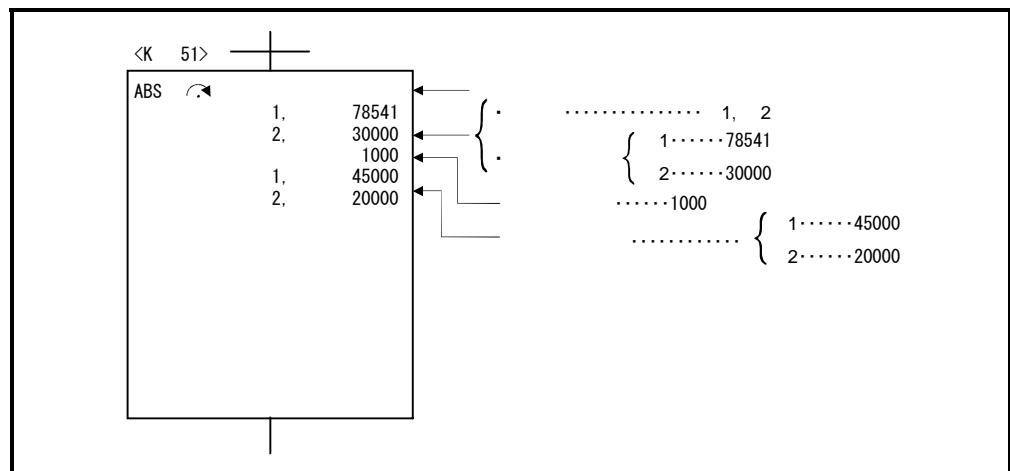
..... PX000 (OFF→ON)

(4)



(5)

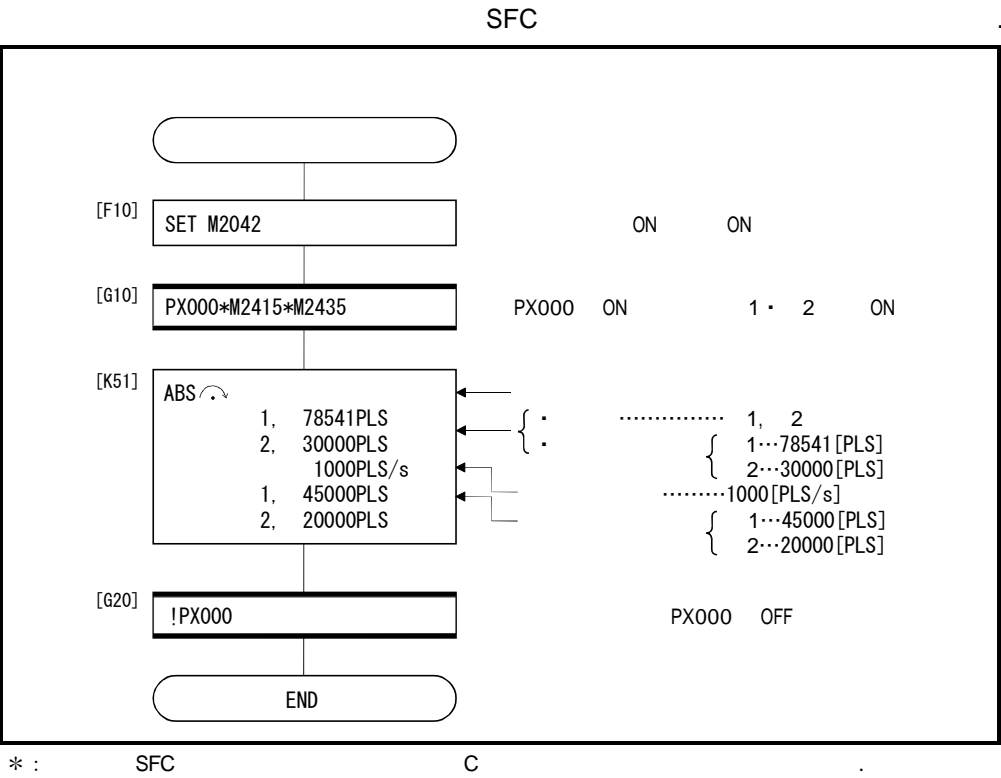
No.51



* :

SFC

(6) SFC

















6.

6.9

3

2

			No.	/				M							가				STOP	S	WAIT TION /OFF		
<div>ABH </div>	CW 180°	3	△	○	○	○	△	△			○		○	△	△	△	△	△	△	△	△	△	
<div>ABH </div>	CW 180°	3	△	○	○	○	△	△			○		○	△	△	△	△	△	△	△	△	△	
<div>ABH </div>	CCW 180°	3	△	○	○	○	△	△			○		○	△	△	△	△	△	△	△	△	△	
<div>ABH </div>	CCW 180°	3	△	○	○	○	△	△			○		○	△	△	△	△	△	△	△	△	△	
<div>INH </div>	CW 180°	3	△	○	○	○	△	△			○		○	△	△	△	△	△	△	△	△	△	
<div>INH </div>	CW 180°	3	△	○	○	○	△	△			○		○	△	△	△	△	△	△	△	△	△	
<div>INH </div>	CCW 180°	3	△	○	○	○	△	△			○		○	△	△	△	△	△	△	△	△	△	
<div>INH </div>	CCW 180°	3	△	○	○	○	△	△			○		○	△	△	△	△	△	△	△	△	△	
<div>ABH </div>	CW	3	△	○	○	○	△	△				○	○	△	△	△	△	△	△	△	△	△	
<div>ABH </div>	CCW	3	△	○	○	○	△	△				○	○	△	△	△	△	△	△	△	△	△	
<div>INH </div>	CW	3	△	○	○	○	△	△				○	○	△	△	△	△	△	△	△	△	△	
<div>INH </div>	CCW	3	△	○	○	○	△	△				○	○	△	△	△	△	△	△	△	△	△	
<div>ABH </div>		3	△	○	○	○	△	△		○			○	△	△	△	△	△	△	△	△	△	
<div>INH </div>		3	△	○	○	○	△	△		○			○	△	△	△	△	△	△	△	△	△	

○ :
△ :

(3) $=0$ 가 .

$=0$	• 가 . (.)
$\neq 0$	• , , (,)

(4) .

(5) .
 • 가[drgee] () , 1
 [degree] ()
 • [degree] ()
 • 가 , 가 .

(6) , CHGV ,
 2 , , CHGV
 ,
 가 가 .

(7) , = , =1,
 $=0$, 가 . /
 , = ,
 가 .

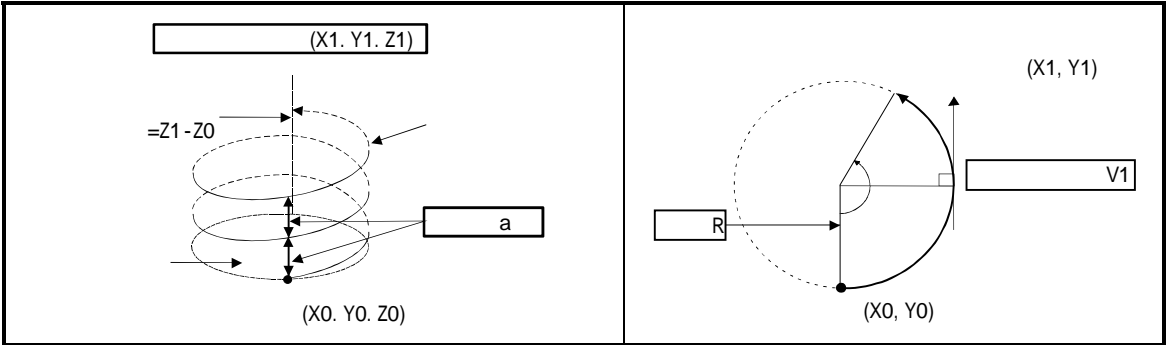
(8) 가[degree] 가 , 가
 , ,
 .

(9) .

ABH↺, ABH↻, ABH↻, ABH↺

[]

(X₀, Y₀, Z₀) , (X₁, Y₁),
(Z₁) , 2 ,
가

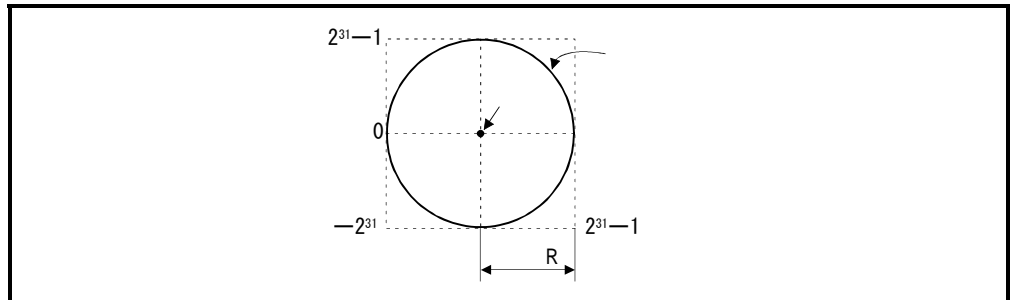


		가	
ABH↺ CW180°	(CW)	0° < θ < 180°	
ABH↻ CW180°	(CCW)		
ABH↻ CCW180	(CW)	180° ≤ θ ≤ 360°	
ABH↺ CCW180	(CCW)		

6.

(1) , , $(-2^{31}) \sim (2^{31}-1)$.

(2) , $2^{31}-1$.
 , 1 : 1 [mm] , 214748364.7 [μm]
 .



(3) , 2 .

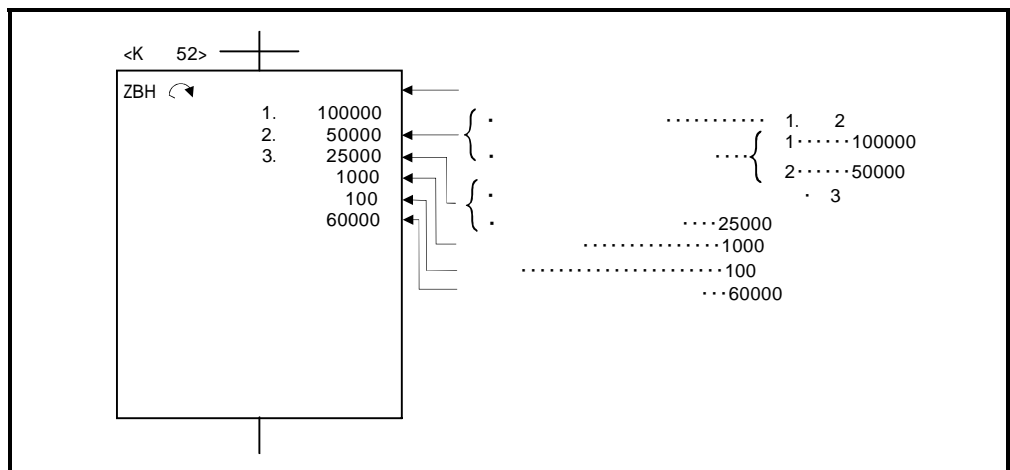
(4) , 가 .

(5) , 0~999 . ,
 [28]가 .

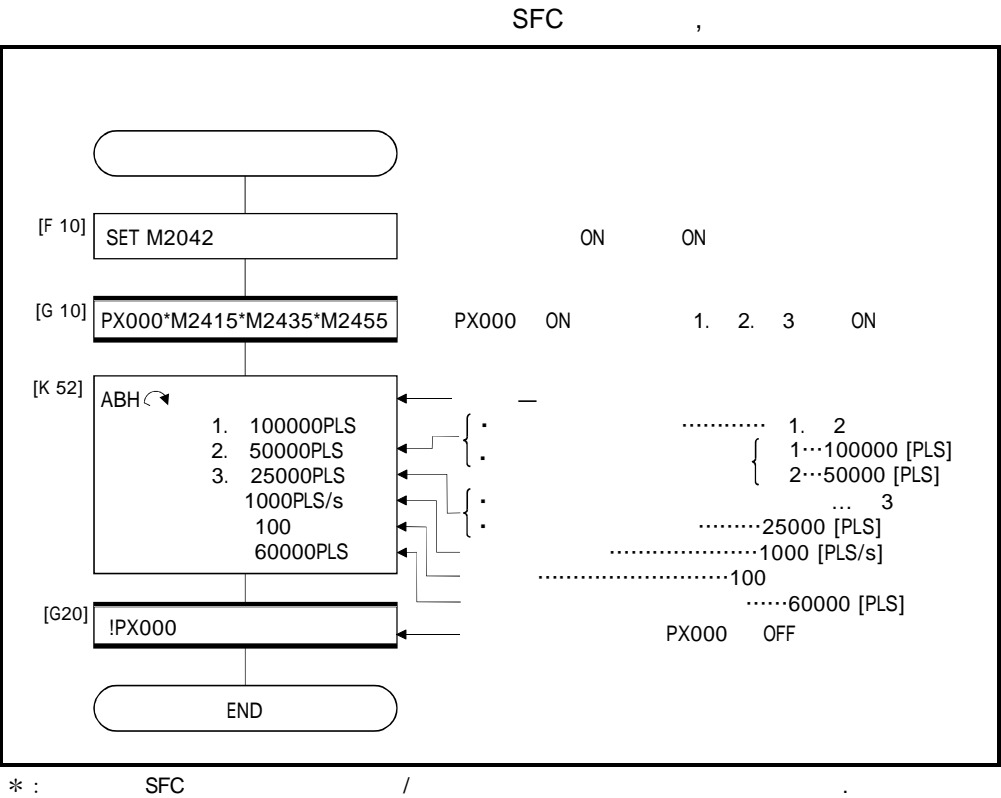
[]

(1)

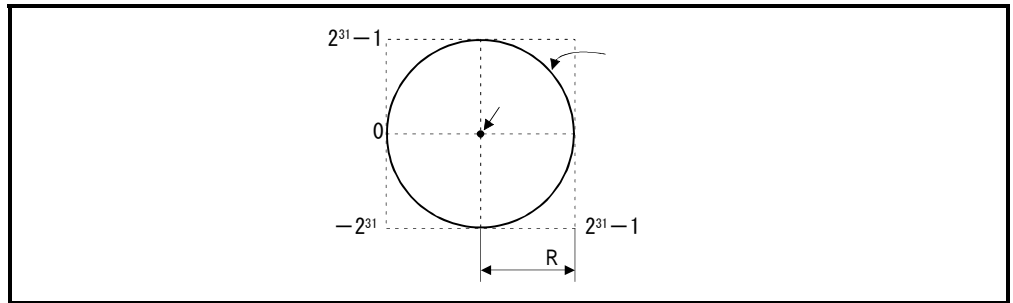
No.52



(2) SFC



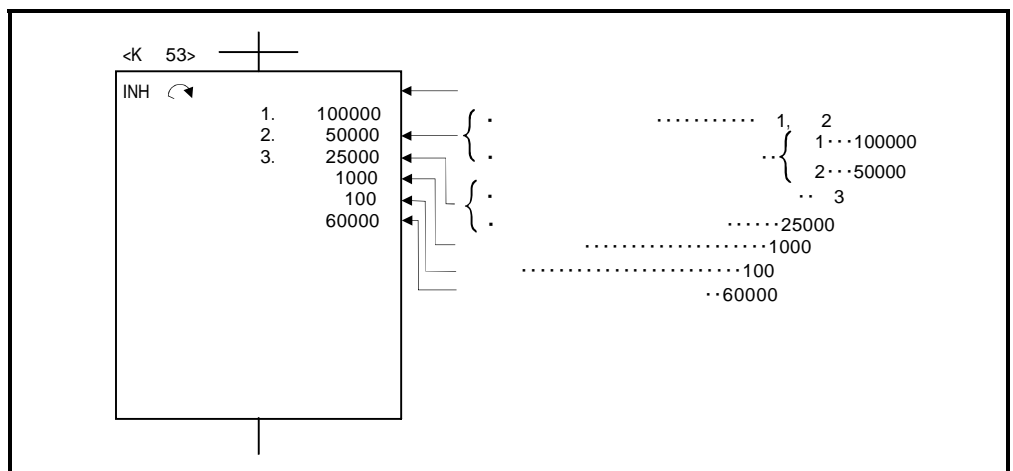
- (1) , , $0 \sim \pm (2^{31}-1)$.
 . (가)
 . ()
- (2) , $2^{31}-1$.
 , $1 : 1$ [mm] , $214748364.7 [\mu m]$
 가 .



- (3) , 2 .
- (4) , 가 .
- (5) , $0 \sim 999$,
 [28]가 .
- (6) , , (2 가),
 (1) ,D, W, # 가 .

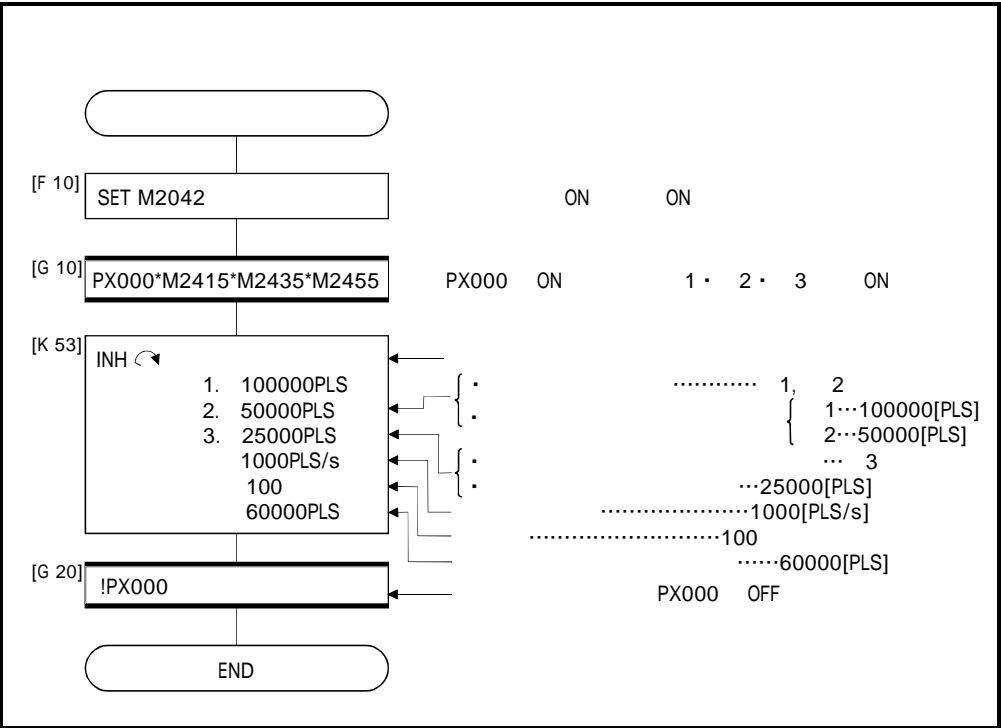
[]

- (1) No. 53



(2) SFC

SFC ,



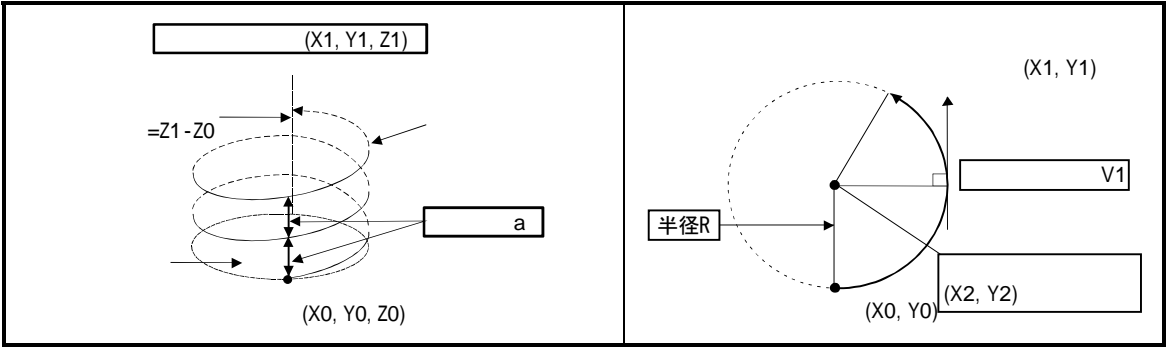
* : SFC

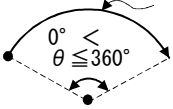
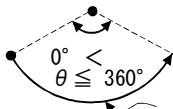
/

ABH ↻, ABH ↺

[]

(X₀, Y₀, Z₀) (X₁, Y₁),
(Z₁) , 2
가

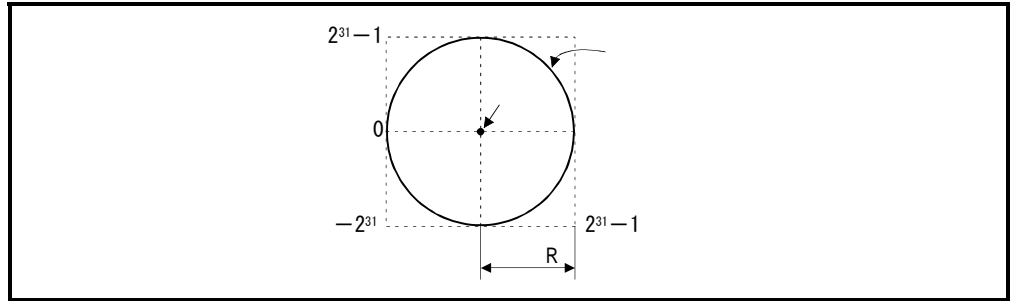


		가	
ABH ↻ CW	(CW)	$0^\circ < \theta \leq 360^\circ$	
ABH ↺ CCW	(CCW)		

- (1) , , (- 2³¹)~(2³¹ - 1) .
- (2) , (- 2³¹)~(2³¹ - 1) .

6.

- (3) , $2^{31}-1$.
 , 1:1 [mm] , 214748364.7 [μm]
 .



- (4) , 2 .

- (5) , 가 .

- (6) , 0~999 . ,
 [28] , .

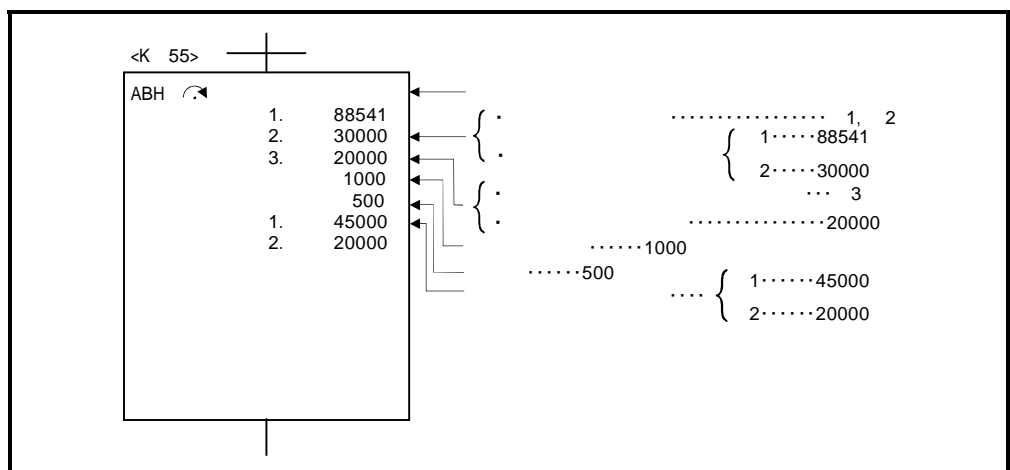
- (7) , , (2),
 (1) , D, W, # 가 .

- (8) , = , =1, =0
 가 .

[]

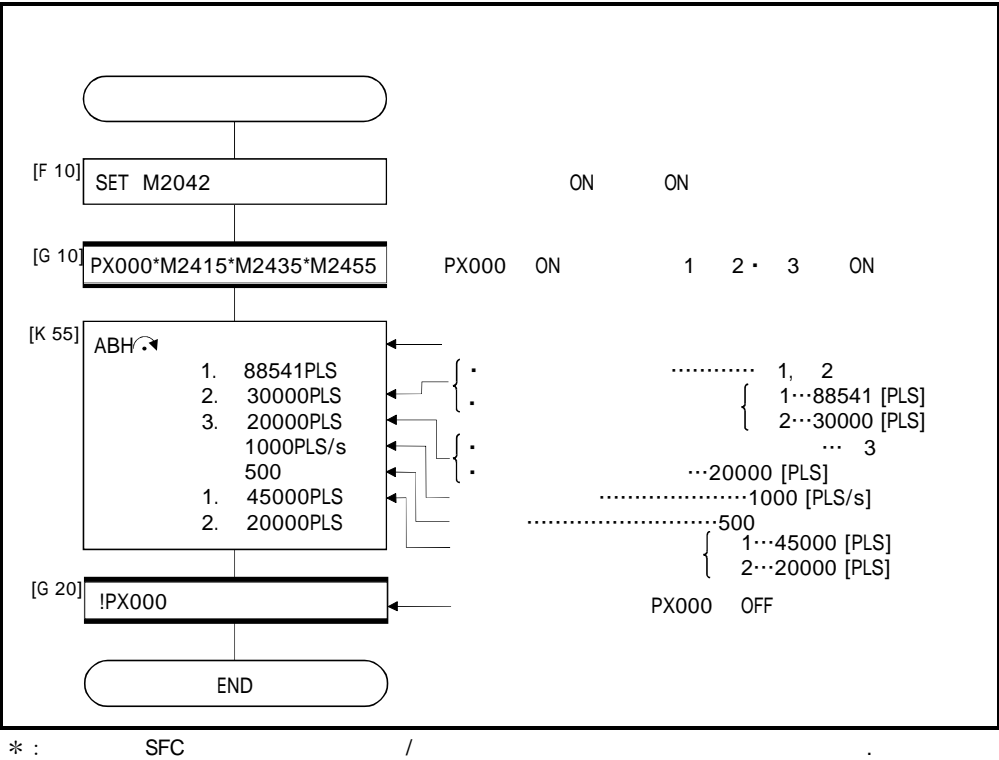
- (1)

No. 55

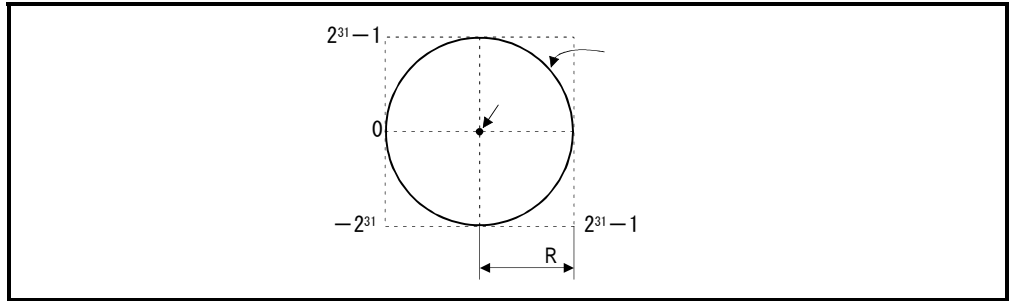


(2) SFC

SFC



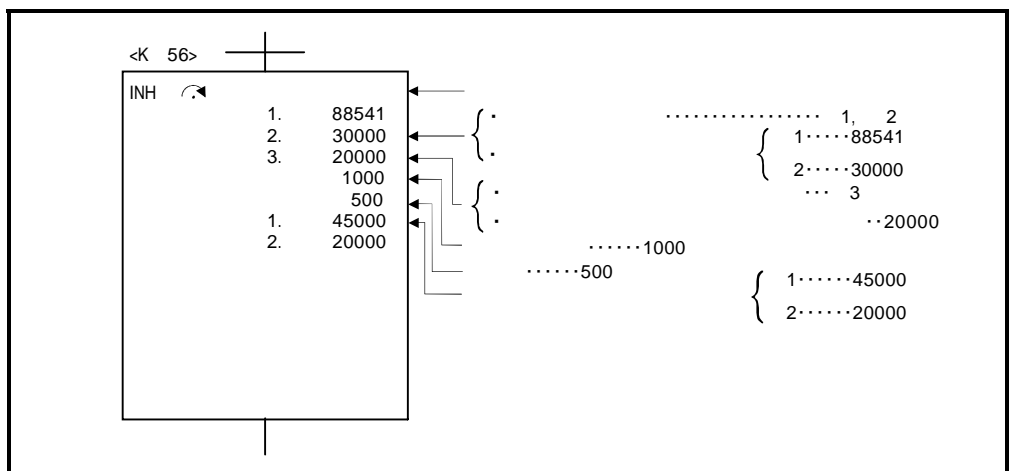
- (3) , $2^{31}-1$, 1:1 [mm] , 214748364.7 [μ m] 가 .



- (4) , 2 .
- (5) , 가 .
- (6) 0~999 , [28]가 .
- (7) , , (2), (1) , D, W, # 가 .
- (8) =0 , = , =1, 가 .

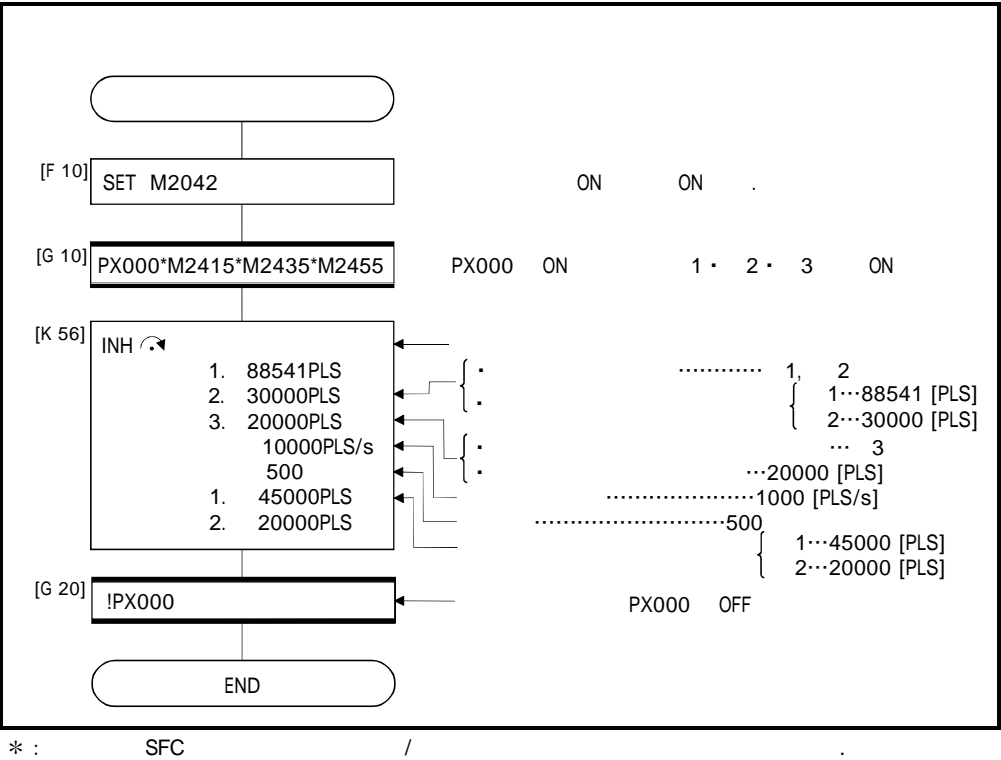
[]

- (1) No. 56



(2) SFC

SFC



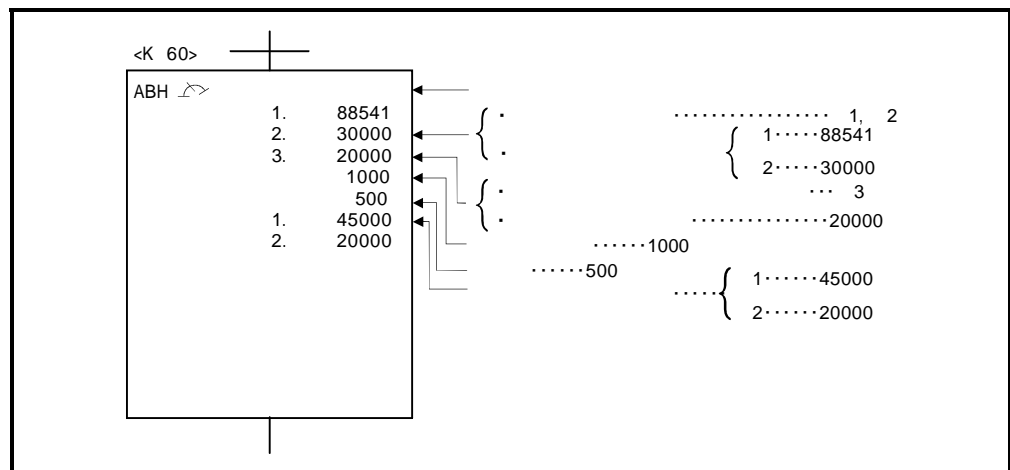
6.

(6) 0~999 ,
[28]가 ,

(7) , , (2),
(1) D, W, # 가 .

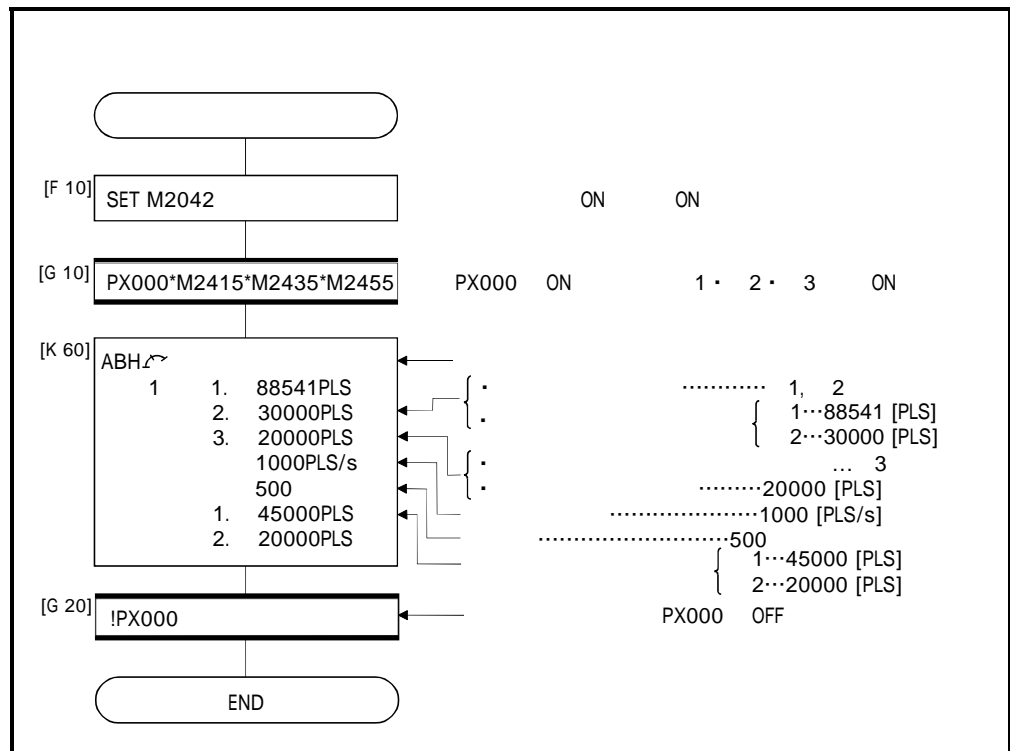
[]

(1) No. 60 ,



(2) SFC

SFC

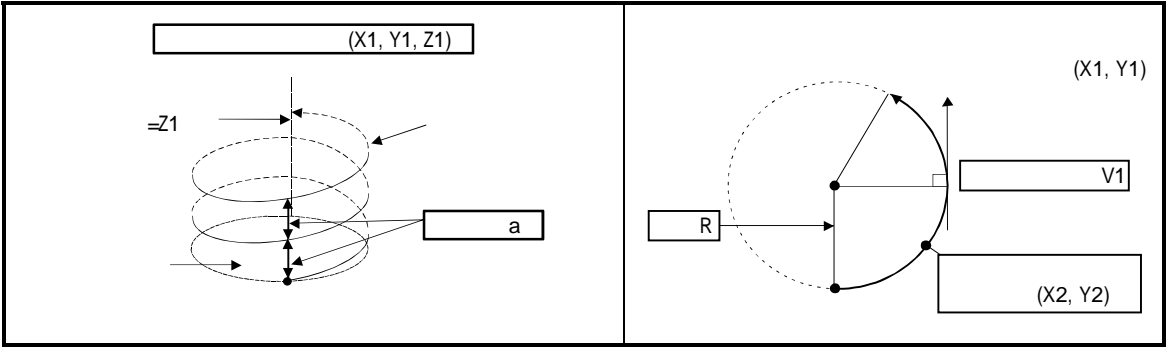


* : SFC /

INH \angle°

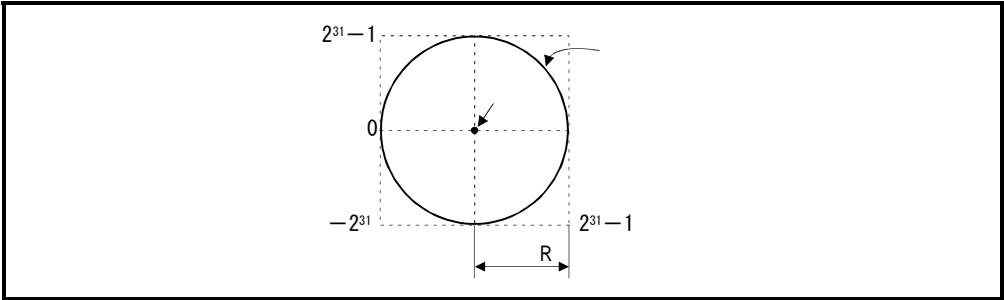
[]

() (X₁, Y₁),
(Z₁) , 2
가



		가
INH \angle°	(CW) (CCW)	$0^\circ < \theta \leq 360^\circ$

- (1) , , $0 \sim \pm (2^{31}-1)$.
- (2) , $\pm (2^{31}-1)$.
- (3) , $2^{31}-1$.
가 , 1:1 [mm] , 214748364.7 [μm]



- (4) , 2 .
- (5) , 가 .

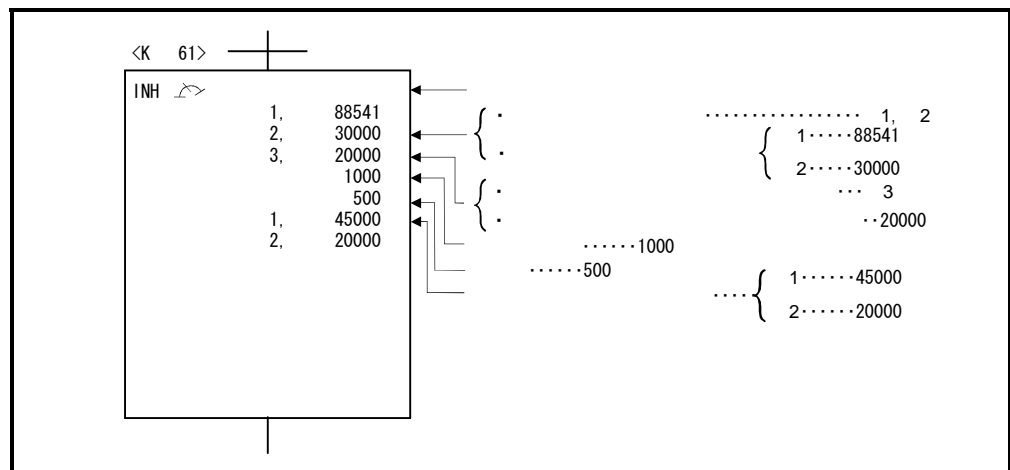
(6) , 0~999
[28]가 ,

(7) , , (2),
(1) , D,W,# 가 .

【 】

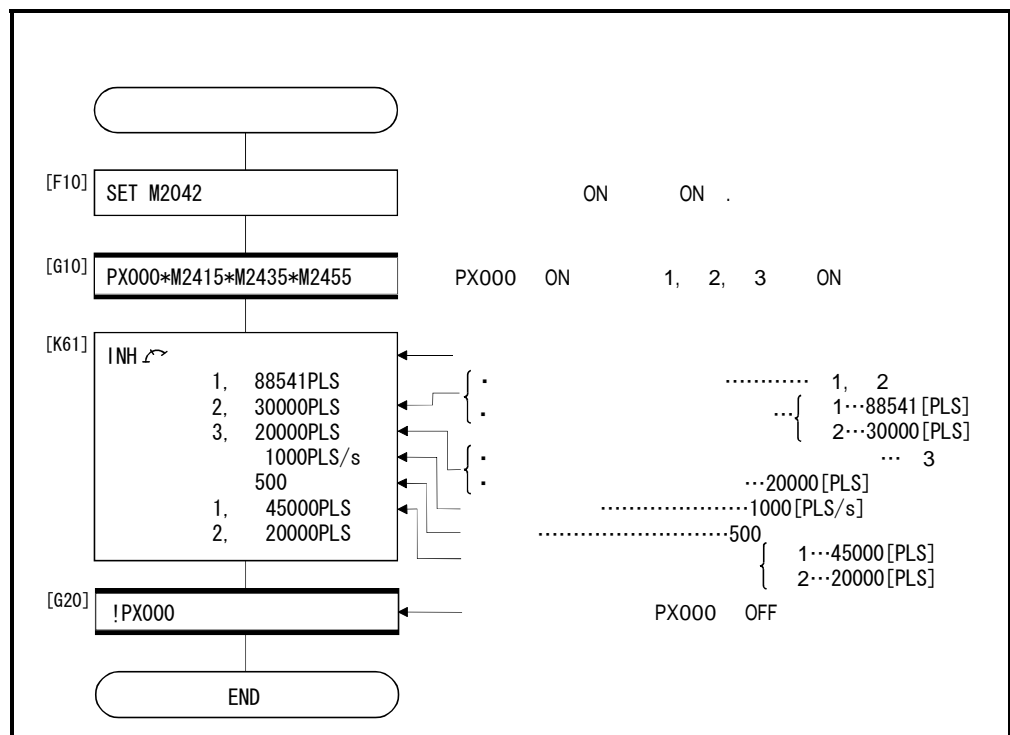
(1)

No.61



(2) SFC

SFC



* : SFC

/

FEED-1

			No.		/		M						가			S T O P		S	W A I T I O N / O F F			
FEED-1		1	△	○	○	○	△	△					△	△	△	△	△	△	△	△		가

○ :
△ :

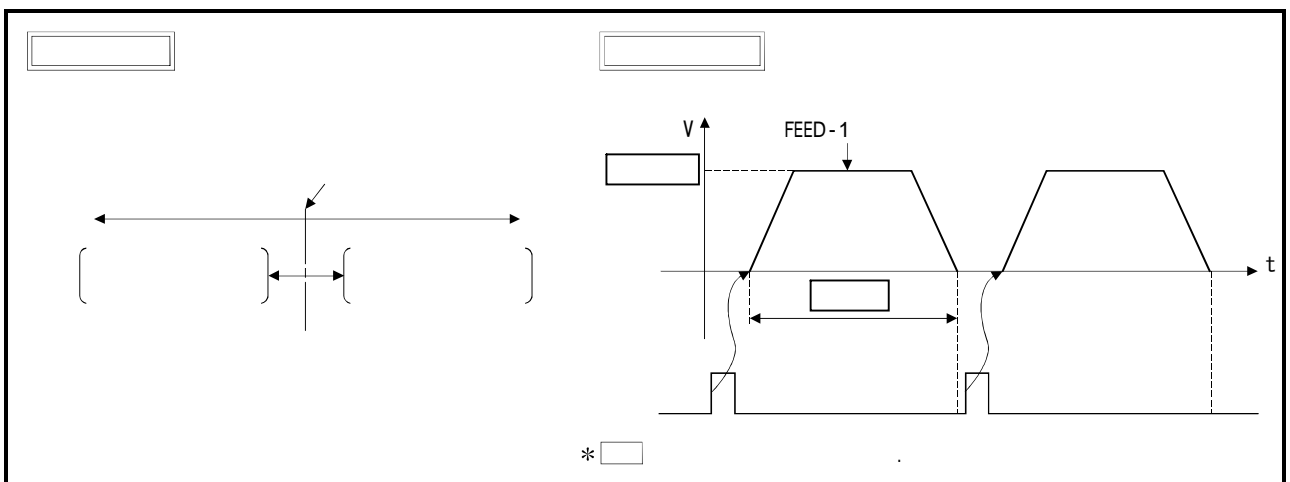
【 】

(1) 0 , \quad .

(2) , .

• (가)

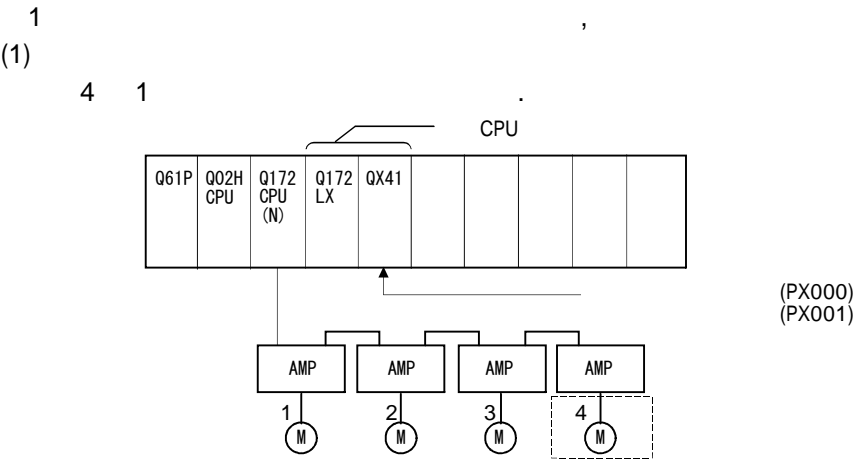
• ()



6.23 1

가

【 】



(2)

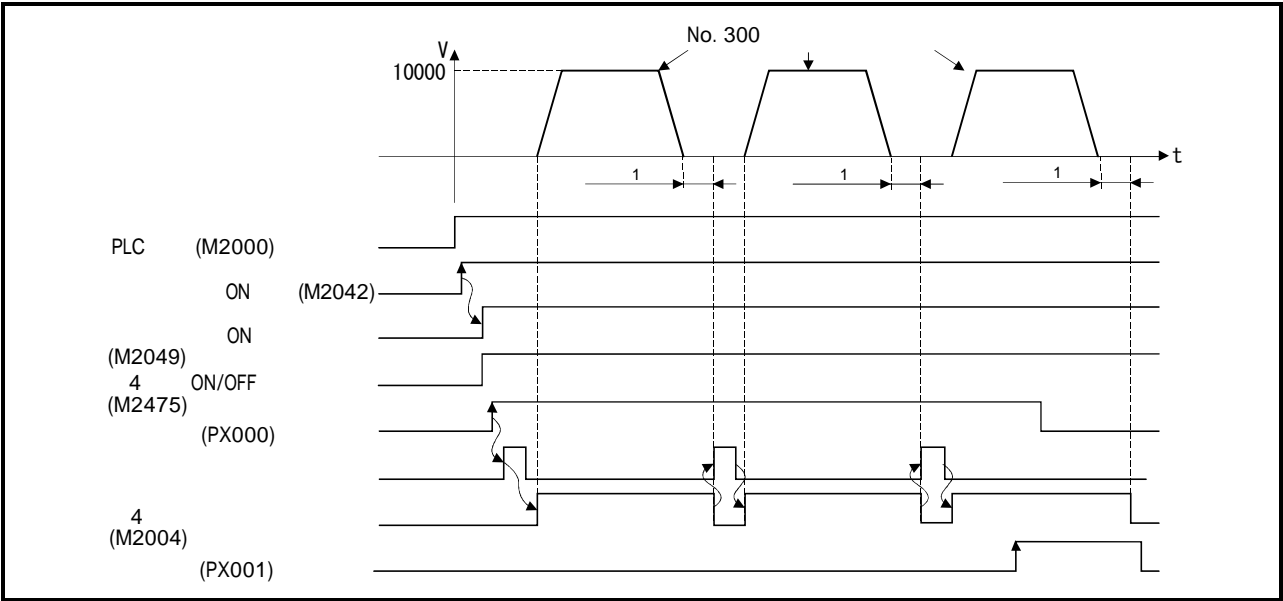
(a)

No.	No.300
	4
	10000
	80000

(b) ··PX000 (OFF ON)

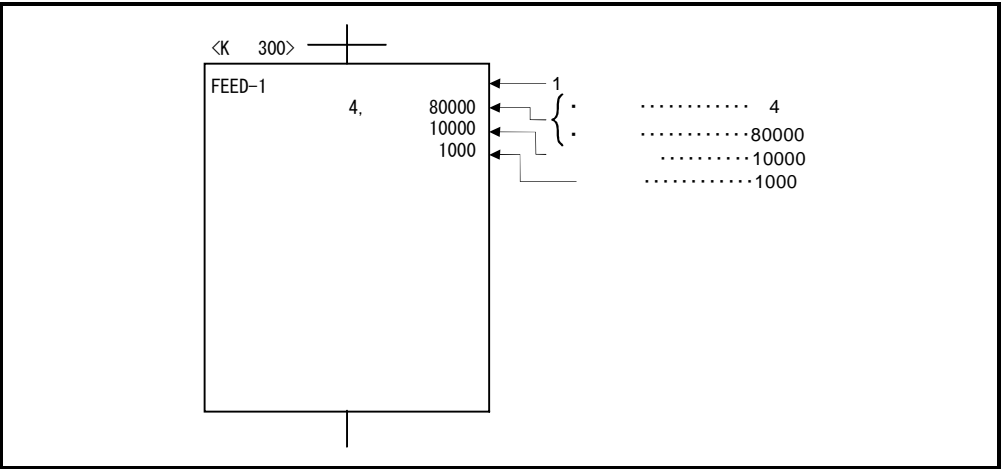
(c) ··PX001 (OFF ON)

(3)



(4)

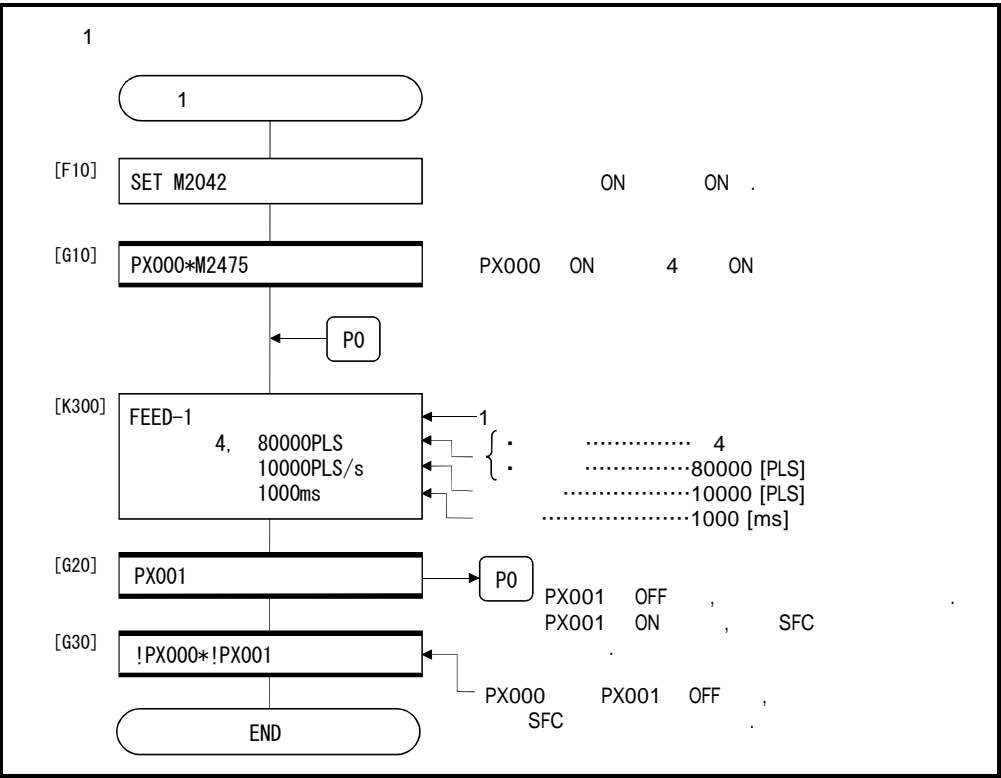
No.300



(5)

SFC

SFC



* : SFC

6.

6.11 2

2 , 2

2 , FEED-2

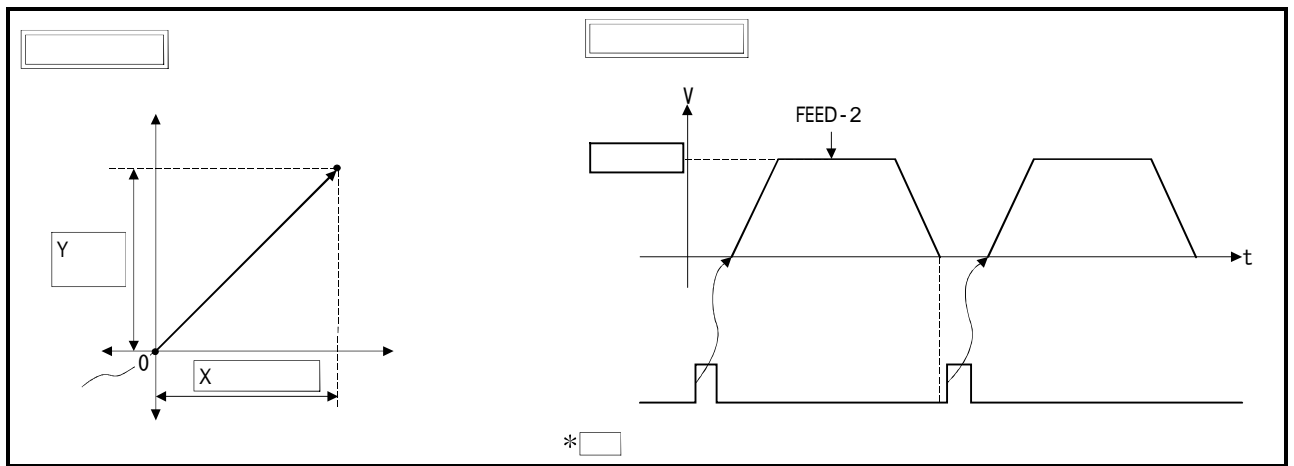
			No.	/			M						가				STOP	S	WAIT TION /OFF				
FEED - 2		2	△	○	○	○	△	△					△	△	△	△	△	△	△	△	△		가

○ :
△ :

【 】

(1) 0 ,

(2) , (가)
..... ()



6.24 2

0
(1) 2 0 , 가

2

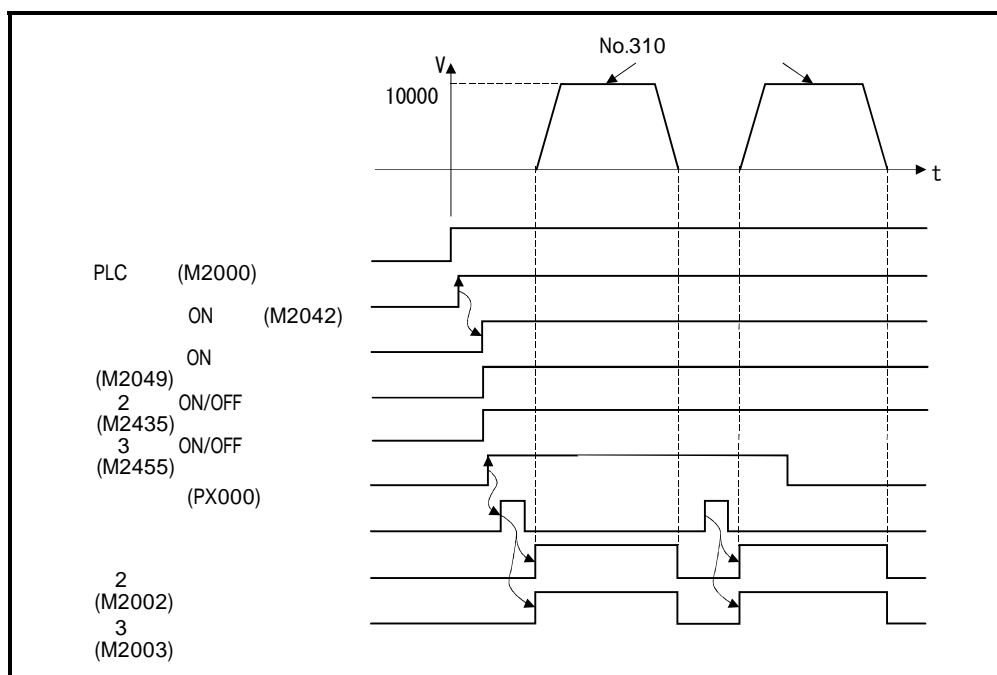
2 3



No.	No.310	
	10000	
	2	3
	500000	300000

(3)

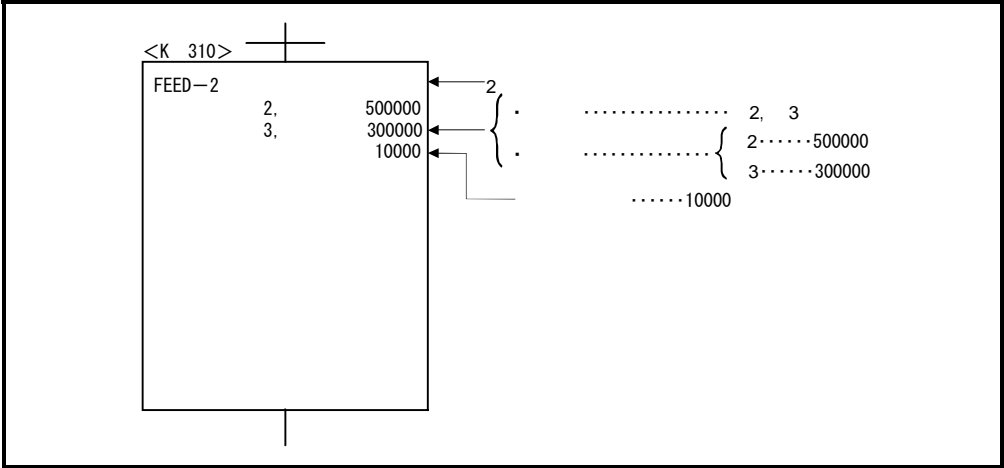
2



(4)

2

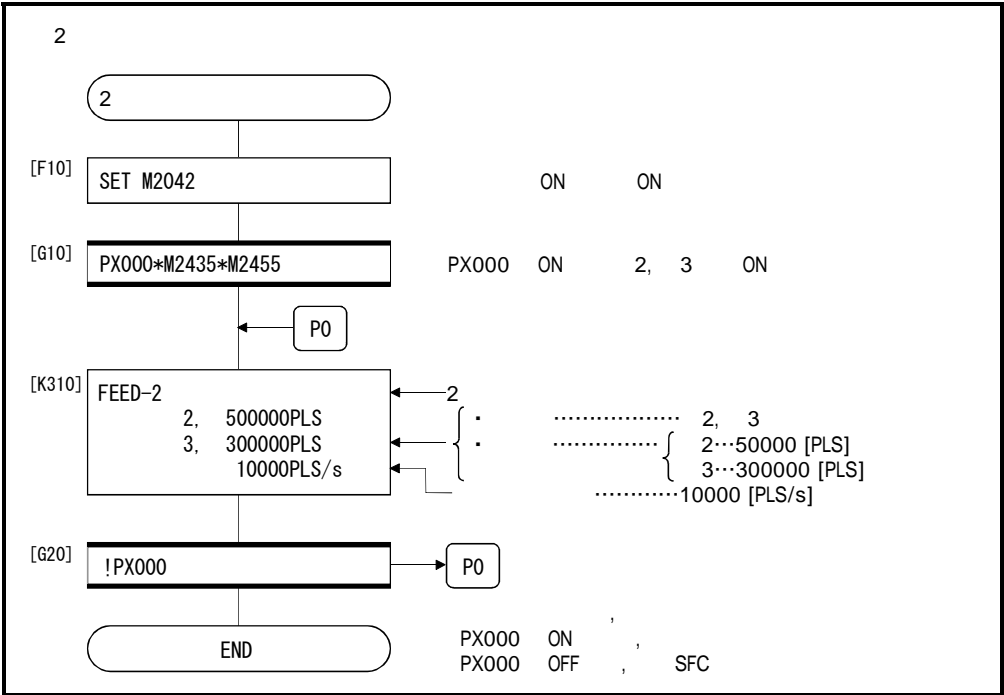
No.310



(5)

SFC

SFC



* : SFC

,

۳

,

■

FEED-3

가

0,

,

가)
)

*

--

6. 25 3

0

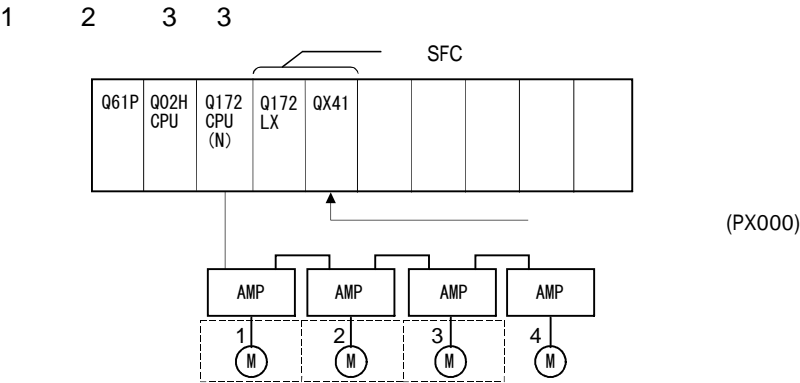
(1) 3

가

【 】

3

(1)



(2)

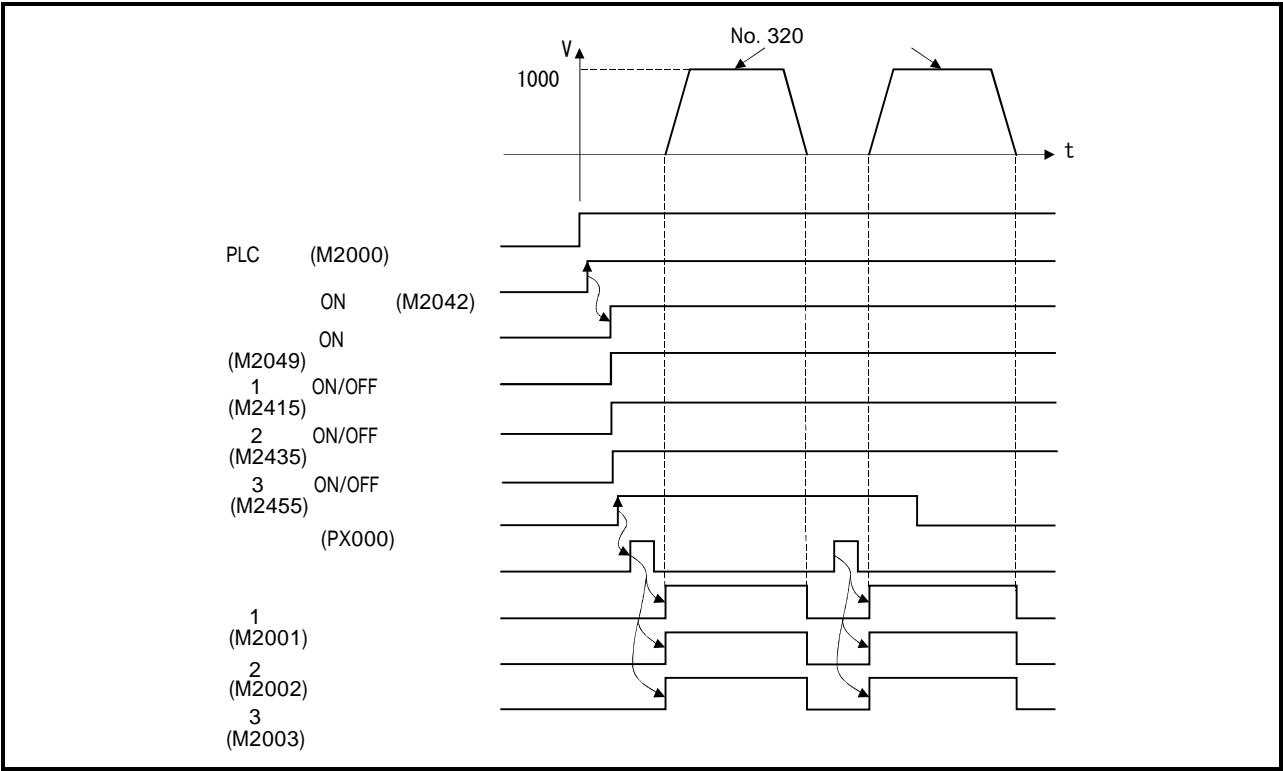
(a)

No.	No.320		
	1000		
	1	2	3
	50000	40000	30000

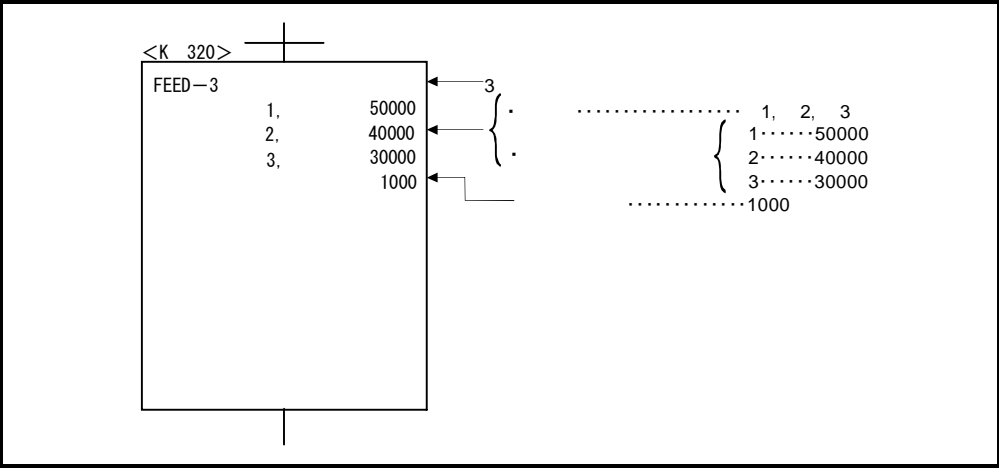
(b) PX000 (OFF →ON)

(3)

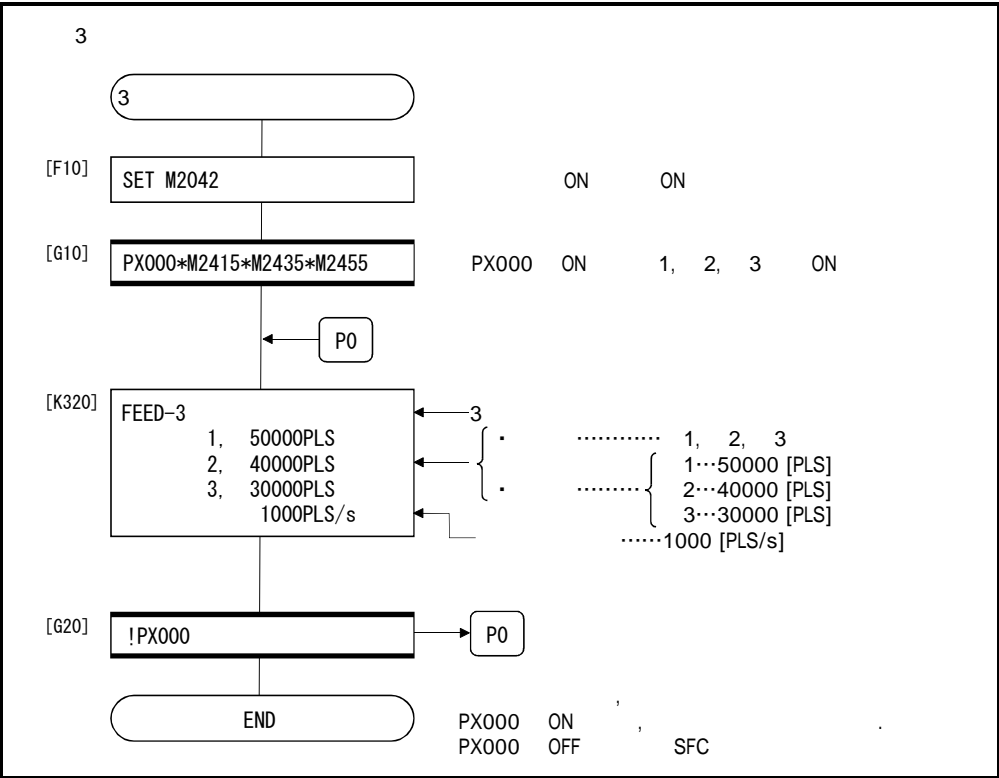
3



(4) 3 No.320 ,



(5) SFC SFC ,



* : SFC /

6. 13 ()

- (1) .
- (2) , .
- (3) () , VF() VR() .

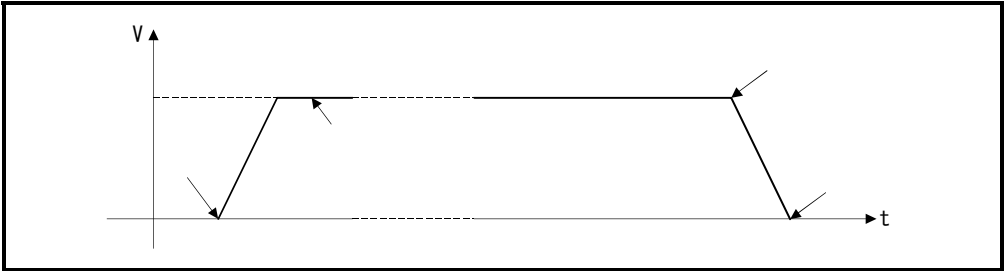
			No.	/		M						가			STOP	S	WAIT TION /OFF				
VF	—	1	△	○	○	△					△	△	△	△	△		△	△		가	
VR																					

○ :
△ :

【 】

- (1) , , .
- VF
• VR

(2) , 0 .



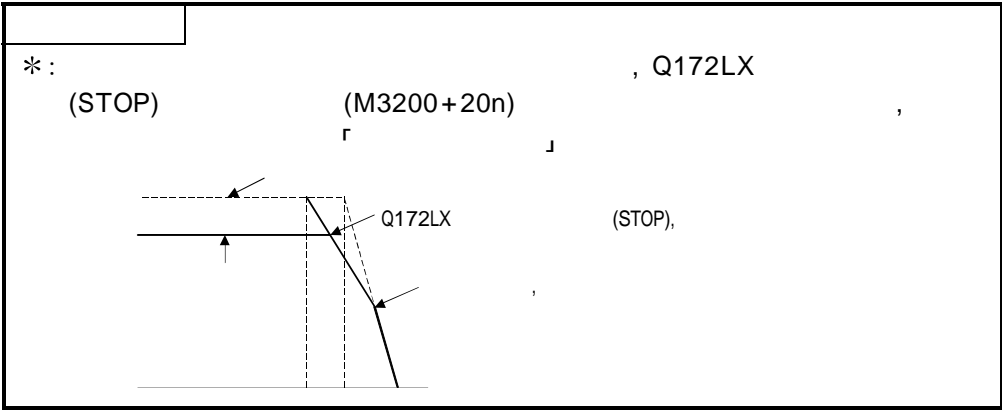
6. 26 ()

(3)

, 6.1 .

6. 1

Q172LX (STOP)	OFF→ON		「STOP」
(M3200+20n)			「」
*(M3201+20)			「」
*/ *()			「」
0			「」



【 】

- (1) , , ,
0 .
.
(OFF→ON)
- (2) 가 .

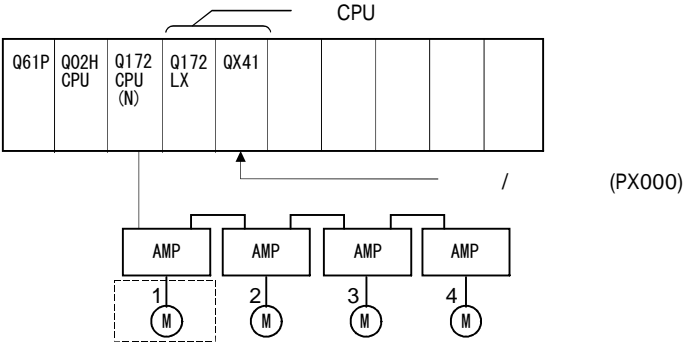
【 】

()

(1)

1

()



(2)

()

(a)

()

No.	No.91
	1
	3000

(b)

()

..... PX000

(OFF → ON)

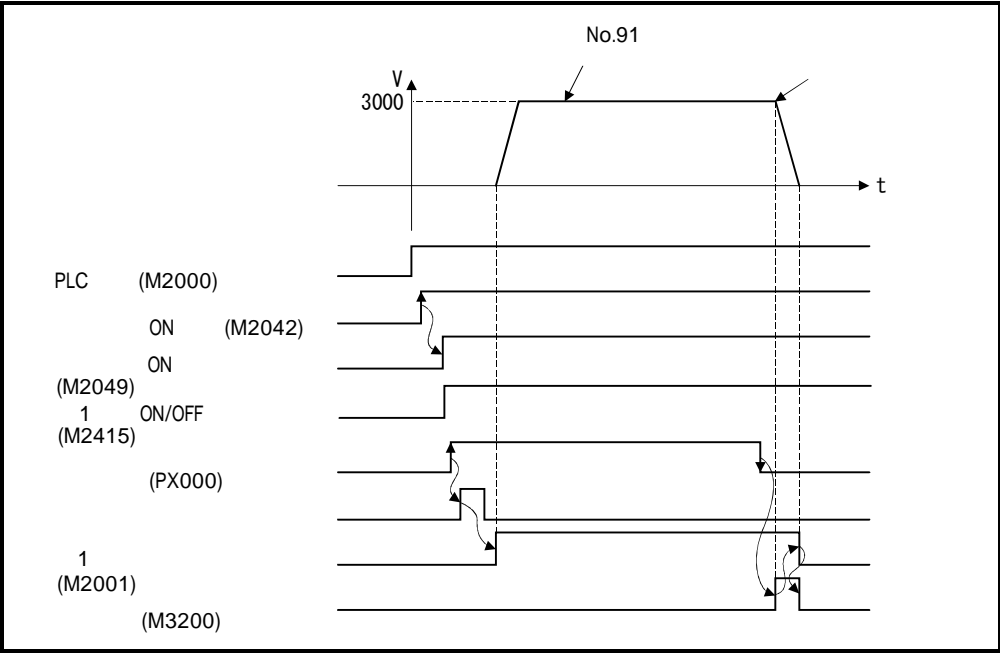
(c)

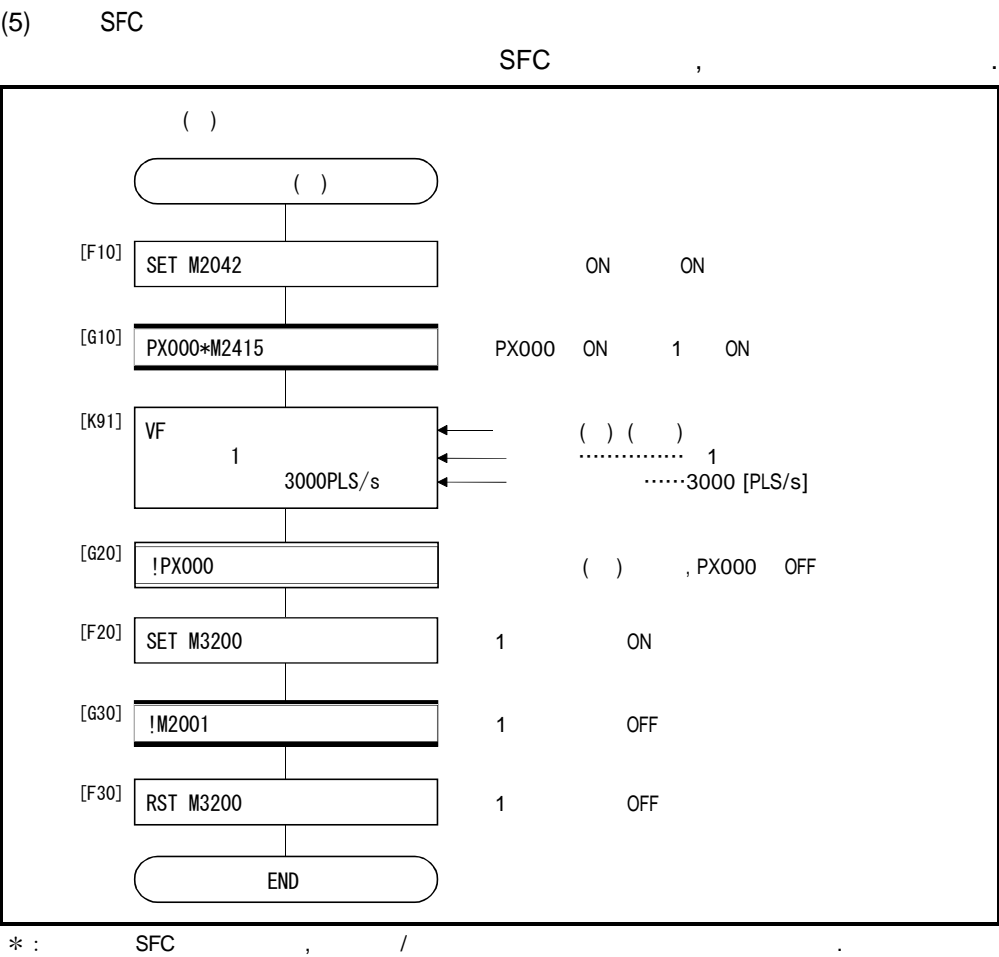
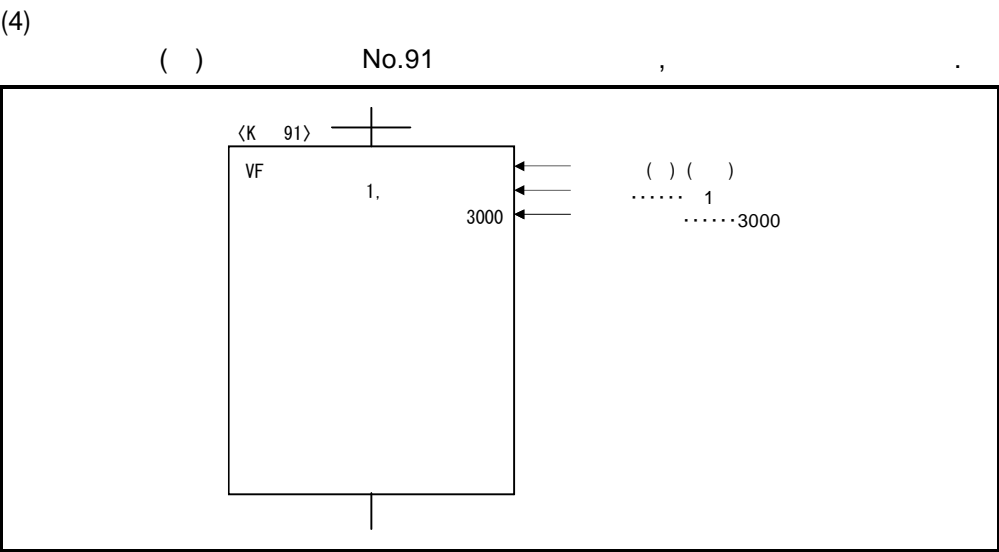
..... PX000

(ON → OFF)

(3)

()





6.

6.14 ()

(1) .

(2) , 가 , .

(3) () , VVF() VVR() .

			No		/			M						가				S T O P	S	W A I T I O N / O F F			
VVF	—	1	△	○		○		△	△					△	△	△	△	△		△	△		가
VVR																							

○ :
△ :

【 】

(1) , , .

- VVF
- VVR

(2) , , 0 .

(3) , 「 」 , .

(4) , () .

【 】

(1) , , ,
0 .
.
.
(OFF→ON)

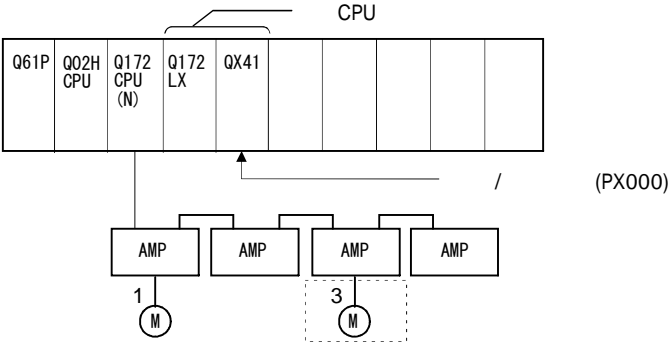
(2) 가 .

【 】

()

(1)

3



(2)

()

(a)

()

No.	No.55
	3
	4000

(b)

()

..... PX000

(OFF→ ON)

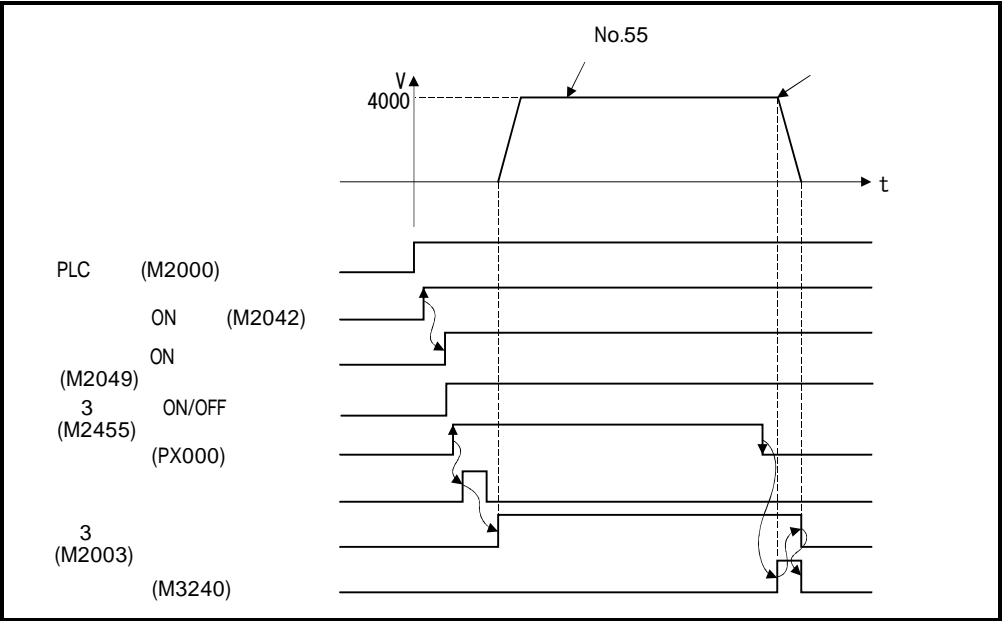
(c)

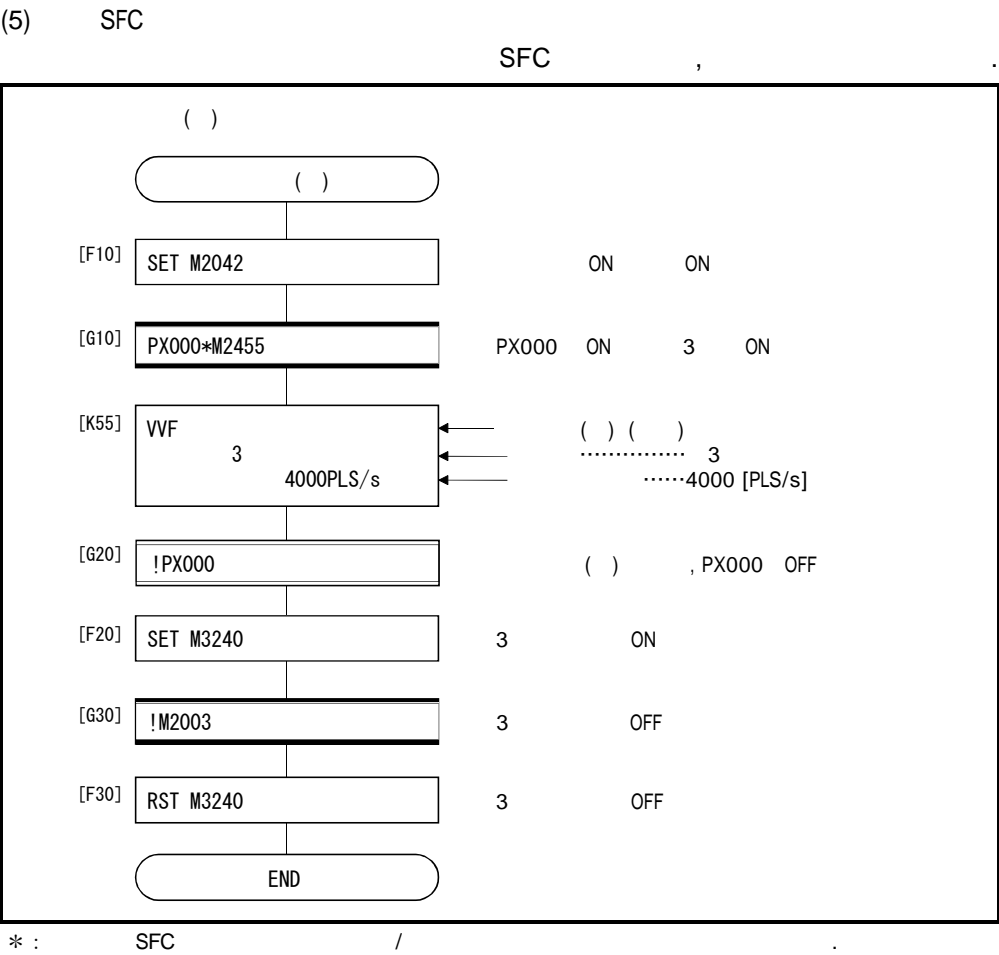
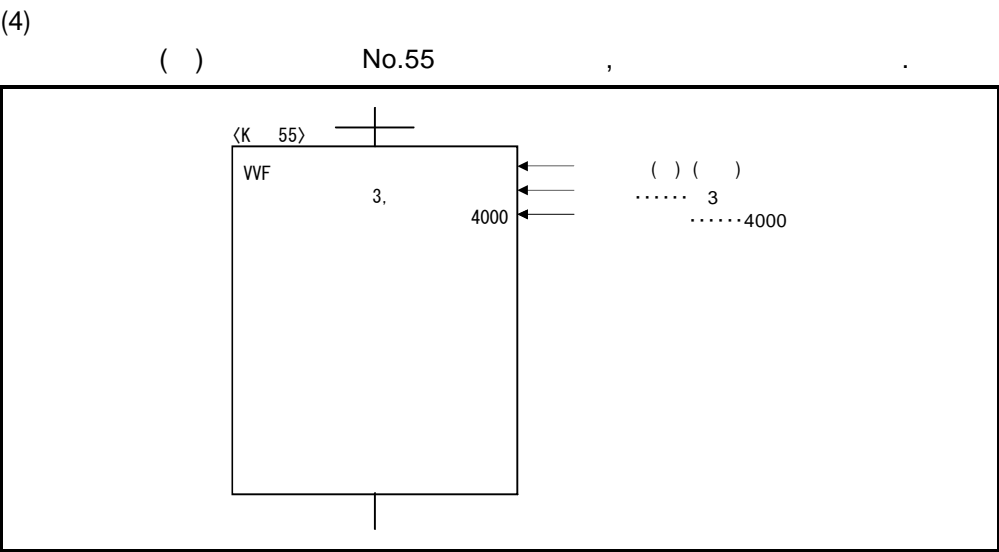
..... PX000

(ON→ OFF)

(3)

()





,

1

○ :
△ :

(1) CHANGE (

)

- VPF
- VPR

,





* : " CHANGE " , Q172LX CHANGE
 ON CHANGE , 「A」 , CHANGE
 OFF CHANGE , 「B」 , CHANGE
 CHANGE . (, 「Q173CPU/Q172CPU」
 」 .)

(3)

· , M3212+20n () ON/OFF

(a) M3213+20n OFF : * , 0

* ()

*

$$[] = [] + []$$

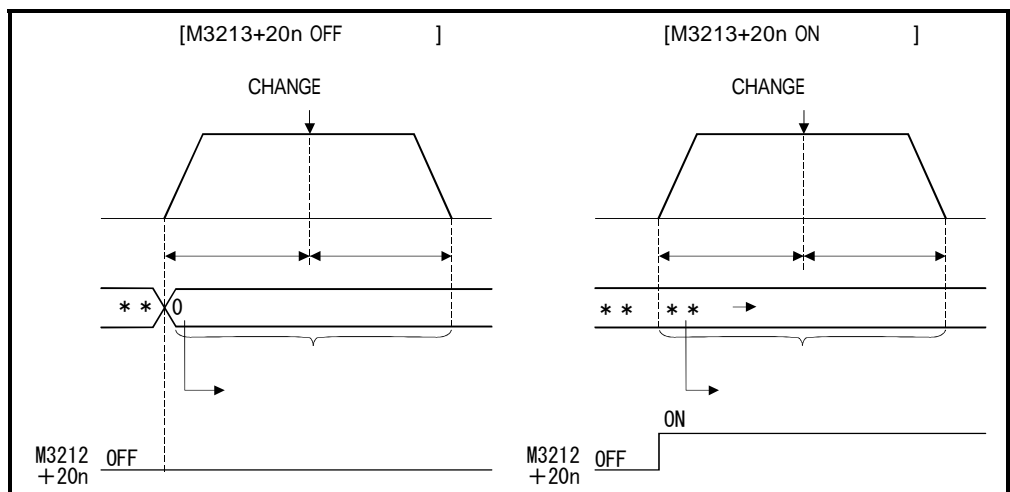
(b) M3213+20n ON

· ()

·

·

$$[] = [] + [] + []$$



M3213+20n ON , 가 M3213+
 20n ON
 M3213+20n OFF , 가

(4)

(a)

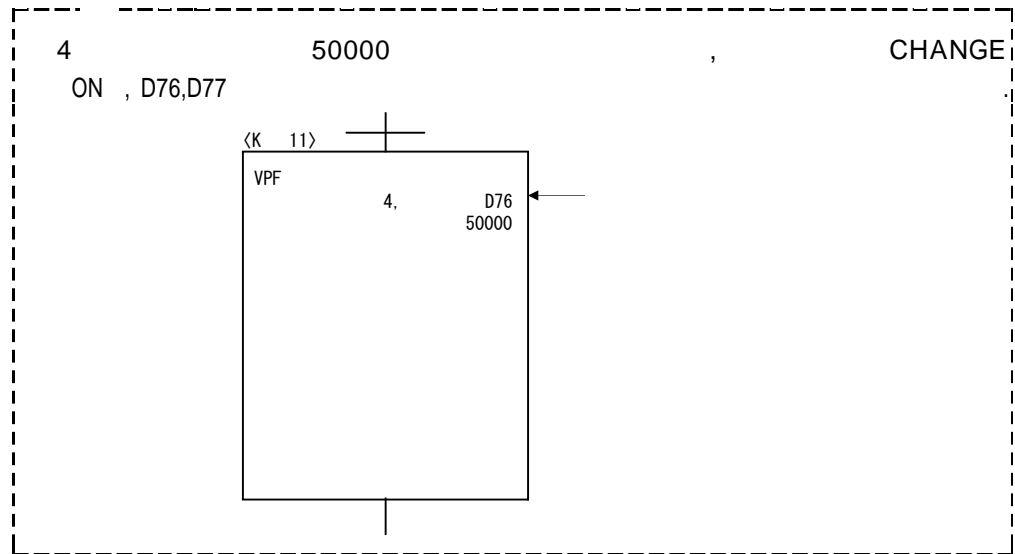
(2)

No.			
1	D16	D17	D16
2	D36	D37	D36
3	D56	D57	D56
4	D76	D77	D76
5	D96	D97	D96
6	D116	D117	D116
7	D136	D137	D136
8	D156	D157	D156
9	D176	D177	D176
10	D196	D197	D196
11	D216	D217	D216
12	D236	D237	D236
13	D256	D257	D256
14	D276	D277	D276
15	D296	D297	D296
16	D316	D317	D316
17	D336	D337	D336
18	D356	D357	D356
19	D376	D377	D376
20	D396	D397	D396
21	D416	D417	D416
22	D436	D437	D436
23	D456	D457	D456
24	D476	D477	D476
25	D496	D497	D496
26	D516	D517	D516
27	D536	D537	D536
28	D556	D557	D556
29	D576	D577	D576
30	D596	D597	D596
31	D616	D617	D616
32	D636	D637	D636

*Q172CPU(N)

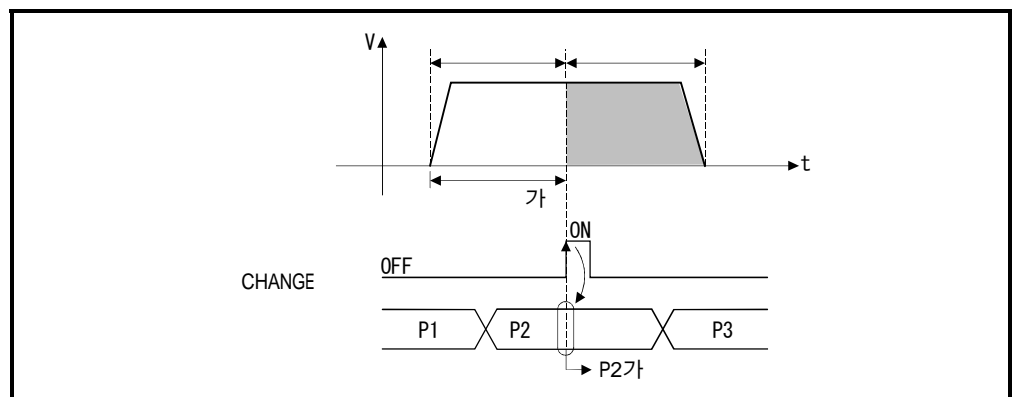
No.1~ No.8

가



(b) SFC

. CHANGE ON ,

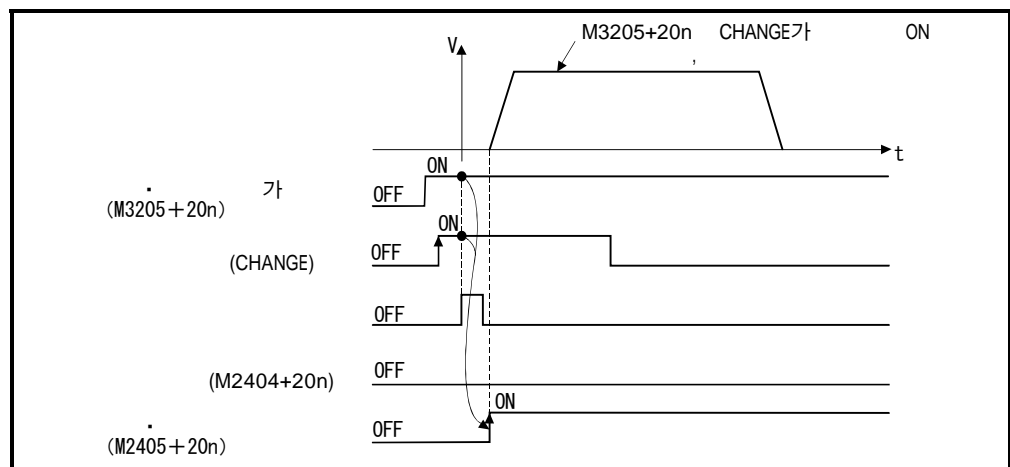
(5) ON
CHANGE
, DOG/CHANGE ON

(3.2.1)

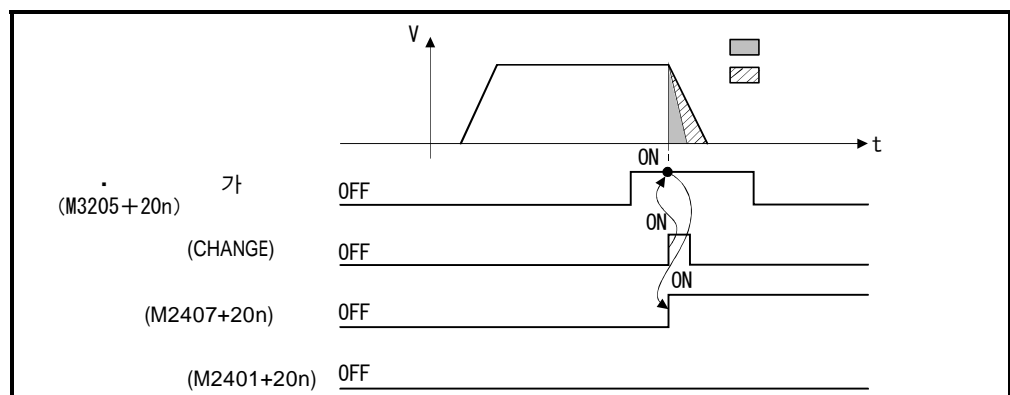
【 】

- (1) CHANGE CHANGE ON
CHANGE 가 ON ,
→
· (M2001+n)가 ON
·
· 가 (M3205+20n)가 ON

- (2) M3205+20n CHANGE 가 ON ,
(M2404+20n) , ON



- (3) () < ()
(a) , CHANGE가
(b) , 가
(M2407+20n)가 ON , [209]
(c) (M2401+20n) ON

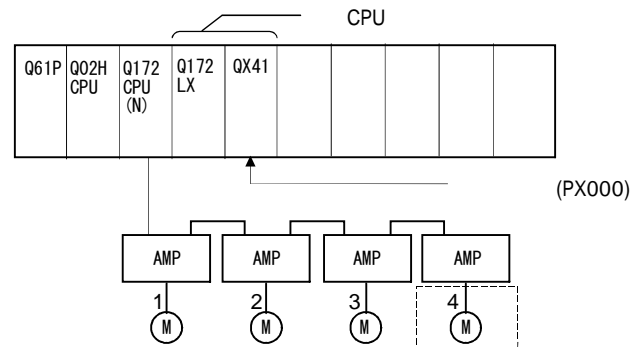


- (4) ,
(:210)가 ,
() TEL : 02-3660-9531

【 】

(1)

4



(2)

(a)

No.	101
	4
	40000
	1000

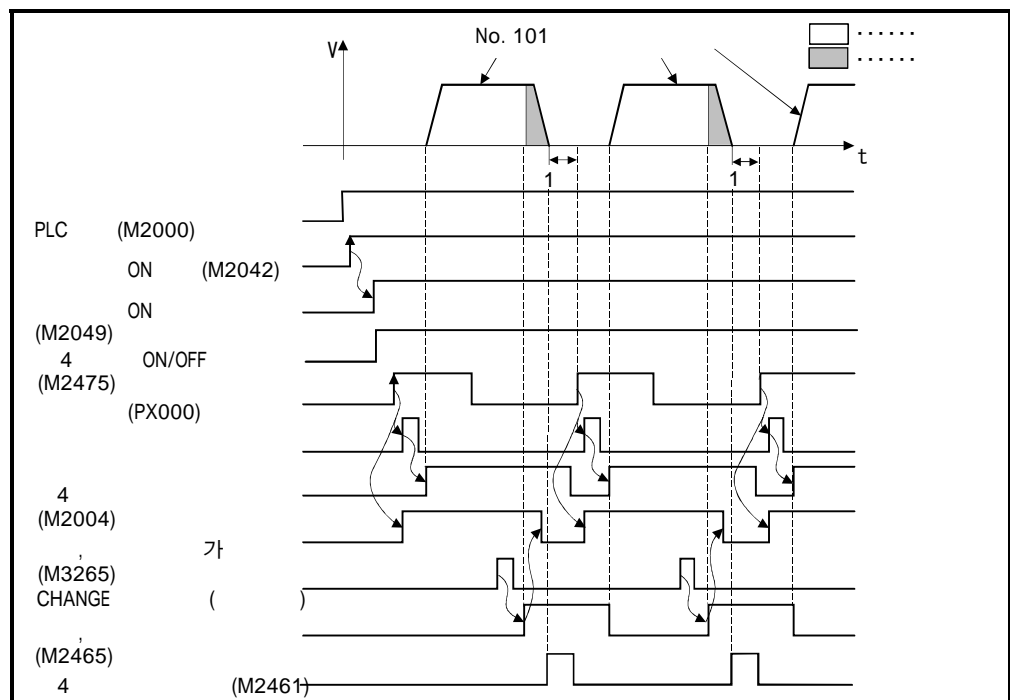
(b)

..... PX000 (OFF→ON)

(c)

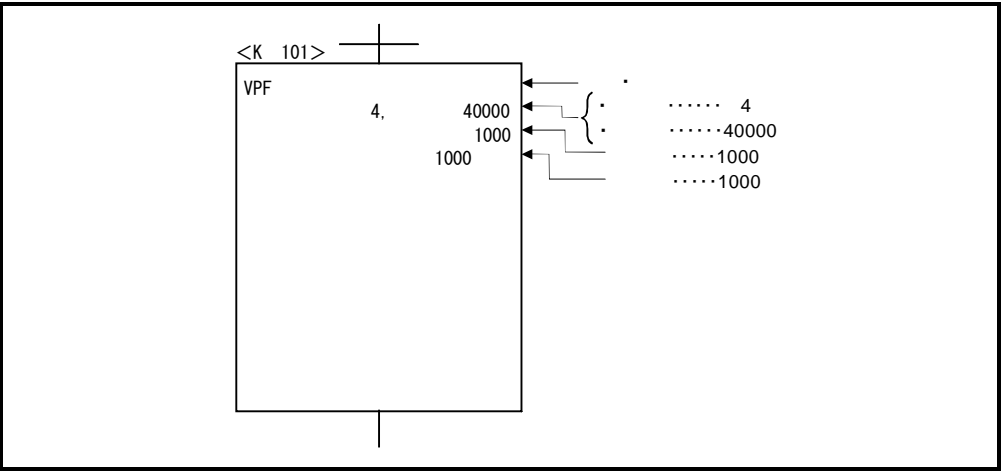
가 M3265

(3)



(4)

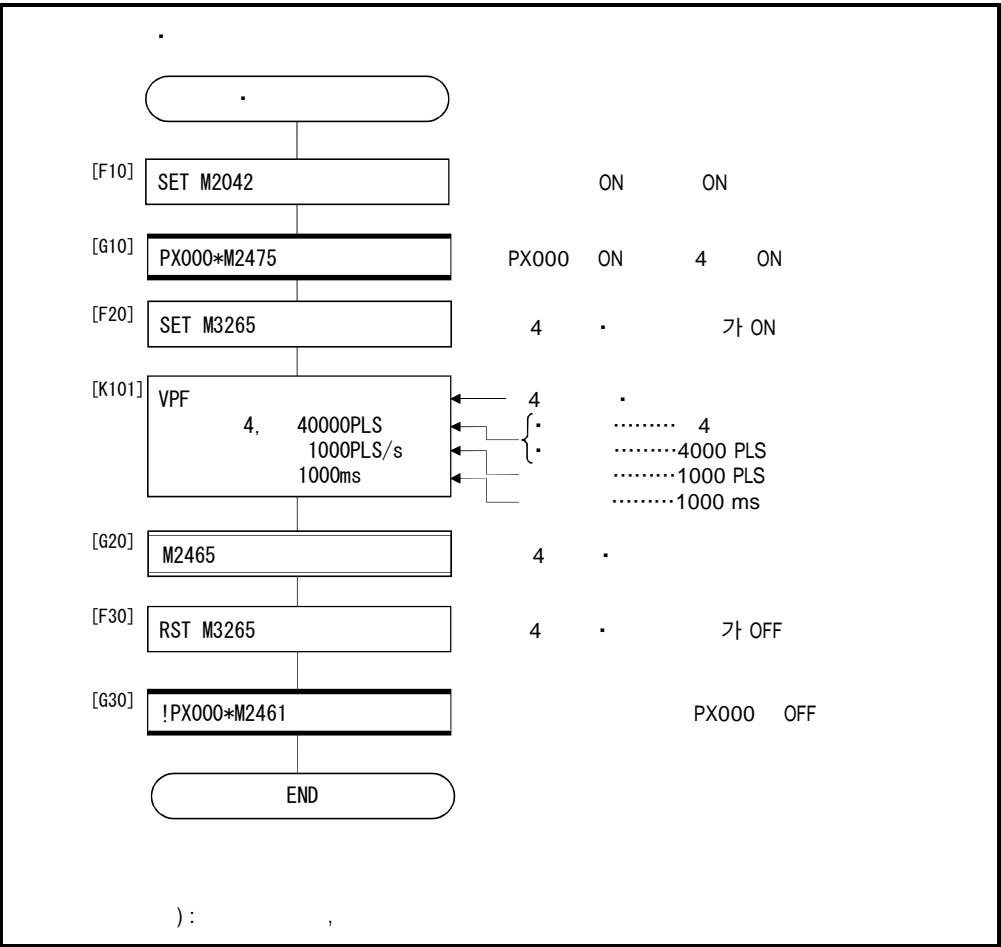
No.101



(5)

SFC

SFC



* : SFC , /

, VPSTART .

[illegible]

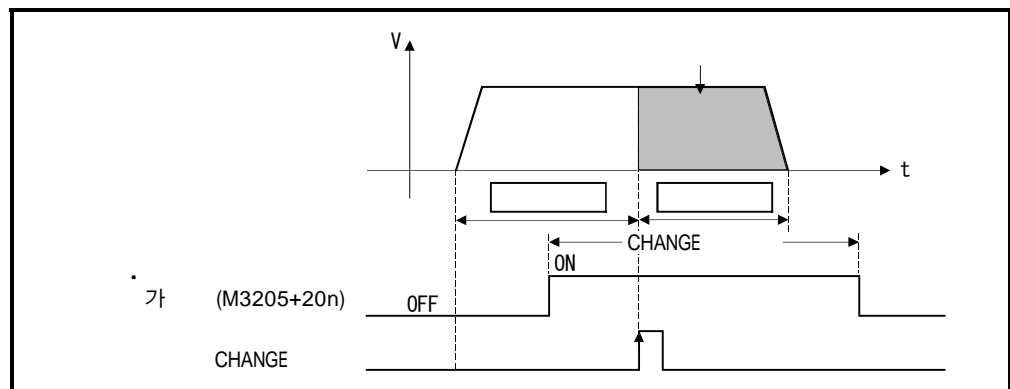
○ :
△ :

【 】

(1) \bullet , \bullet .

(2) VPSTART ,

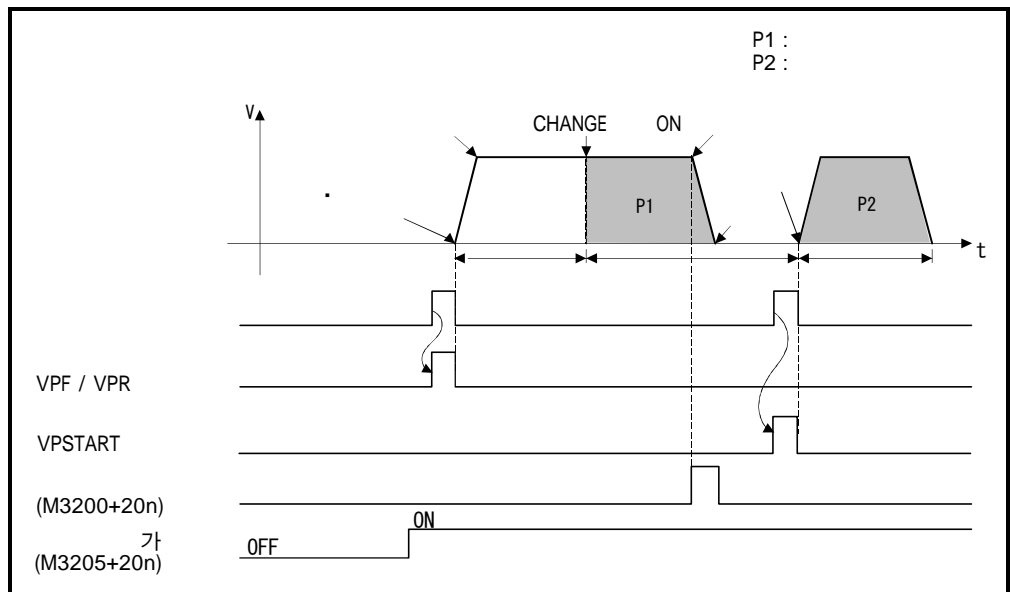
(a) , CHANGE 가 ON

$$\left[\begin{array}{ccc} 6.15.1 & \cdot & \cdot \\ & & \cdot \end{array} \right]$$


6. 27

(b)

$$\boxed{} = \boxed{(P)} - \boxed{(P1)}$$



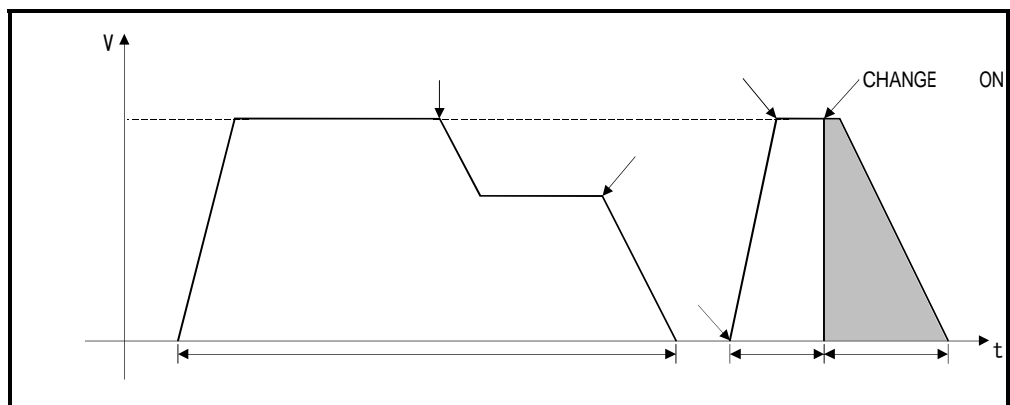
6. 28

(3)

, VPF/VPR

, VPF/VPR

가

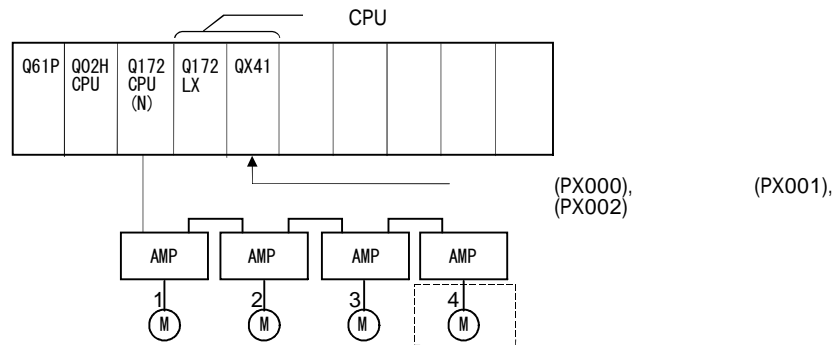


6. 29

【 】

(1)

4



(2)

(a)

	▪	
No.	101	102
	4	4
	40000	—
	1000	—

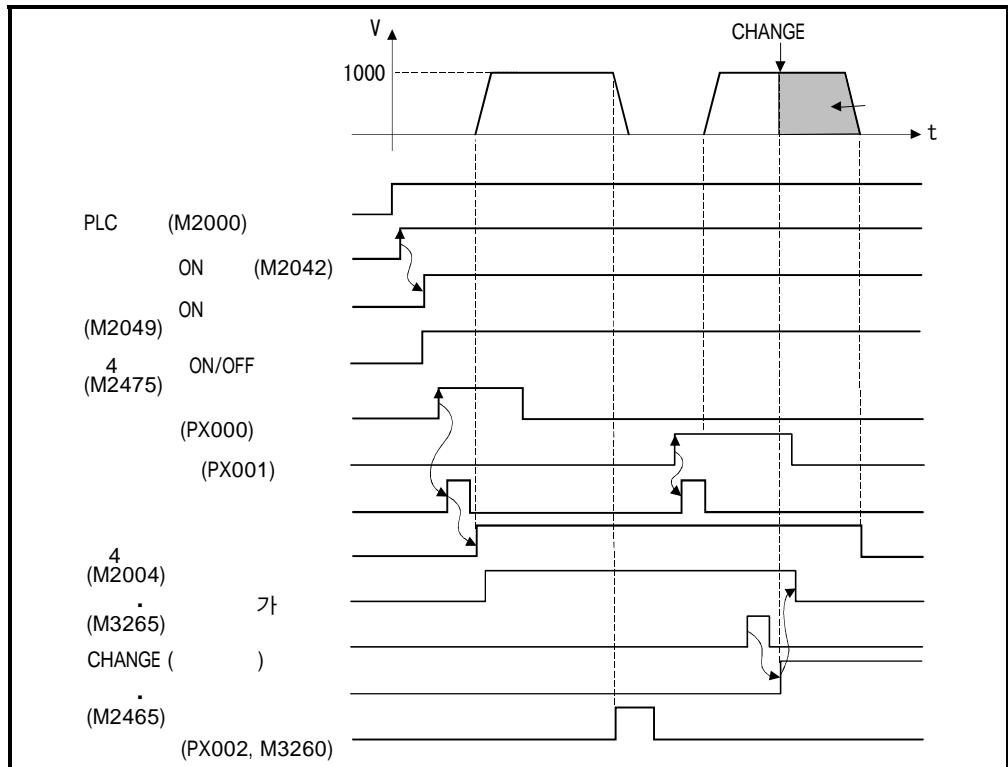
(b) PX000 (OFF→ON)

(c) · 가 · · M3265

(d) PX001 (OFF→ ON)

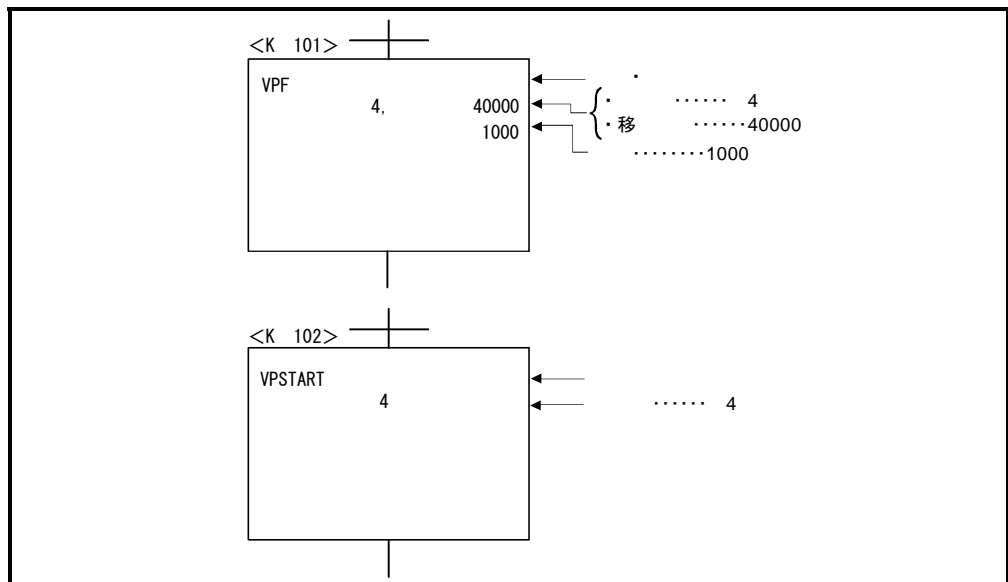
(e) PX002 (OFF→ ON)

(3)



(4)

No.101, No.102

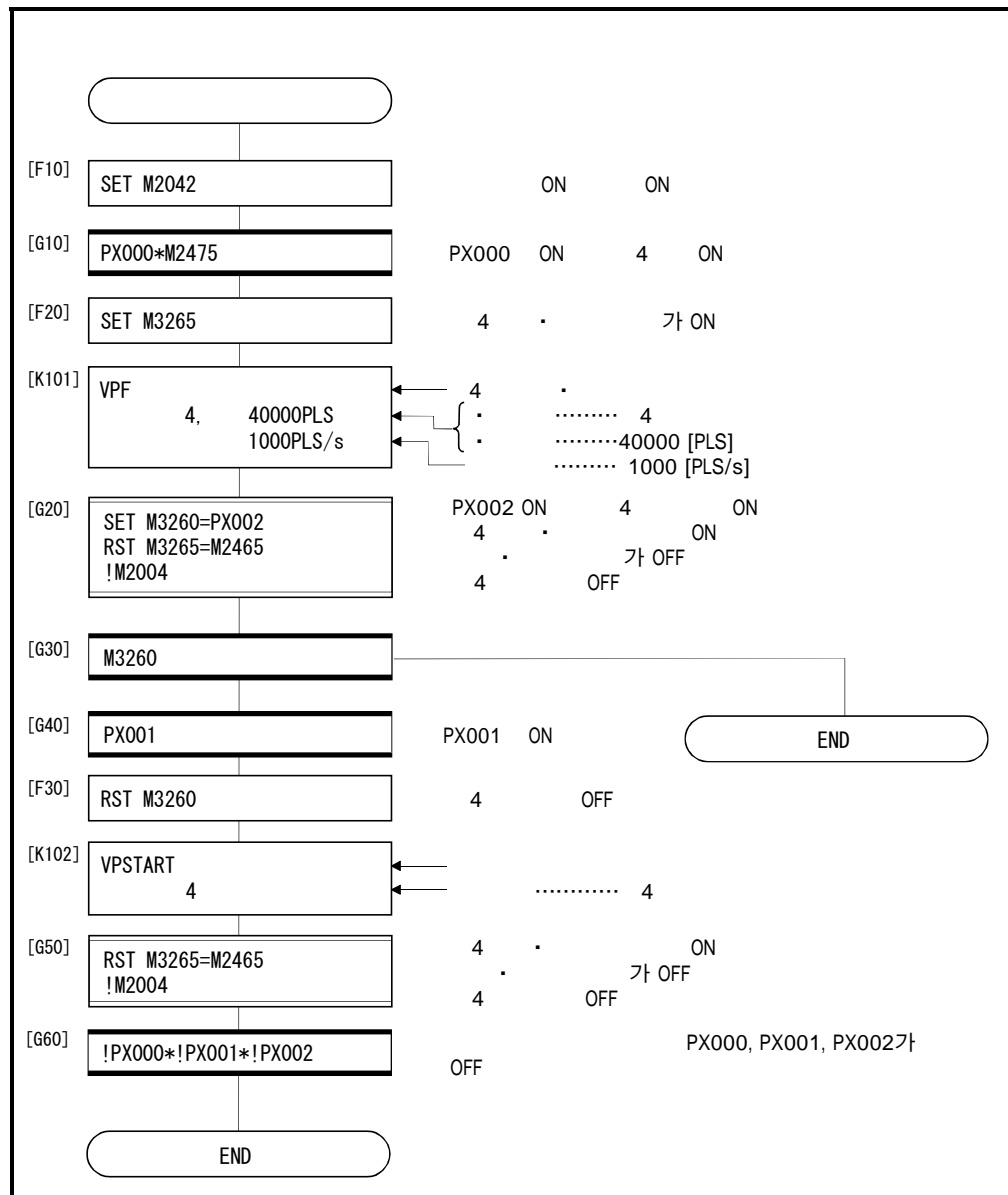


* :

SFC

(5) SFC

SFC



* : SFC , /

6. 16

- (1) 1
- (2)
- (3)
- (4) M

6. 16. 1

				No.	/		M						가			STOP	S	WAIT TION /OFF				
	VSTART	—	—	△								△	△	△	△	△	△	△	△	△		—
	VEND																					
	ABS－1		1																			가
	ABS－2		2																			
	ABS－3		3																			
	INC－1		1	○	○	○	△	△	△											△		
	INC－2		2																			
	INC－3		3																			
	VABS	—	—		○	○	△	△														—
	VINC																					

○ :
△ :

--

1

_____ /

/

1

•

•

1

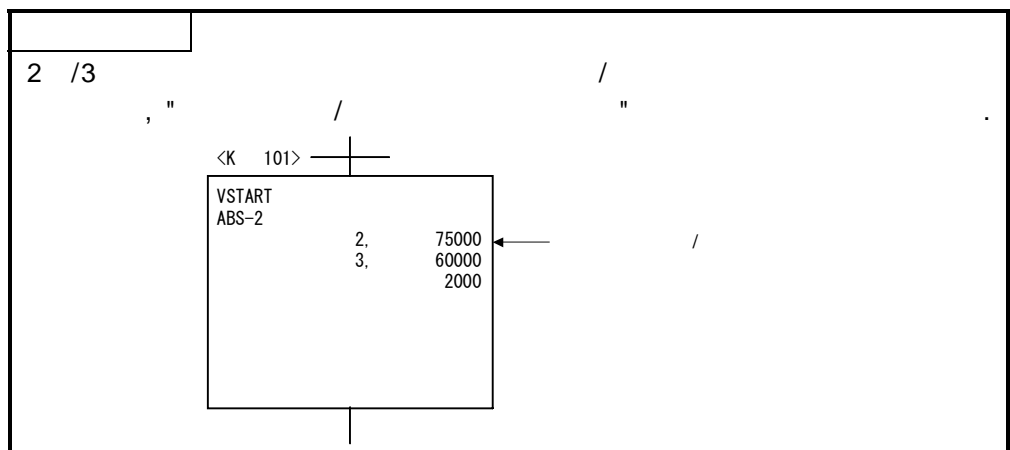
1

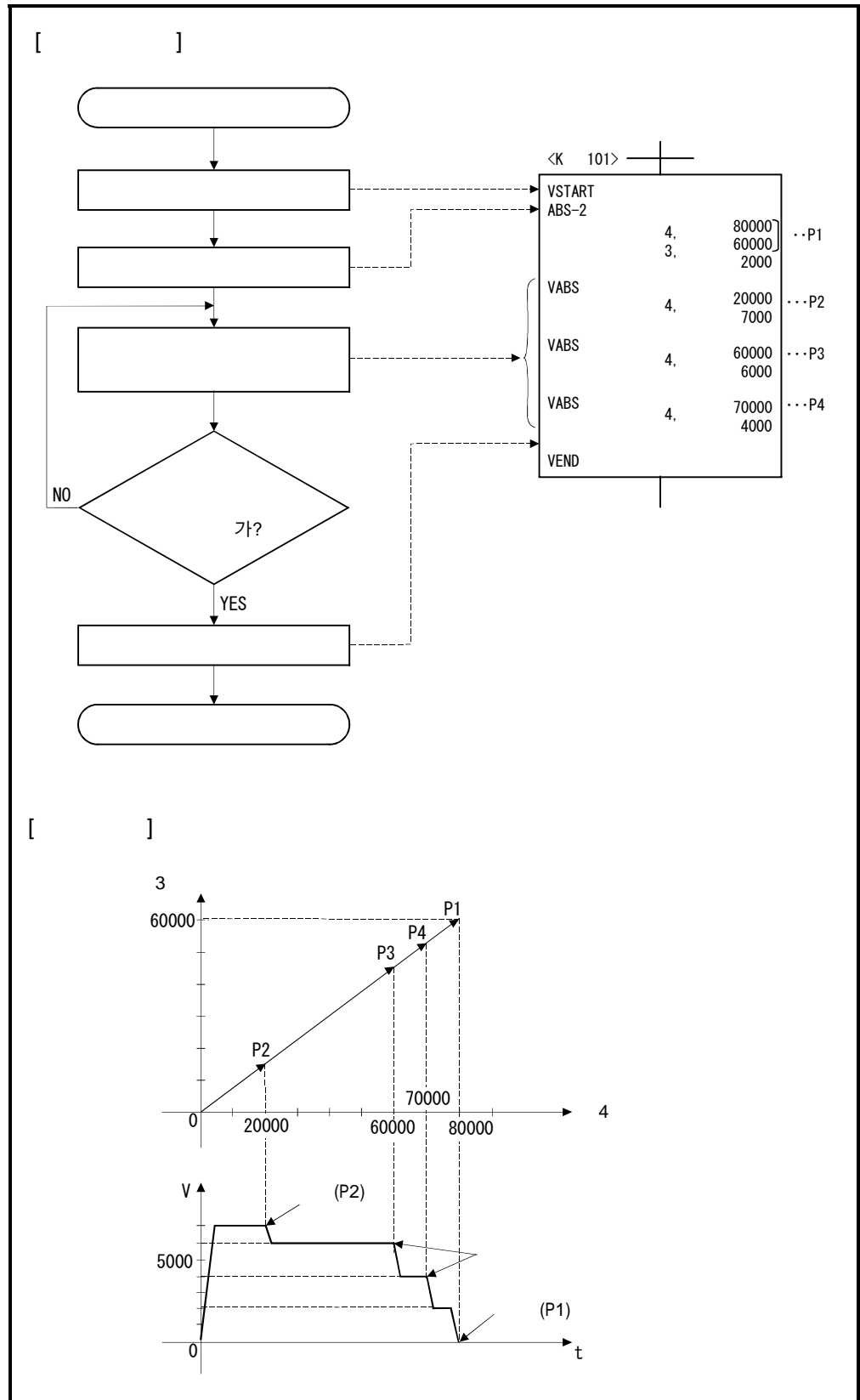
1

1

1

/





3

60000

P1

P4

P3

P2

0

20000

60000

70000

80000

4

V

(P2)

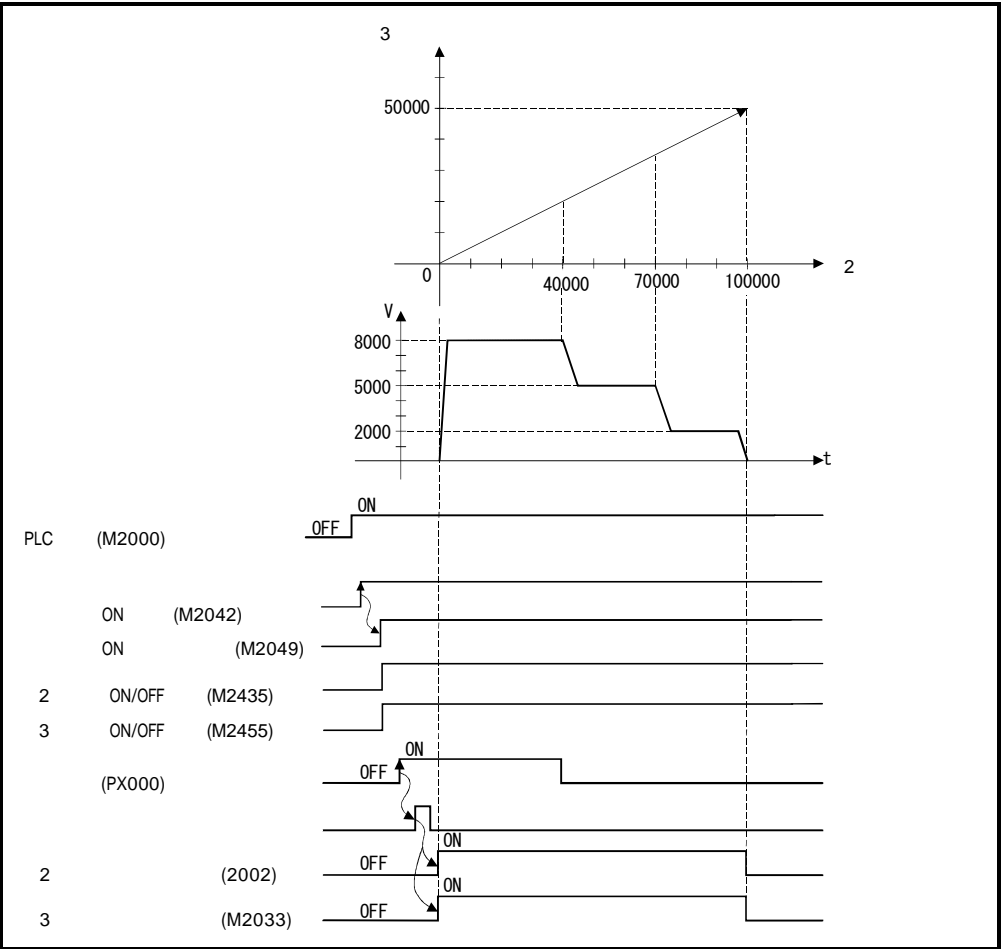
5000

(P1)

0

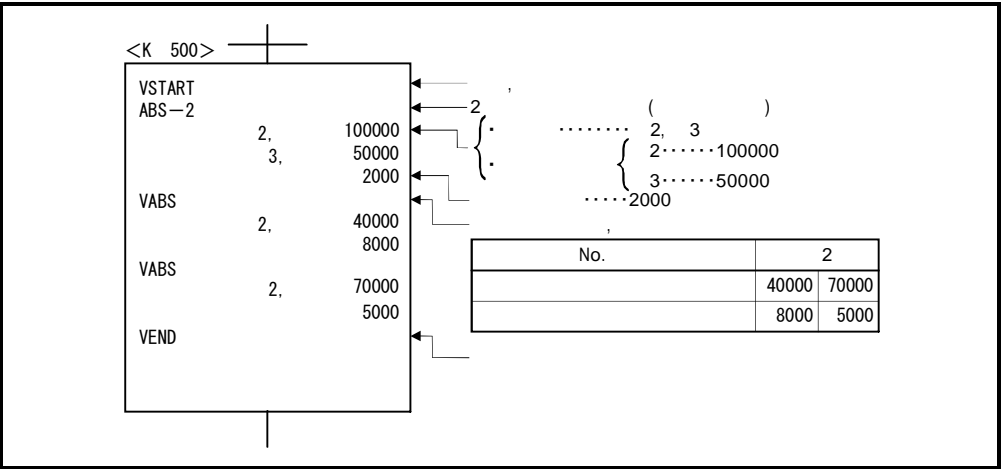
t

(3)



(4)

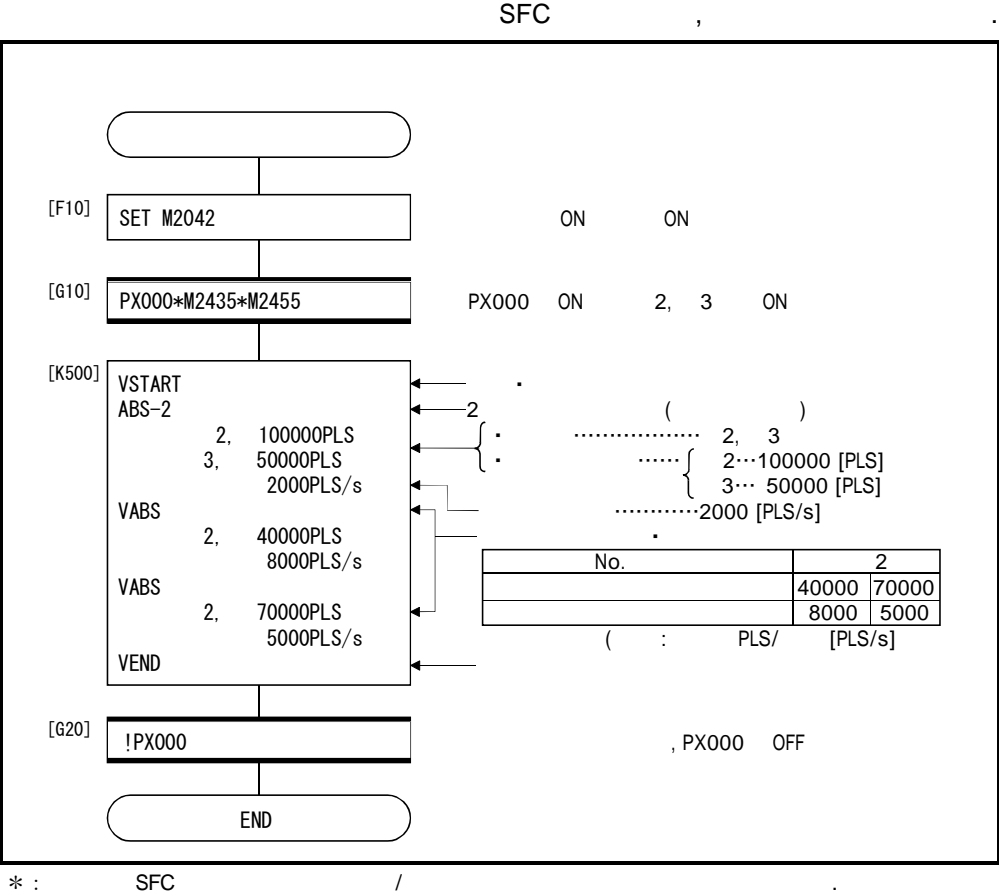
No.500



* :

SFC

(5) SFC



○ :
△ :

--

(a)

"1"

가
(D)
(W)
(#)

(K)
(H)

(a)

,

가

 (Y/PY)

(SP.M)

(B)

(F)

(3) FOR-OFF ()

(a) 가 OFF

(b) , 가 ,

(X/PX)

(Y/PY)

(M)/

(SP.M)

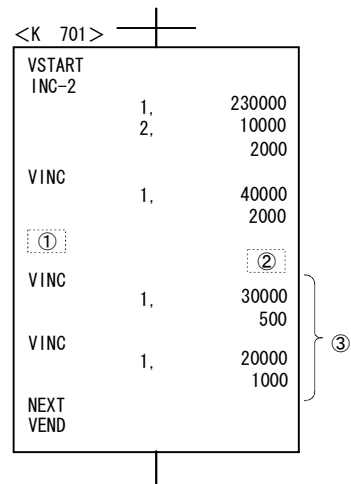
(L)

(B)

(F)

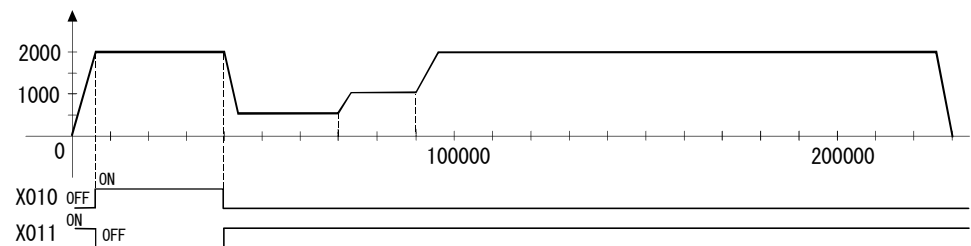
FOR-TIMES, FOR-ON, FOR-OFF

【 】

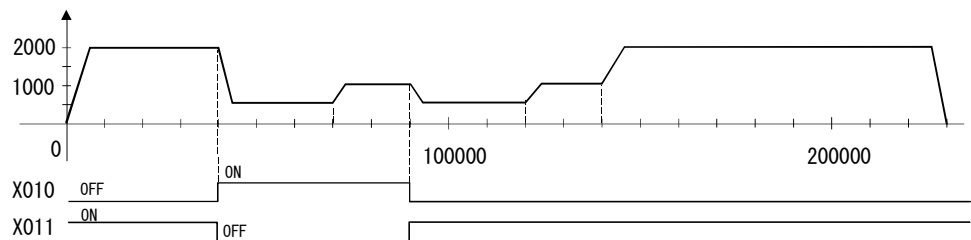


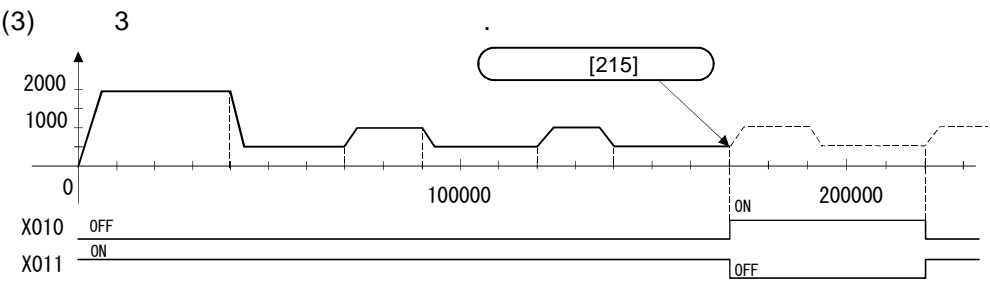
①	②		
	1	2	3
FOR-TIMES	K1	K2	K3
FOR-ON	X010 → ON	1 X010 → ON	3 X010 → ON
FOR-OFF	X011 → OFF	1 X011 → OFF	3 X011 → OFF

(1) 1



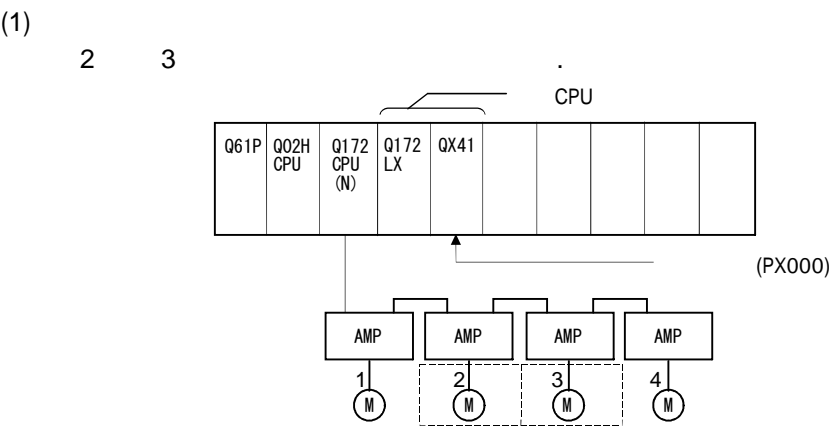
(2) 2





, 가 .

【 】



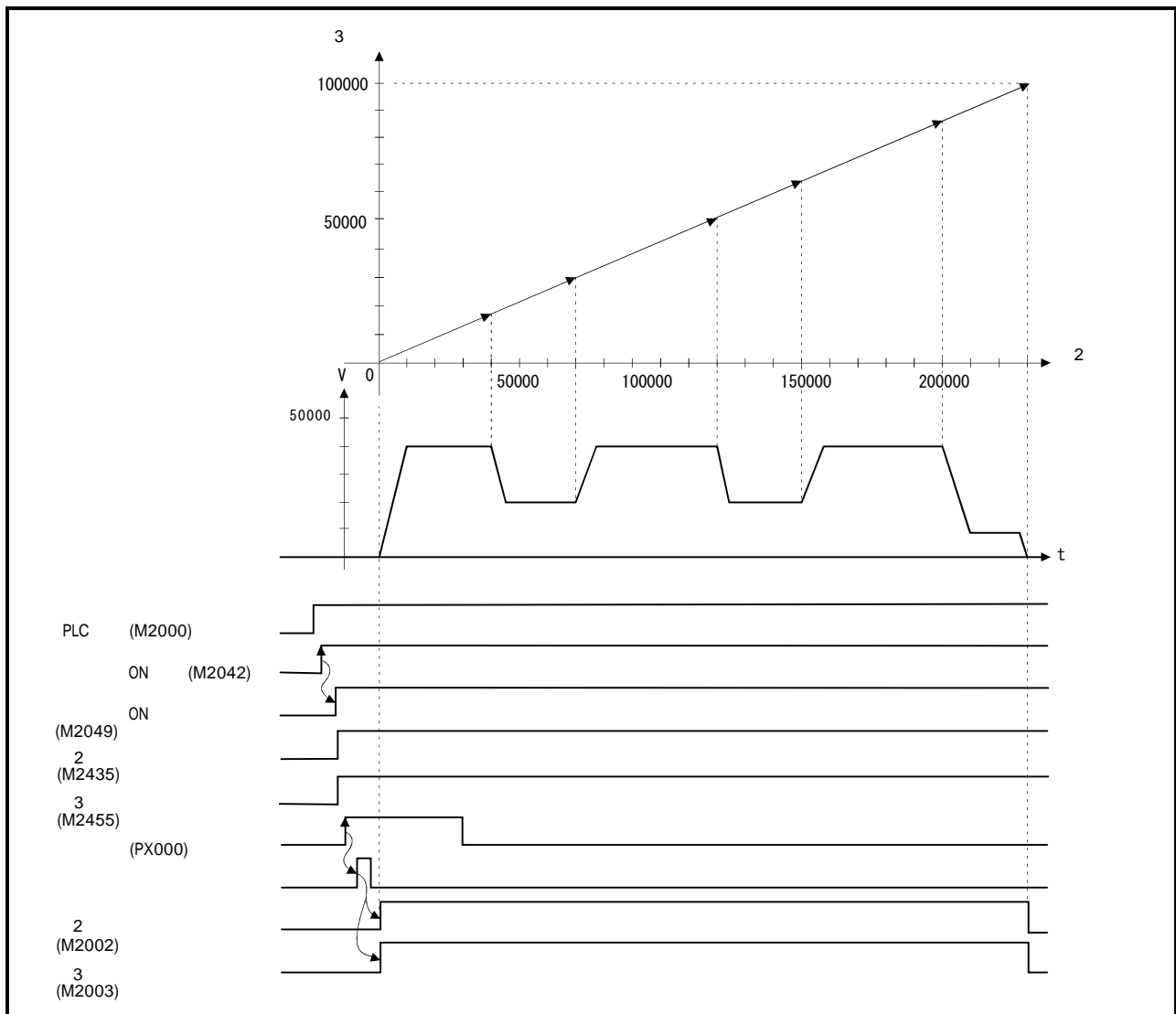
(2)

(a)

No.	501
	2 3
	230000 100000

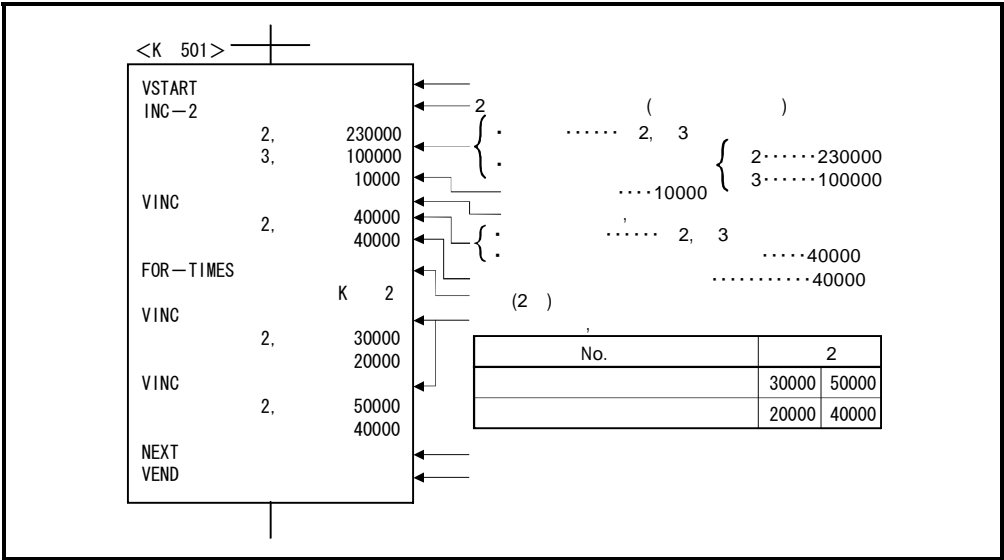
(b) PX000 (OFF → ON)

(3)



(4)

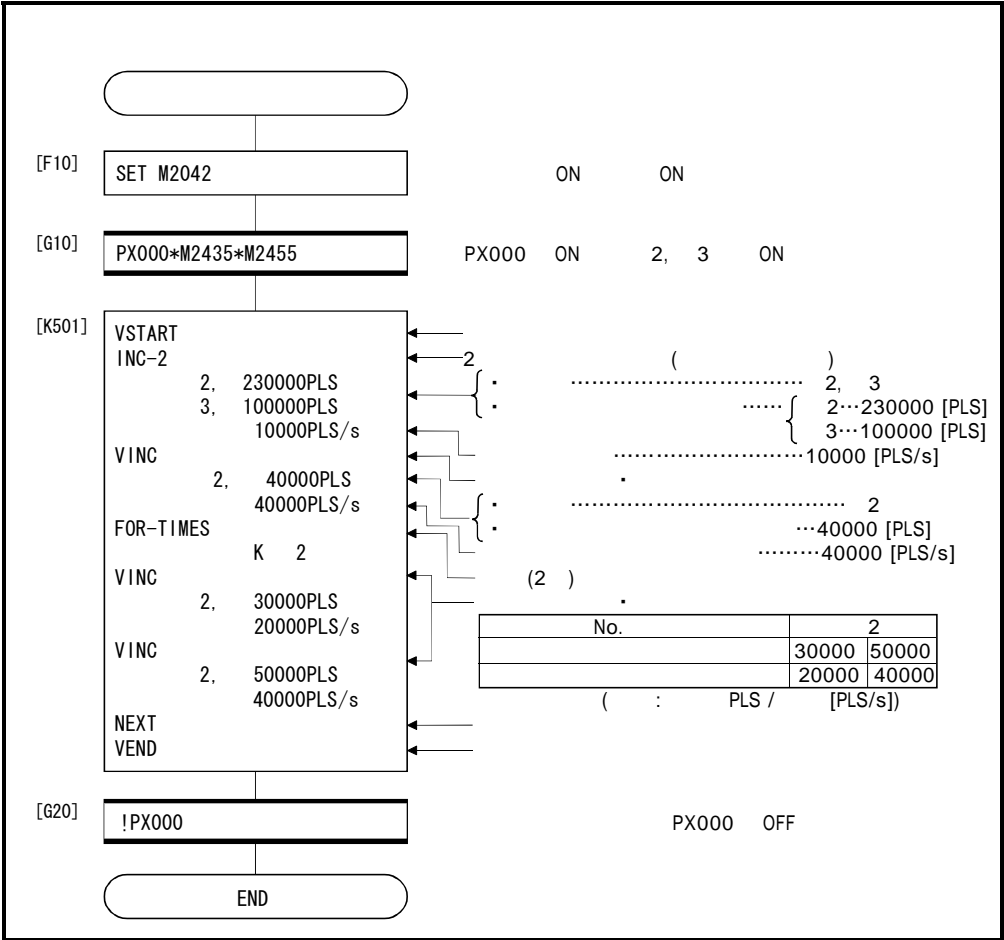
No.501



(5)

SFC

SFC



* : SFC

/

6. 17

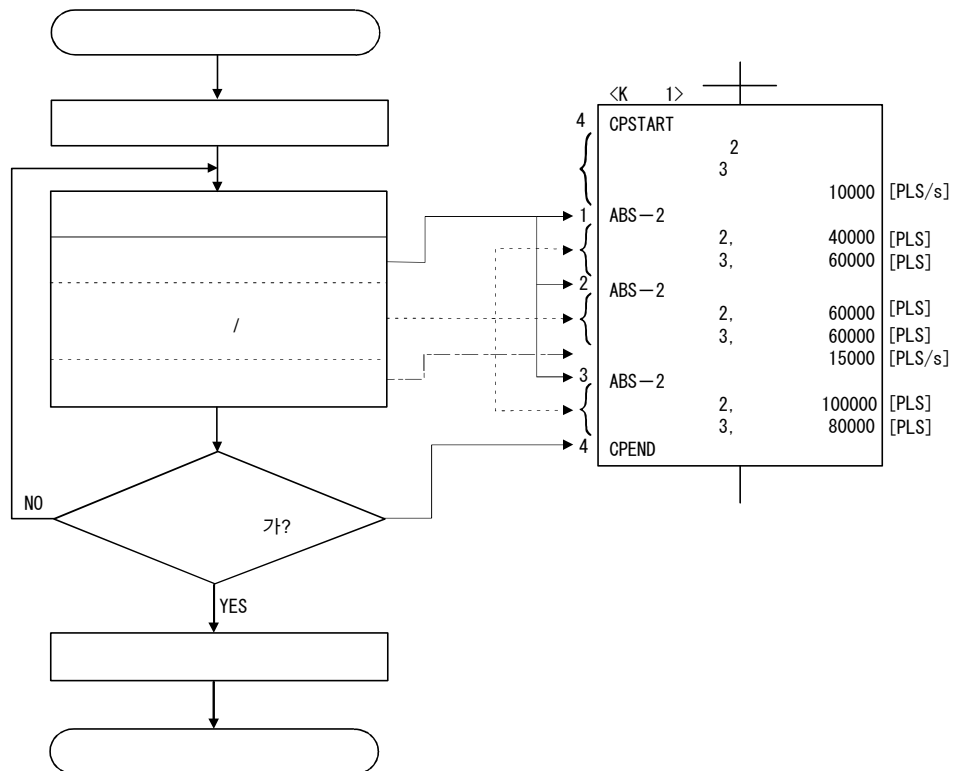
- (1) 1 , , .
- (2) , 가 .
- (3) , .
.
.
.
- (4) , .
- (5) M , .
- (6) 1~4 가 가 .

【 】

[]

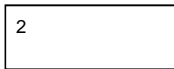
[2

]



;

3



, (ABS) (INC)

•

가 .
가
가
가

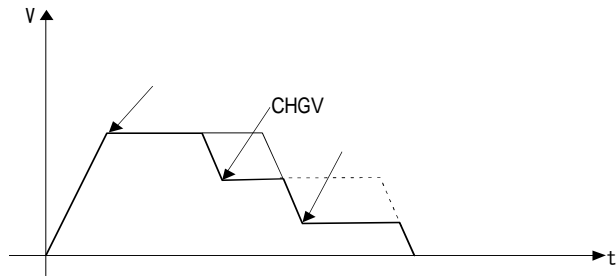
$$, \quad (4.4.3)$$

(b) CHGV

CHGV

, CHGV

CHGV

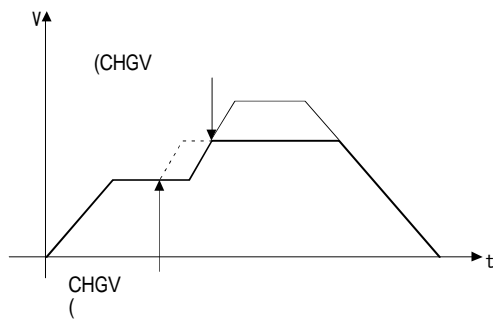
>
가

CHGV

<

CHGV

가



(5) ,
가 () ,
[211] ()가

(6) ,
[106] ,

(7) ,

$\times 0.02 <$ [PLS]

, 가

1 , 280[pps]
가

(3) FOR-OFF ()

(a) 가 OFF ,

(b) , 가 ,

(X/PX)

(Y/PY)

(M)/ (SP.M)

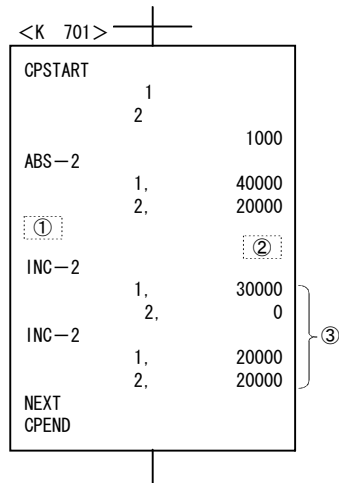
(L)

(B)

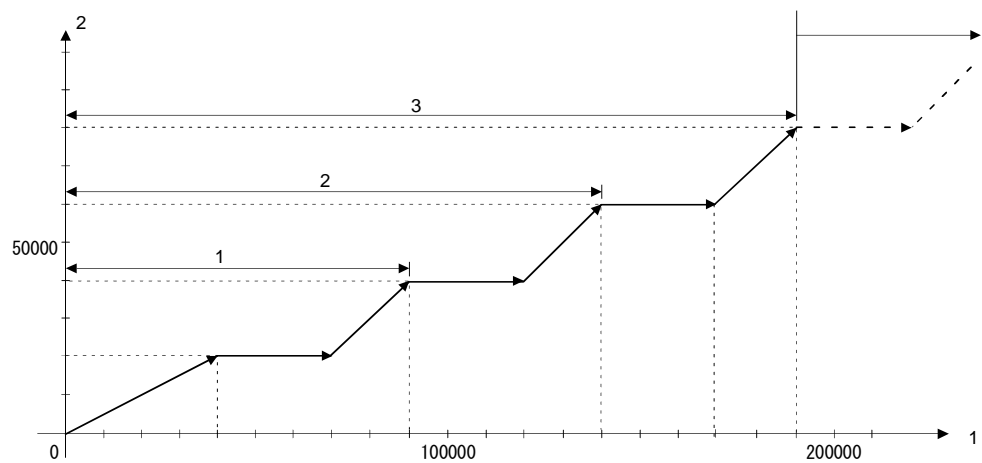
(F)

FOR-TIMES, FOR-ON, FOR-OFF

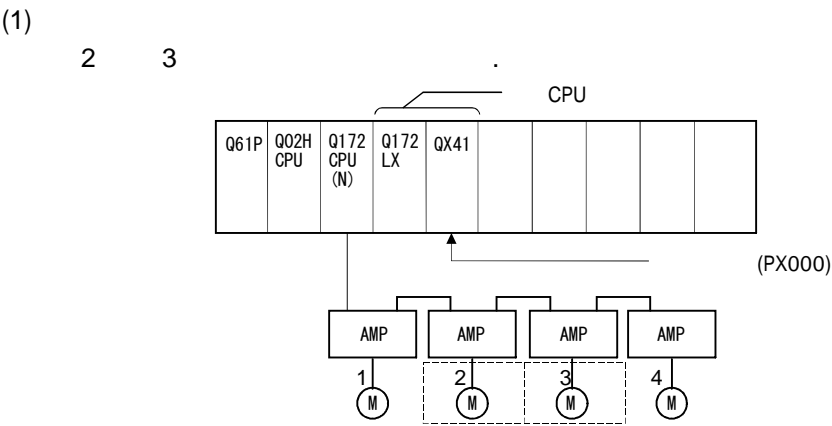
[]



①	②		
	1	2	3
FOR-TIMES	K1	K2	K3
FOR-ON	1 X010→ON	2 X010→ON	3 X010→ON
FOR-OFF	1 X011→OFF	2 X011→OFF	3 X011→OFF



【 】



(2)

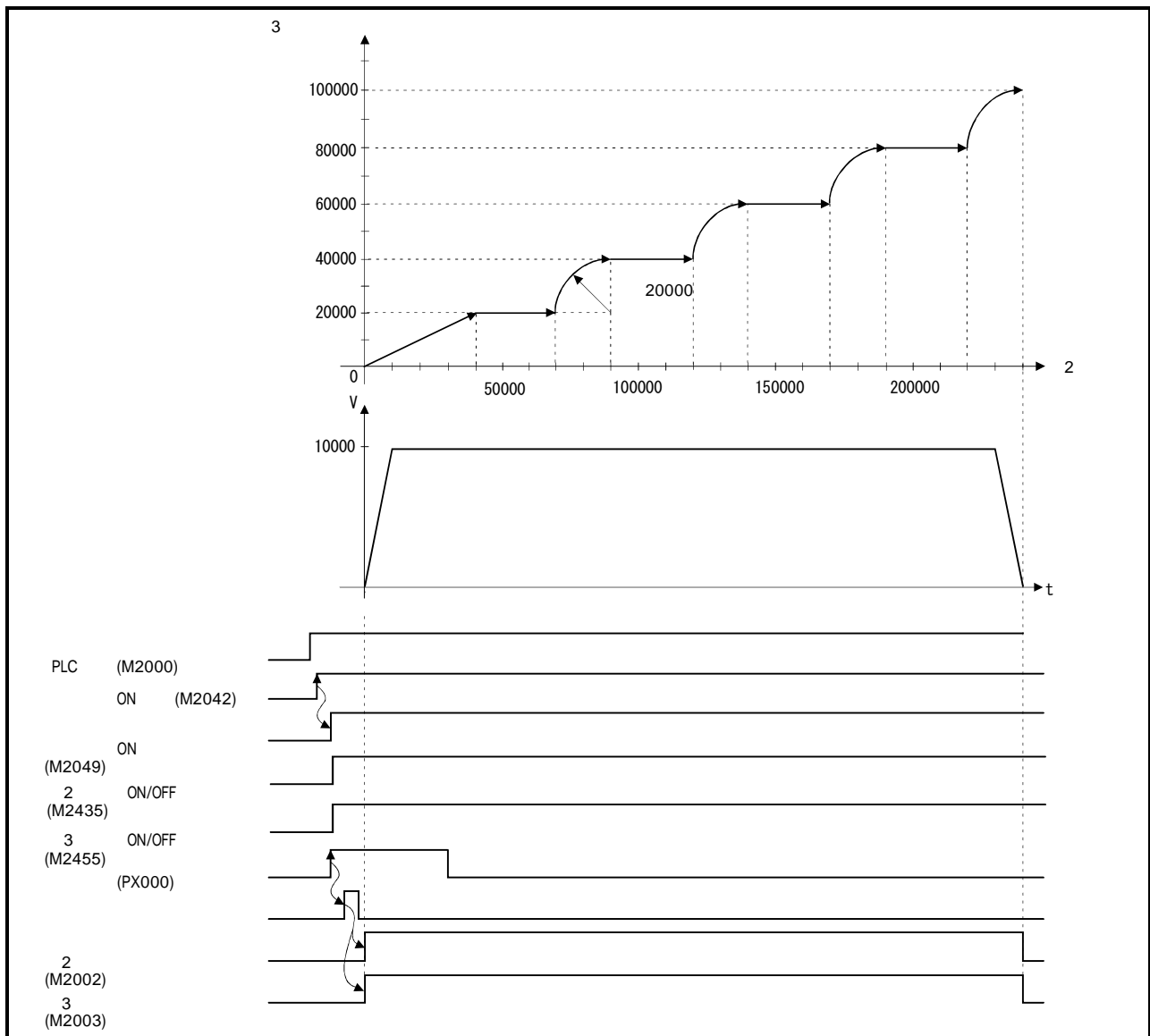
(a)

No.	510
	2, 3
	10000

(b)

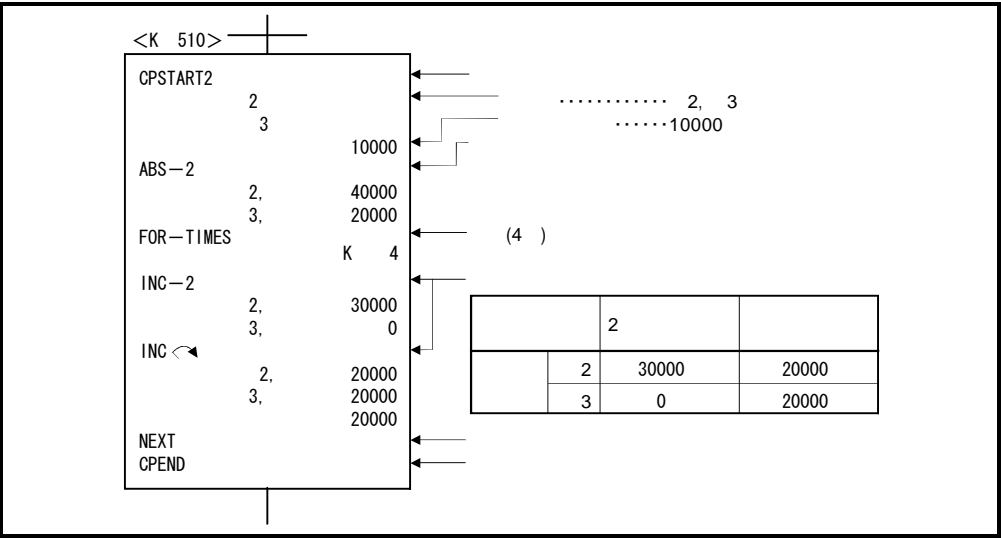
.. PX000 (OFF→ ON)

(3)



(4)

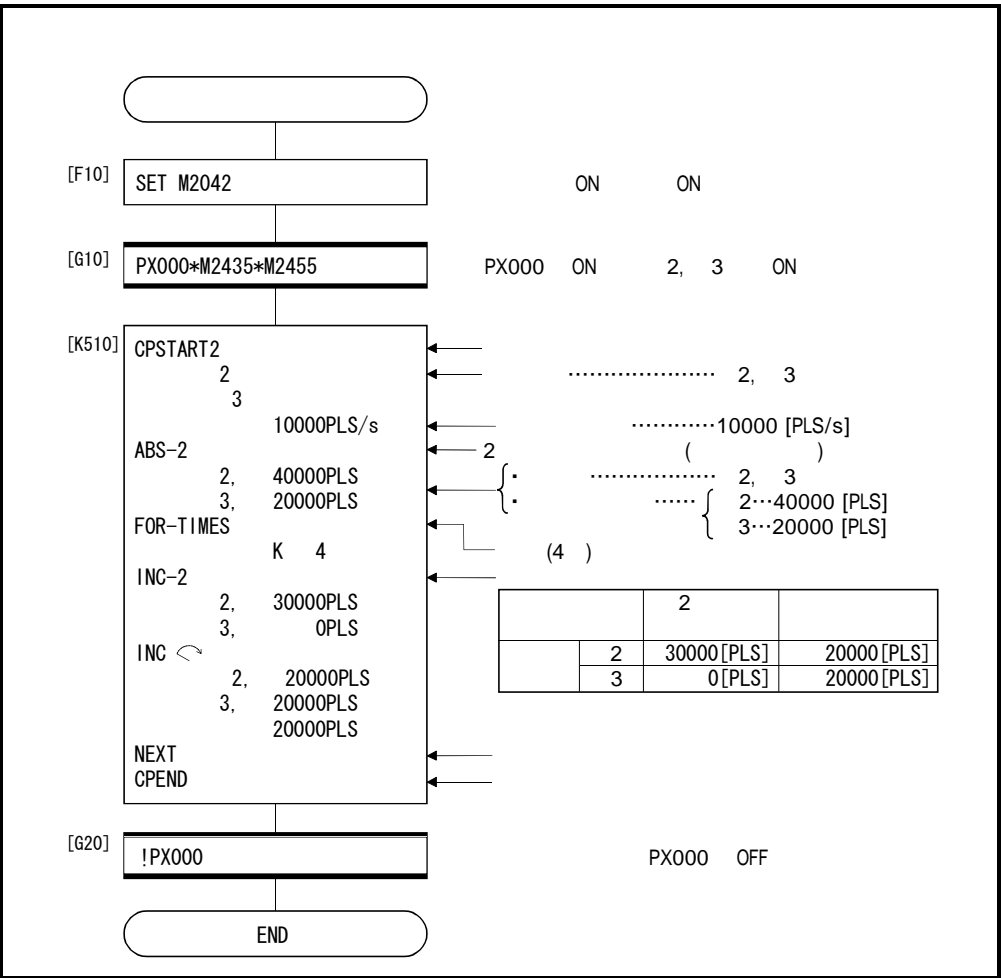
No.510



(5)

SFC

SFC



* : SFC /

6. 17. 2

【 】

(1) 1~4

가

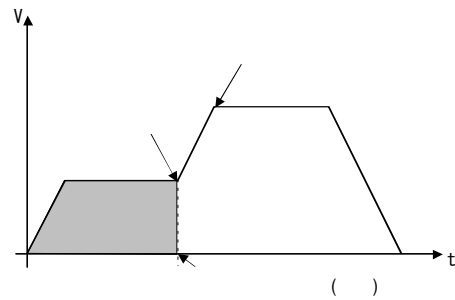
(2)

(3)

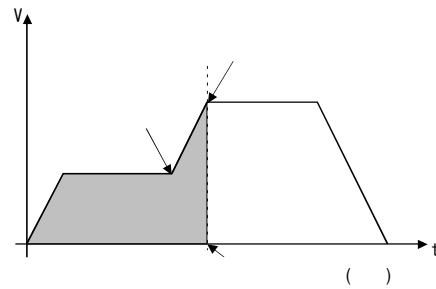
M2040(3.1.3)

ON

ON/OFF
(a) M2040 OFF



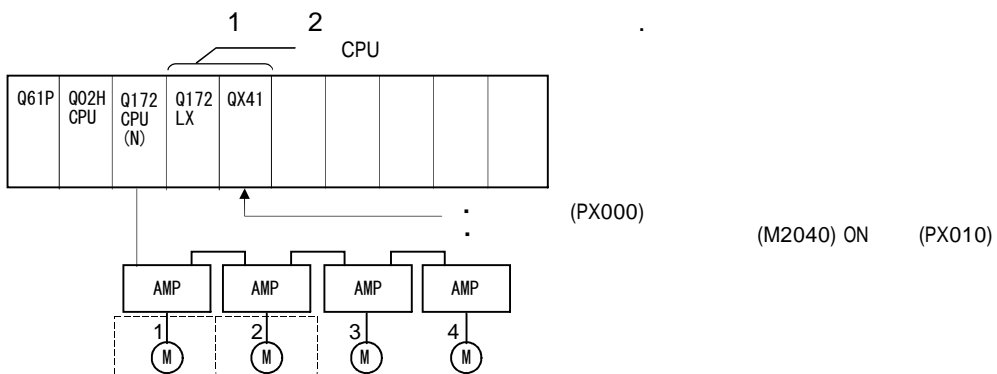
(b) M2040 ON



【 】

, M2040 ON

(1)



(2)

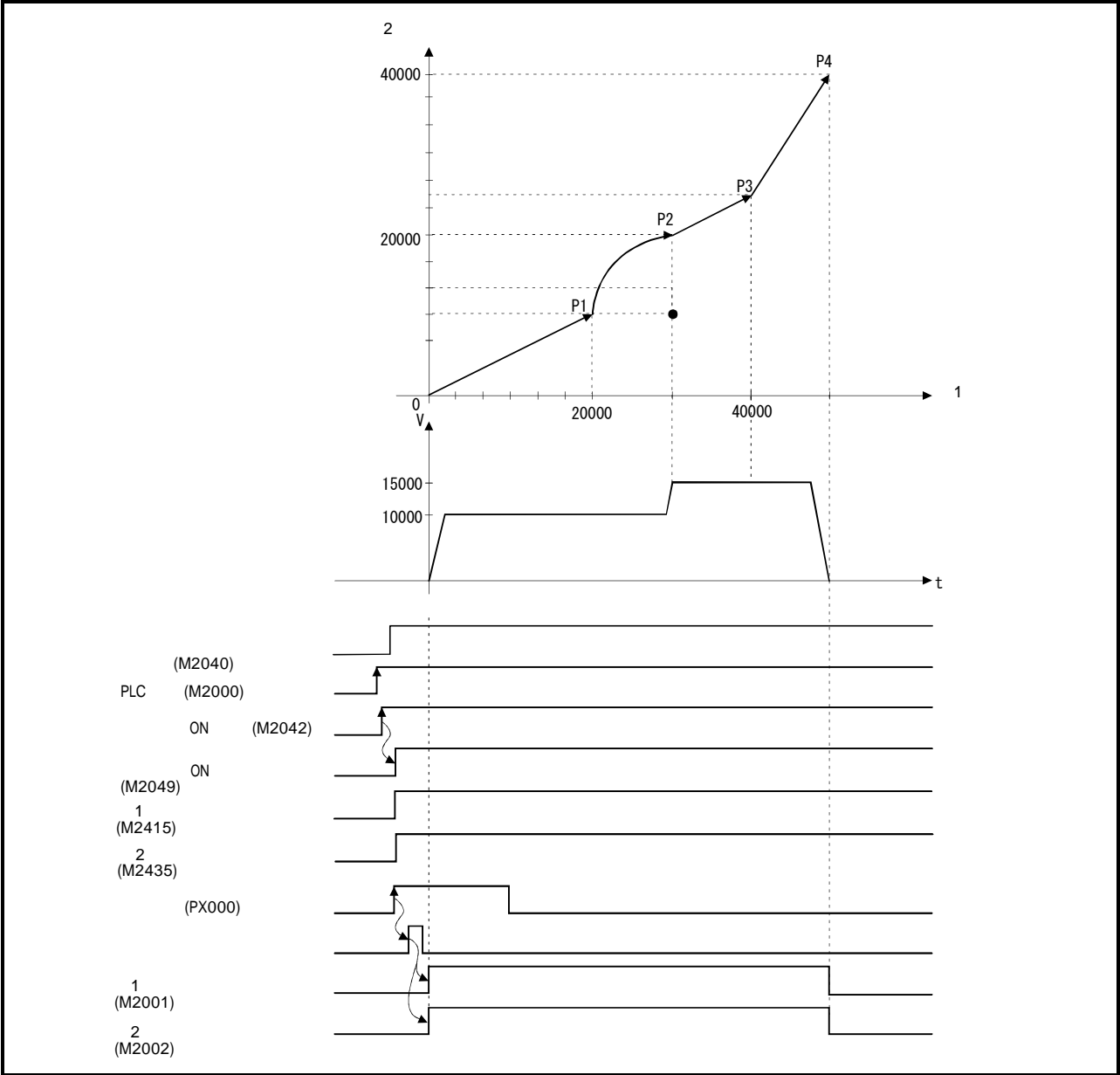
(a)

No.		310			
		10000		15000	
		2		2	2
	1	20000	30000	40000	50000
	2	10000	20000	25000	40000

(b)

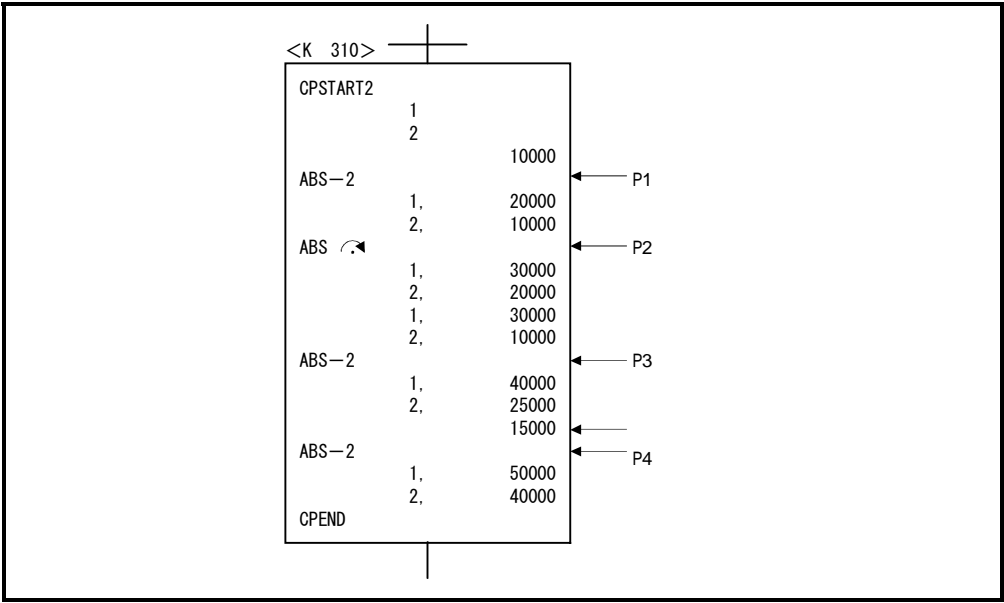
: PX000 (OFF →ON)

(3)



(4)

No.310

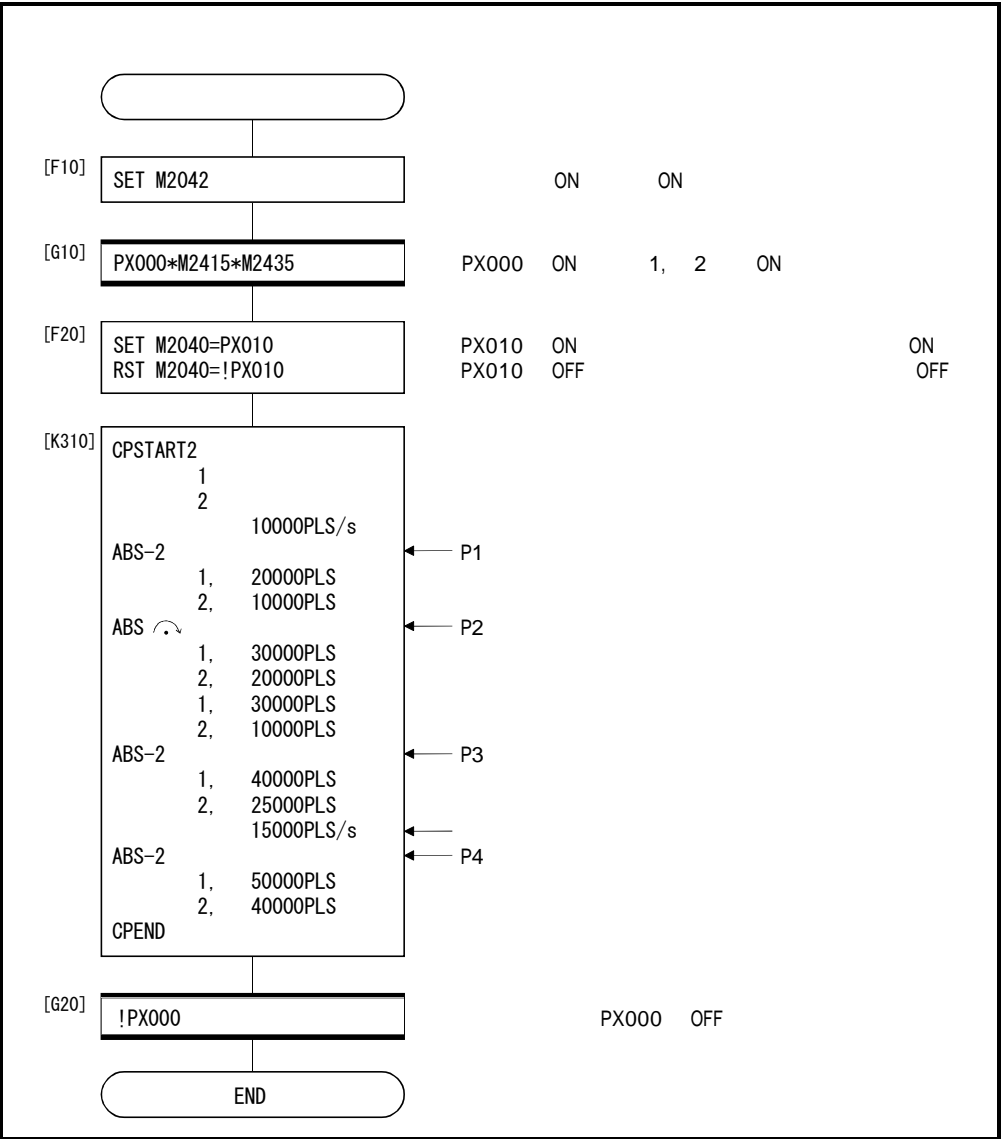


* :

SFC

(5) SFC

SFC



* : SFC /

6. 17. 3 1

				No.	/			M						가				S T O P	S	()			F I N 가	W A I T I O N / O F F
	CPSTART1	—	1	△	○		○						△	△	△	△	△		△		△		가	
	CPEND	—	—					△																
	ABS-1		1		○	○			△	△									△		△			△
	INC-1		1		○	○			△	△									△		△			△

○ :
△ :

【 】

1

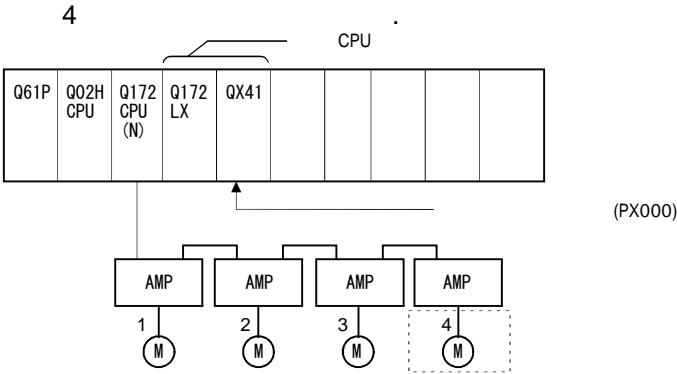
- 1
- (1) CPSTART1
- 1
- No.
- (2) CPEND
- CPSTART1

- (1) ABS-1/INC-1
- 1

, 6. 2 1

【 】

- 1
- (1)



(2)

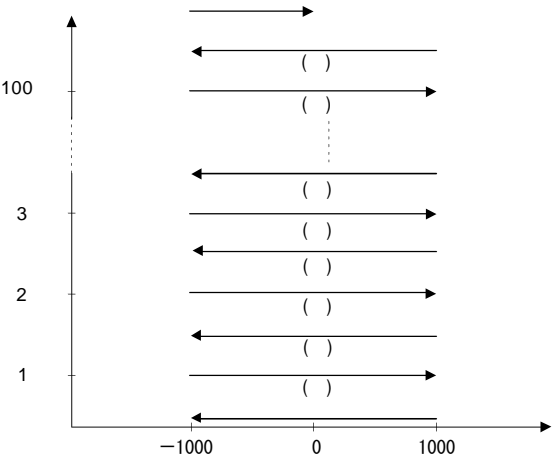
(a)

No.		500
		4
		10000
		100
	P1	-1000
	P2	2000
	P3	-2000
	P4	1000

(b)

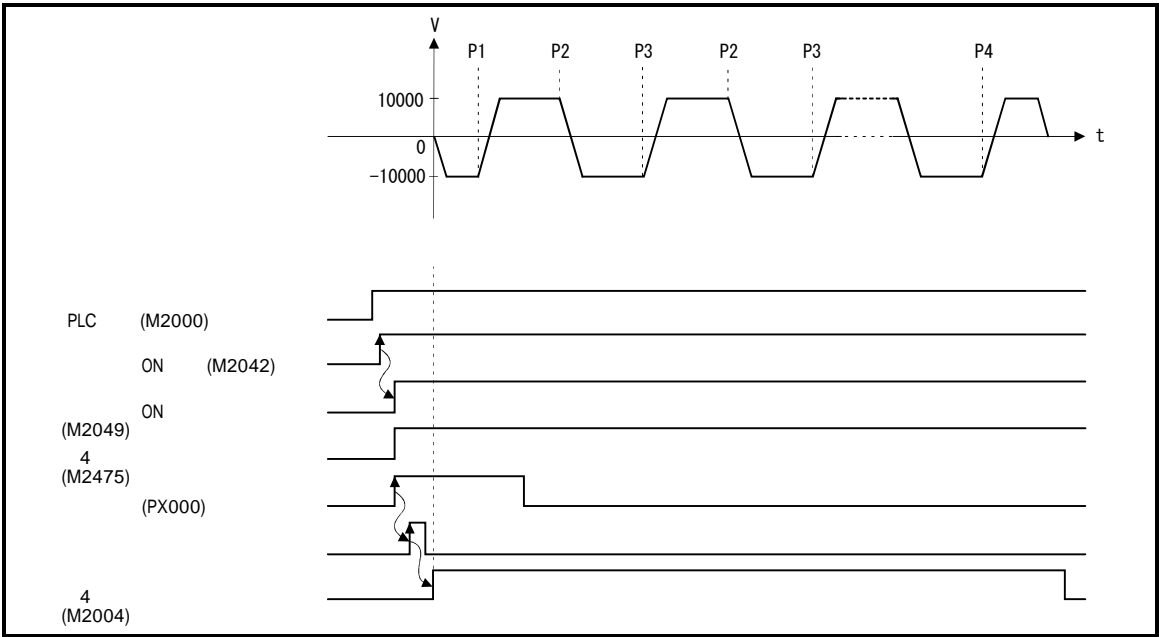
..... PX000 (OFF → ON)

(3)



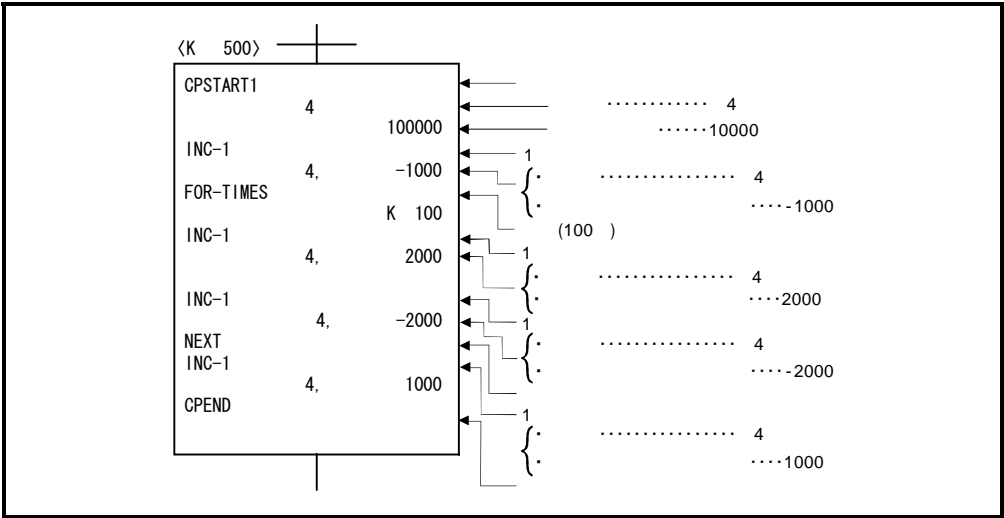
(4)

No.500



(5)

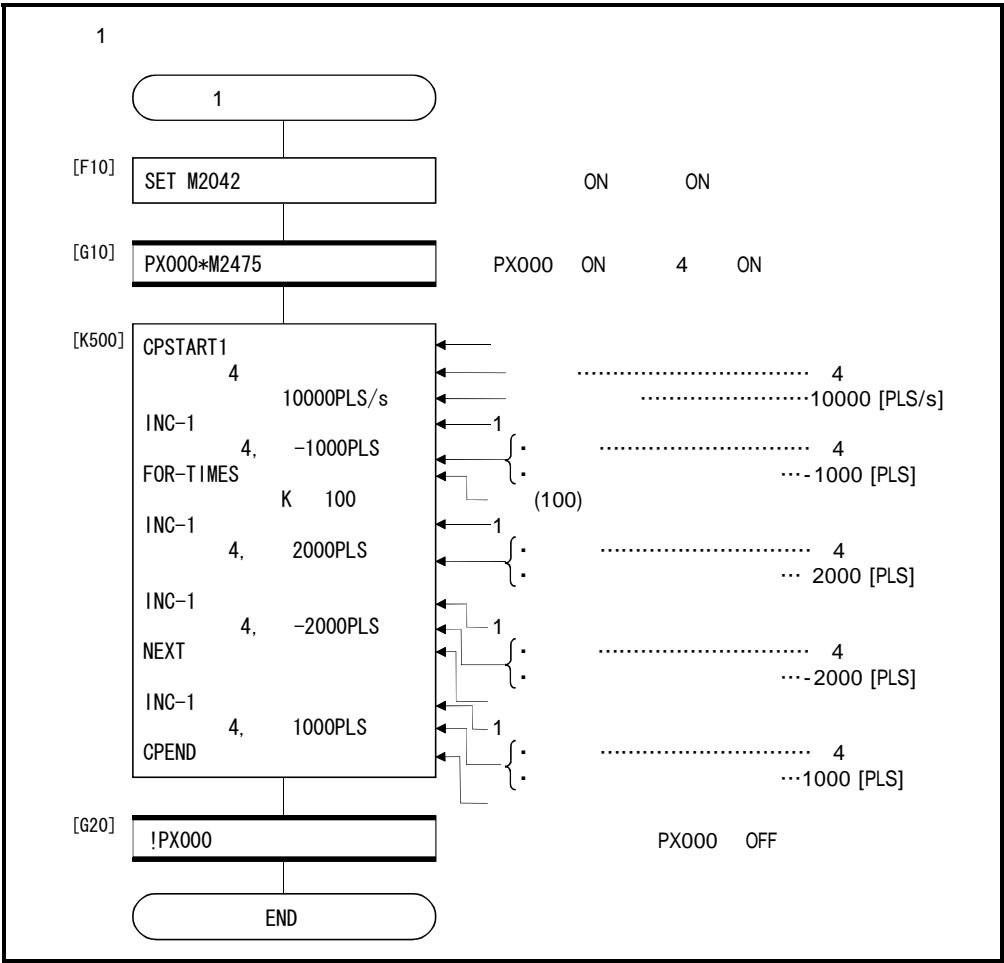
No.500



(6)

SFC

SFC



* : SFC

/

6. 17. 4 2~4

2~4

				No.	/			M						가			STOP	S	()			FIN 가	WAIT ION / OFF	
	CPSTART2	—	2	△	○	○							△	△	△	△	△	△	△	△	△	△		
	CPSTART3		3	△	○	○							△	△	△	△	△	△	△	△	△	△		
	CPSTART4		4	△	○	○							△	△	△	△	△	△	△	△	△	△		
	CPEND		—				△																	
	ABS — 2		2		○	○		△	△										△	△	△			
	ABS — 3		3		○	○		△	△									△	△	△	△			
	ABS — 4		4		○	○		△	△									△	△	△	△			
	ABS ↗		2		○	○		△	△	○									△	△	△	△		
	ABS ↖																							
	ABS ↘				○	○		△	△	○									△	△	△	△		
	ABS ↙																							
	ABS ↗		2		○	○		△	△		○								△	△	△	△		
	ABS ↖																							
	ABS ↘			○	○		△	△		○								△	△	△	△			
	ABS ↙																							
		INC — 2		2		○	○		△	△									△	△	△	△		
INC — 3		3			○	○		△	△									△	△	△	△			
INC — 4		4			○	○		△	△									△	△	△	△			
INC ↗		2		○	○		△	△	○									△	△	△	△			
INC ↖																								
INC ↘				○	○		△	△		○								△	△	△	△			
INC ↙																								
INC ↗				○	○		△	△		○								△	△	△	△			
INC ↖																								
INC ↘				○	○		△	△		○								△	△	△	△			
INC ↙																								
INC ↗				○	○		△	△		○								△	△	△	△			
INC ↖																								

○ :
△ :

【 】

2~4

2~4

(1) CPSTART2

2

No.

(2) CPSTART3

3

No.

(3) CPSTART4

4

No.

(4) CPEND

CPSTART2 / CPSTART3 / CPSTART4

(1) ABS-2, INC-2

2

, 6. 3 2

(2) ABS-3, INC-3

3

, 6. 4 3

(3) ABS-4, INC-4

4

, 6. 5 4

(4) ABS/INC ↻

, 6. 6

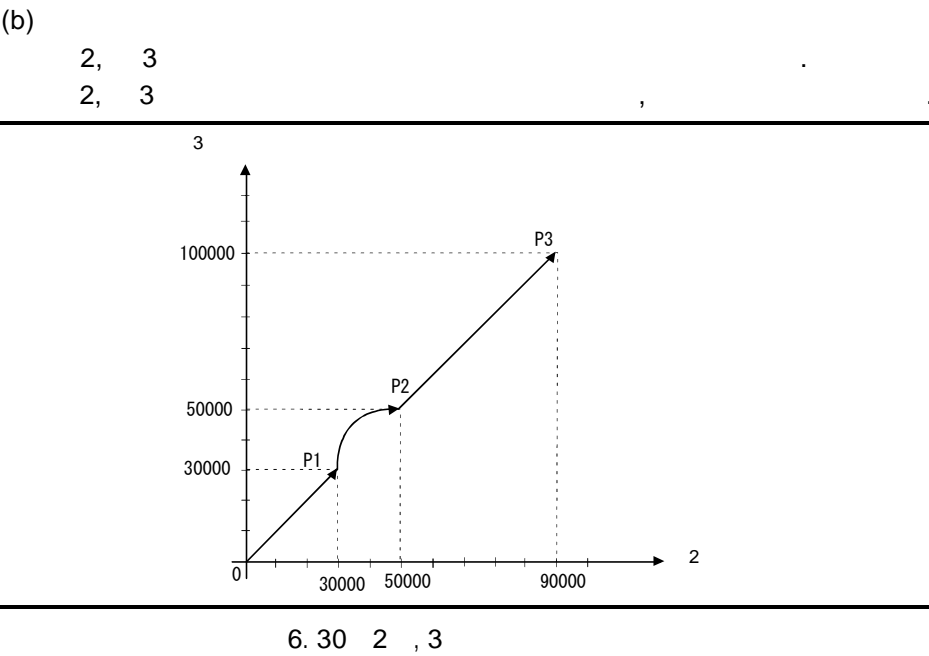
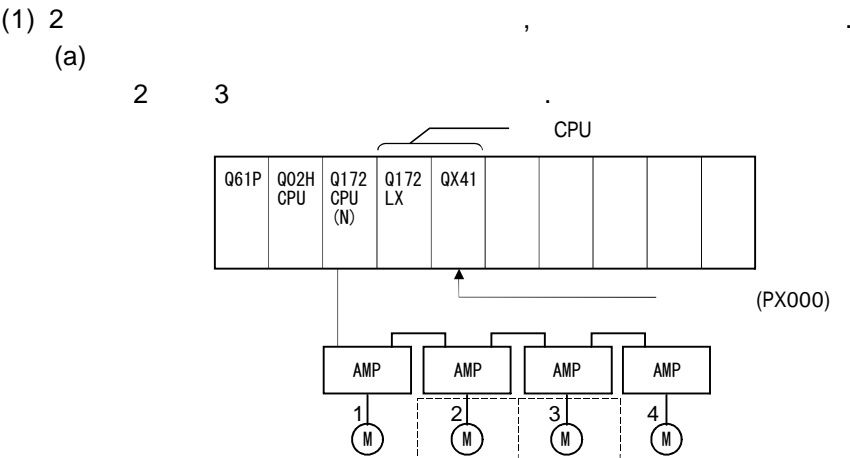
(5) ABS/INC ↻, ABS/INC ↻, ABS/INC ↻, ABS/INC ↻

, 6. 7

(6) ABS/INC ↻, ABS/INC ↻

, 6. 8

【 】



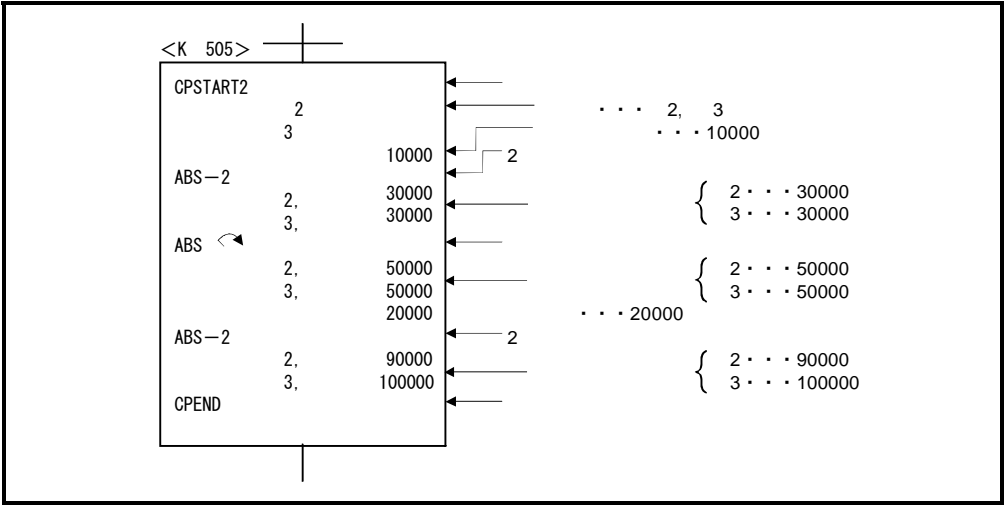
(c)

No.		505		
		10000		
		2		2
	2	30000	50000	90000
	3	30000	50000	100000

∴ PX000 (OFF→ ON)

(d)

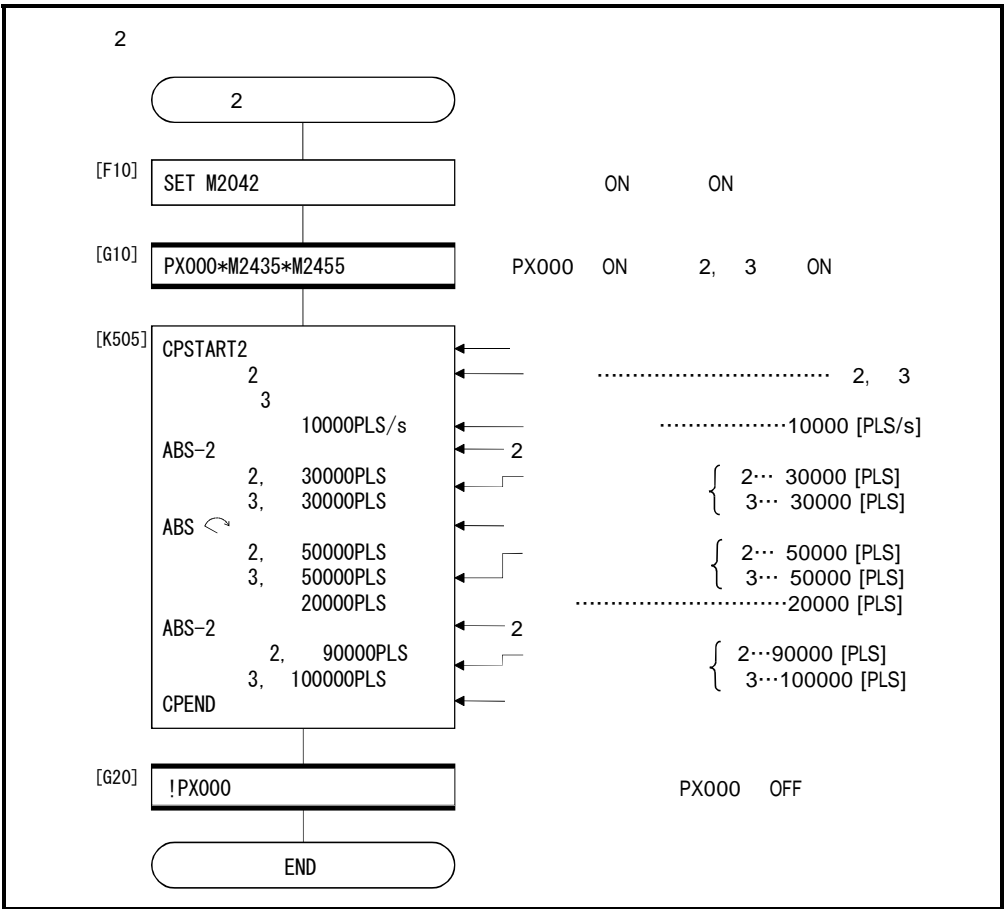
No.505



(e)

SFC

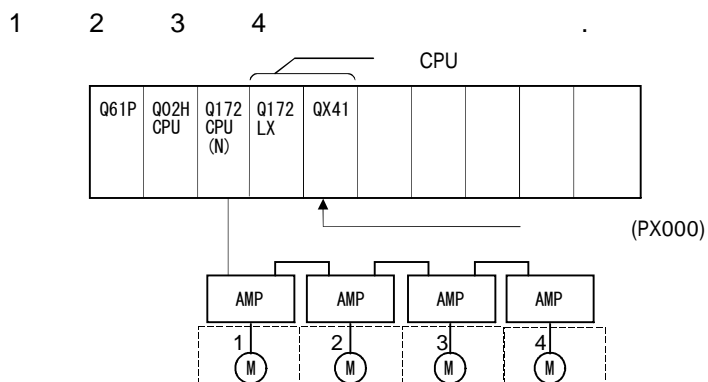
SFC



* : SFC /

(2) 4

(a)



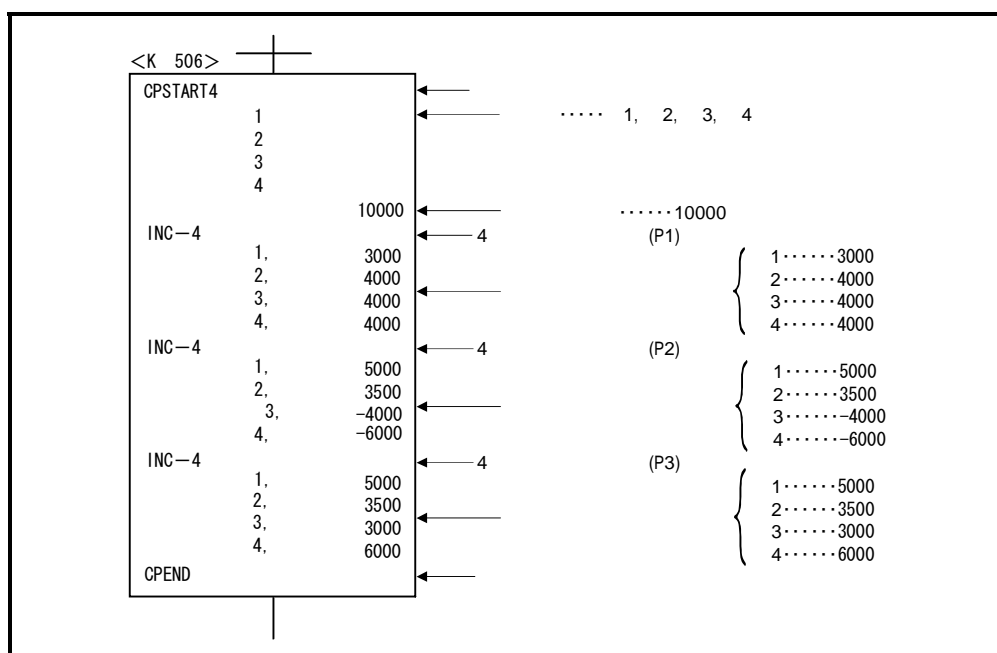
(b)

No.		506		
		10000		
		4	4	4
	1	3000	5000	5000
	2	4000	3500	3500
	3	4000	-4000	3000
	4	4000	-6000	6000

・・ PX000 (OFF→ ON)

(c)

No.506

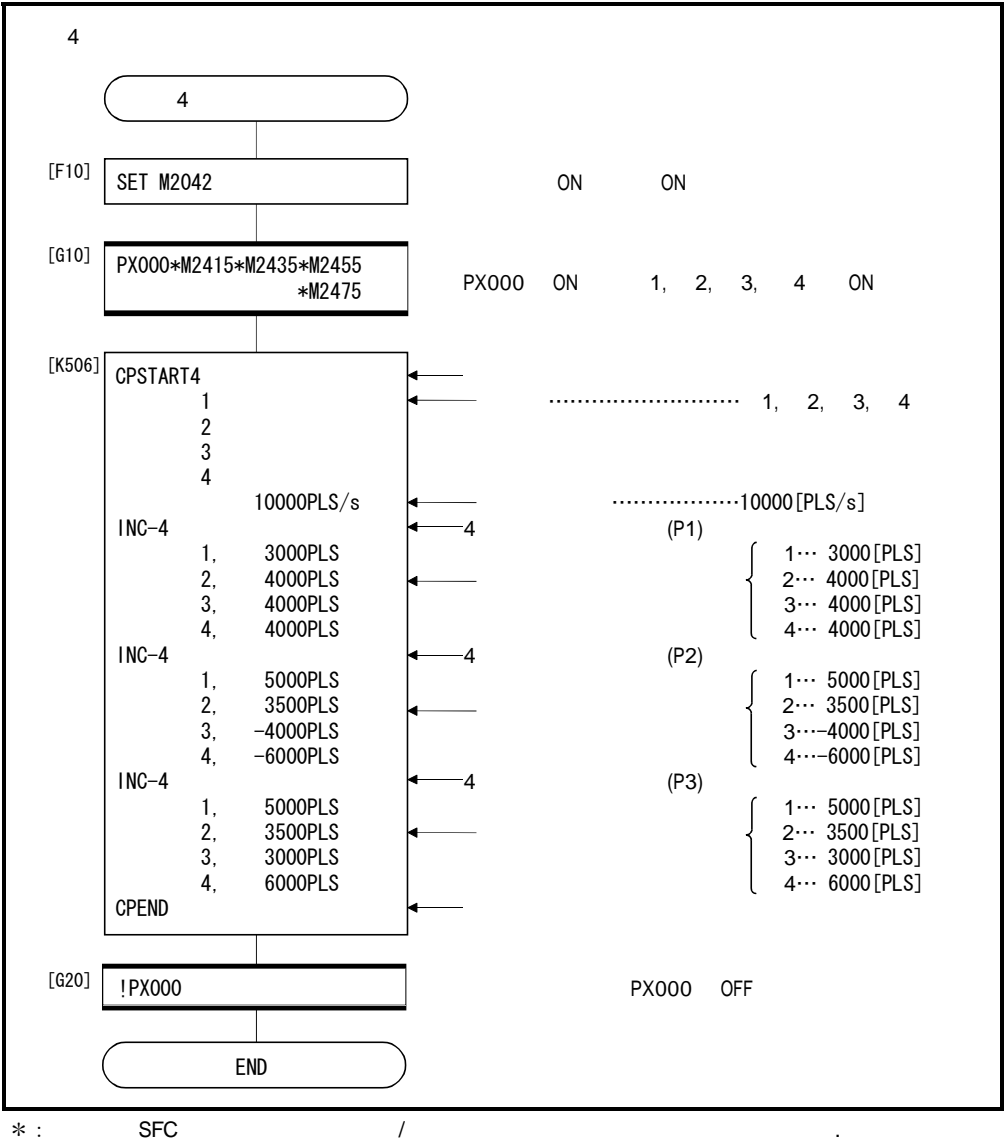


* :

SFC

(d) SFC

SFC ,



6. 17. 5













3 /4

/

, 3 /4

,CPST

ART3/CPATART4/CPEND

			No.	/			M					가			STOP	S	()		FIN 가		WAIT TION /OFF
ABH 		2		○	○			△	△	○								△	△	△	
ABH 																					
ABH 				○	○			△	△		○								△	△	△
ABH 																					
ABH 				○	○			△	△		○								△	△	△
ABH 																					
INH 		2		○	○			△	△	○								△	△	△	
INH 																					
INH 				○	○			△	△		○								△	△	△
INH 																					
INH 				○	○			△	△		○								△	△	△
INH 																					

○ :
△ :

$$\left[\begin{array}{c} \text{---} \\ \text{---} \\ \text{---} \end{array} \right] \quad (1)$$


(1) $\mathcal{H}^1(\mathbb{R}^n) \subset \mathcal{H}^1(\mathbb{R}^n)$, $\mathcal{H}^1(\mathbb{R}^n) \subset \mathcal{H}^1(\mathbb{R}^n)$ /가

(5) $\frac{1}{2} \cdot M$, FIN

(7) 가 ,

6. 17. 6

【 】

(1)

X, Y, M, B, F

【 】

(1) 가

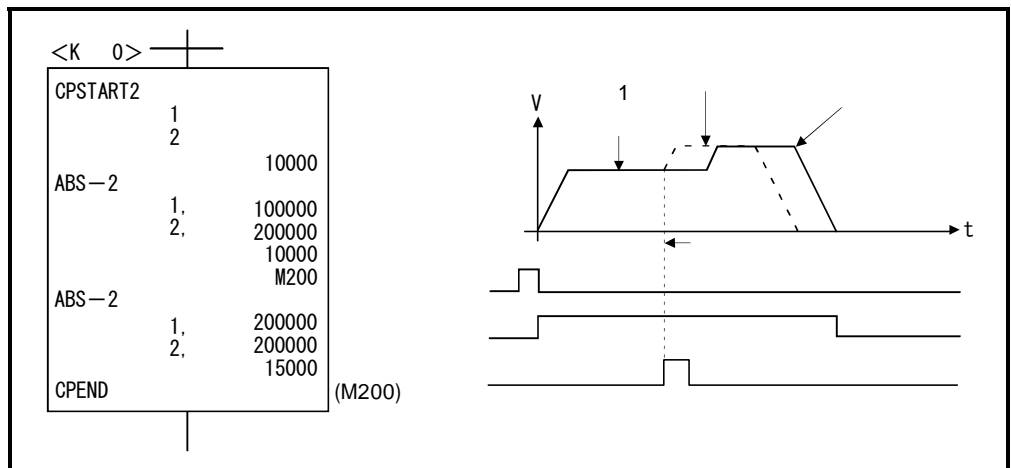
, 가

가

(2)

가

【 】



!

●

[degree]

* :

ABS

(1)

INC

CPSTART1	1	10.000
INC-1	1,	180.00000
		M100
INC-1	1,	180.00000
INC-1	1,	270.00000
CPEND		

01800270[degree]

0100280190[degree]

100[degree]

(2)

ABS

CPSTART1	1	10.000
INC-1	1,	180.00000
		M100
ABS-1	1,	350.00000
INC-1	1,	270.00000
CPEND		

0180350260[degree]

(0100350260[degree])

100[degree]

(3)

INC , ABS

CPSTART1	1	10.000
INC-1	1,	360.00000
		M100
INC-1	1,	180.00000
INC-1	1,	180.00000
ABS-1	1,	90.00000
CPEND		

00180090[degree]

(0802608090[degree])

80[degree]

370[degree]

10[degree]

가

6. 17. 7 FIN

FIN , M , FIN 가 ON OFF ,
 FIN , SFC ON/OFF .

【 】

(1) FIN , 가
 FIN () 가 1~5000[ms]
 D,W,# (1) 가 .

【 】

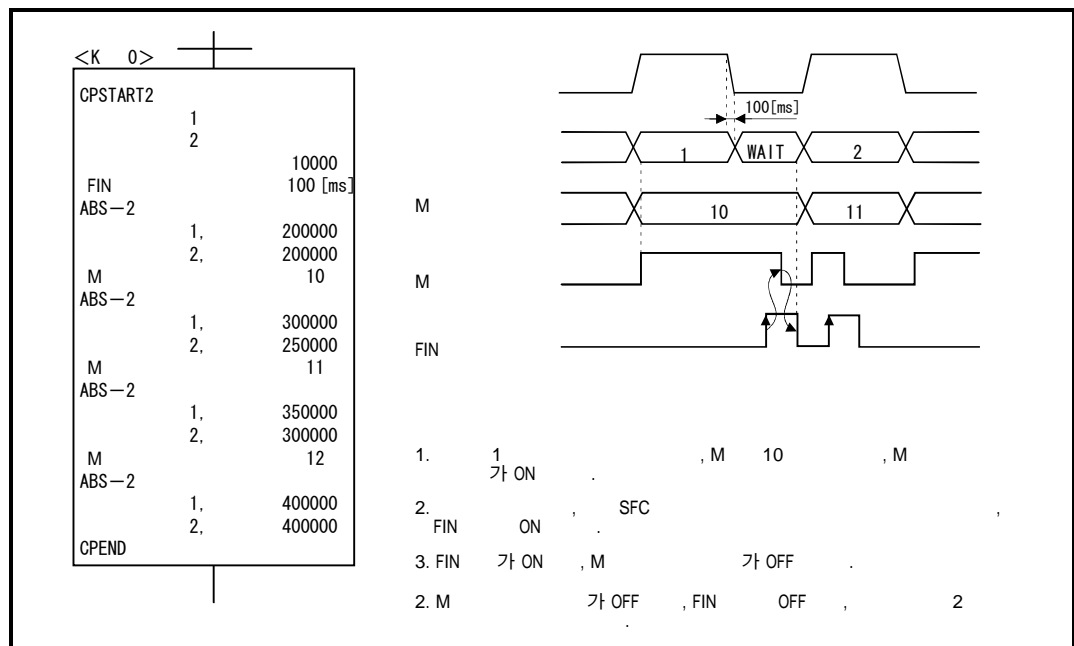
(1) 가 , , [13]가
 , 가 1000[ms] .

(2) , M , ON .

(3) M , FIN 가 OFF→ON→OFF ,
 가 .

【 】

KO FIN ,

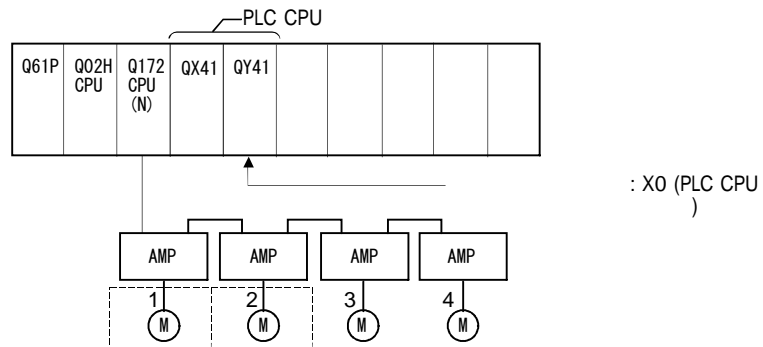


【 】

(1) , FIN

(a)

1 2 , FIN



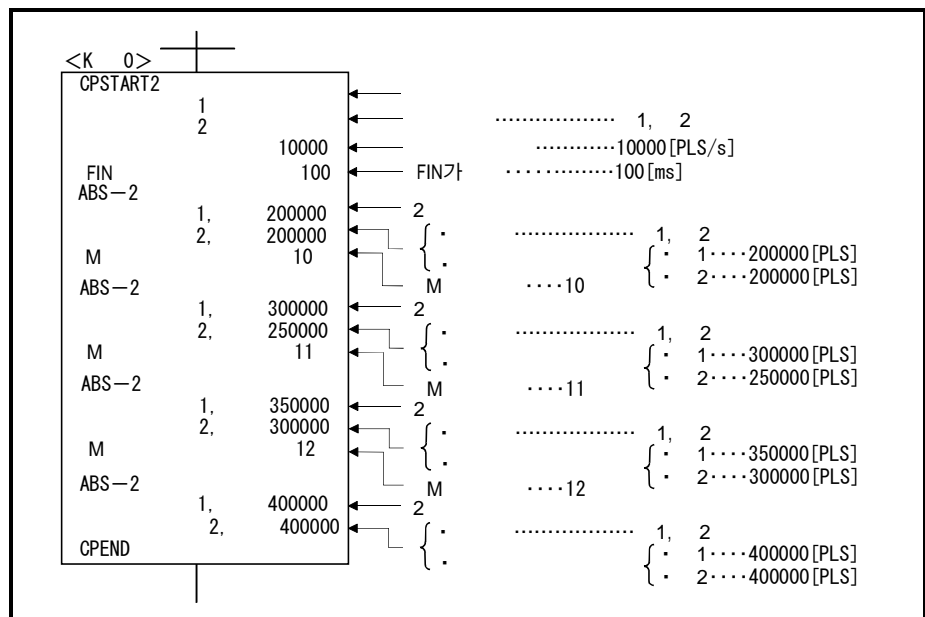
(b)

No.		0			
		10000			
FIN 가		100ms			
		2			
	1	200000	300000	350000	400000
	2	200000	250000	300000	400000
M		10	11	12	-

.. X0 (OFF → ON)
(PLC CPU)

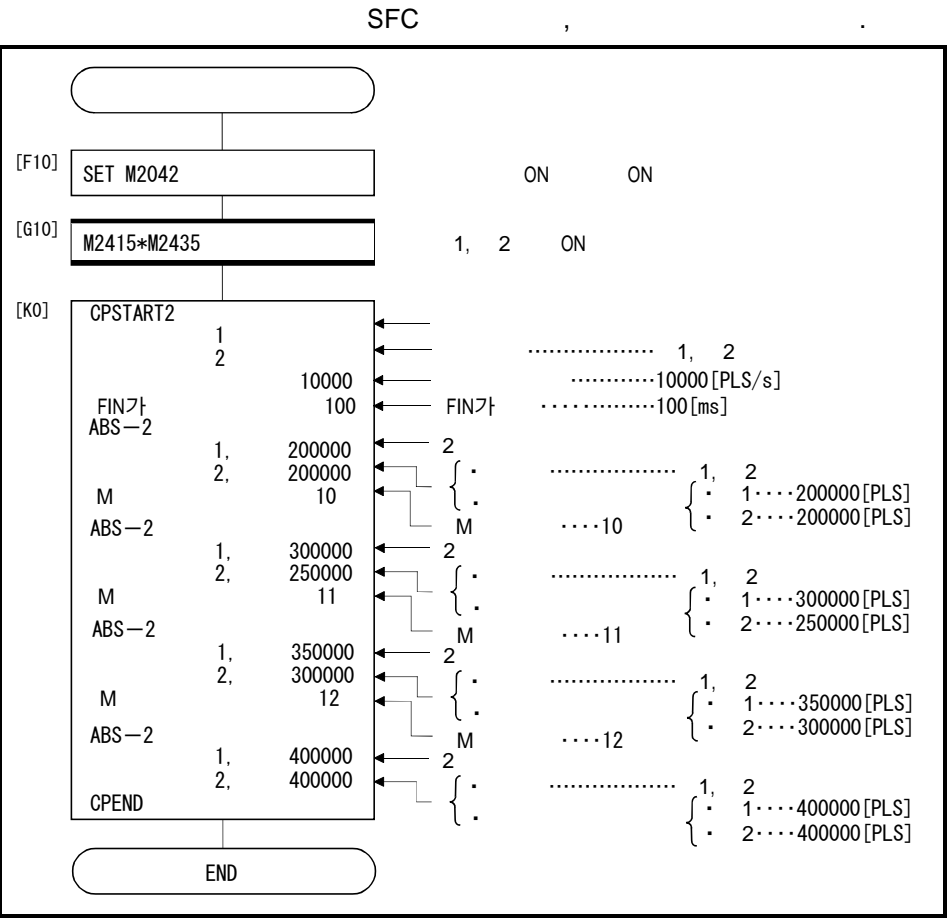
(c)

No.0

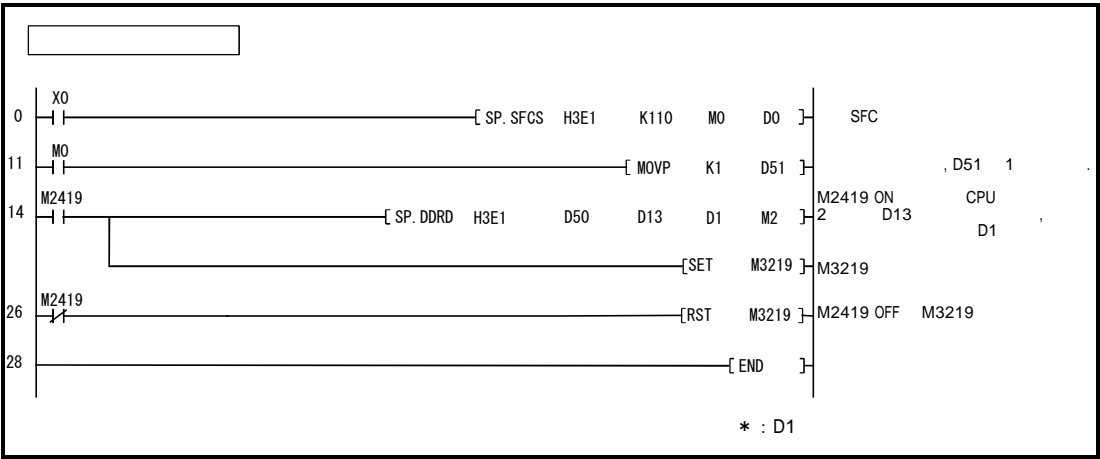


* : SFC

(d) SFC



(e) FIN



* : FIN

(f) (GSV P)
FIN

CPU (1 : M2400~M2495)

基本設定

ベース設定 マルチCPU設定 システム基本設定

マルチCPU台数(*)

2 台

シーケンサCPUを含むCPUの台数を設定してください。

動作モード(*)

CPU停止エラー時の動作モード

☒ 1号機のエラーで全号機停止

☒ 2号機のエラーで全号機停止

☒ 3号機のエラーで全号機停止

☒ 4号機のエラーで全号機停止

自動リフレッシュ設定

設定1

CPU	各CPU送信範囲			CPU側デバイス	
	点数(*)	先頭	最終	先頭	最終
1号機	0				M2400
2号機	6	0800	0805	M2400	M2495
3号機					
4号機					

先頭デバイスの使用可能デバイスは、D,W,M,Y,Bです。
各CPU送信範囲の点数の単位はワードです。

(*) 設定箇所はマルチCPU時、同一設定にしてください。

OK キャンセル

CPU (2 : M3200~M3295)

基本設定

ベース設定 マルチCPU設定 システム基本設定

マルチCPU台数(*)

2 台

シーケンサCPUを含むCPUの台数を設定してください。

動作モード(*)

CPU停止エラー時の動作モード

☒ 1号機のエラーで全号機停止

☒ 2号機のエラーで全号機停止

☒ 3号機のエラーで全号機停止

☒ 4号機のエラーで全号機停止

自動リフレッシュ設定

設定2

CPU	各CPU送信範囲			CPU側デバイス	
	点数(*)	先頭	最終	先頭	最終
1号機	6	0800	0805	M3200	M3295
2号機	0				
3号機					
4号機					

先頭デバイスの使用可能デバイスは、D,W,M,Y,Bです。
各CPU送信範囲の点数の単位はワードです。

(*) 設定箇所はマルチCPU時、同一設定にしてください。

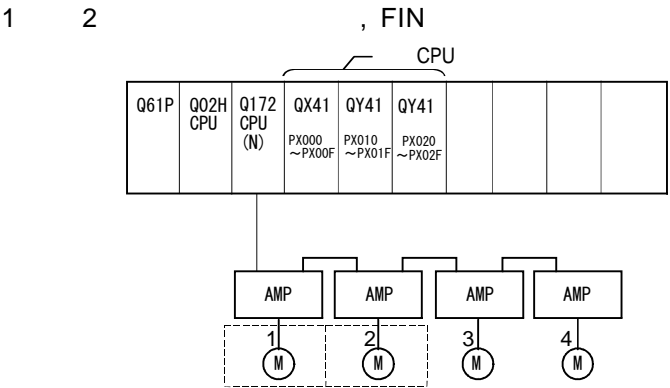
OK キャンセル

6 - 136

() TEL : 02-3660-9531

(2) SFC , FIN

(a)

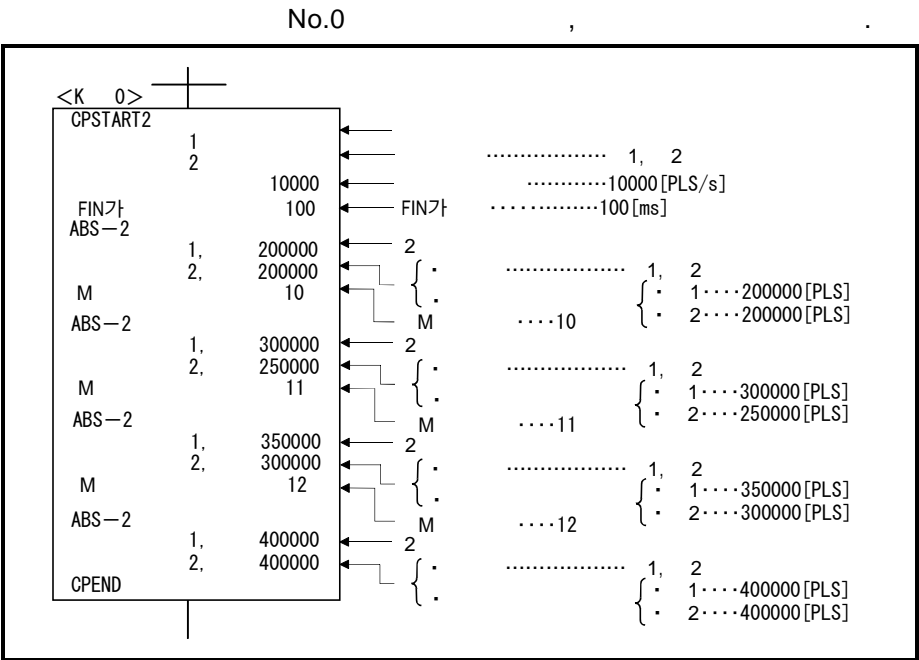


(b)

No.		0			
		10000			
FIN 가		100ms			
		2			
	1	200000	300000	350000	400000
	2	200000	250000	300000	400000
M		10	11	12	-

· · PX000 (OFF → ON)

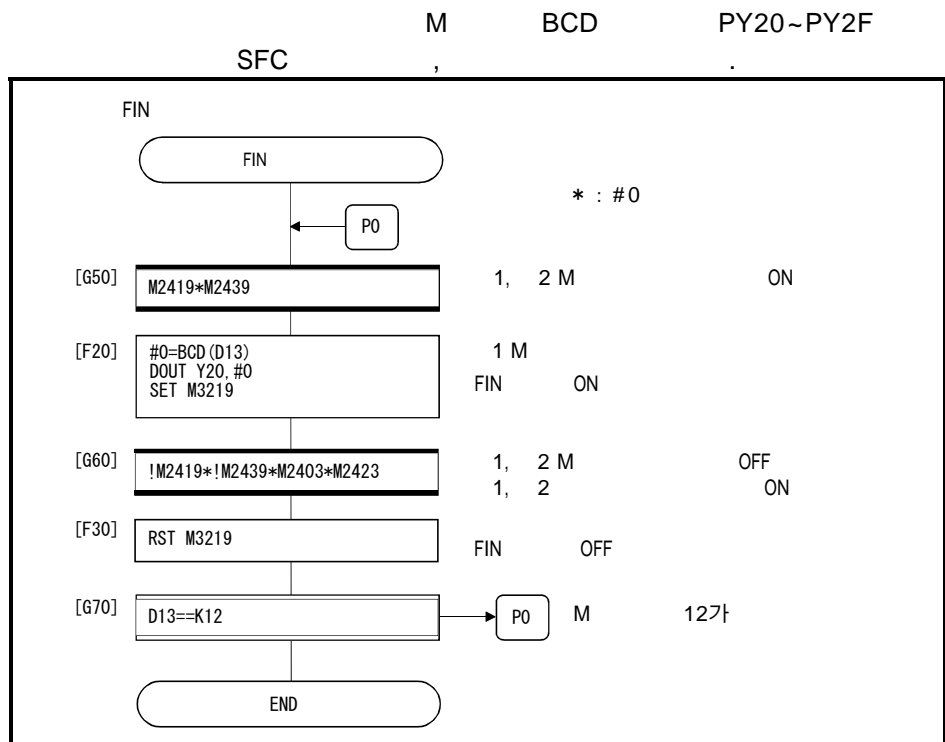
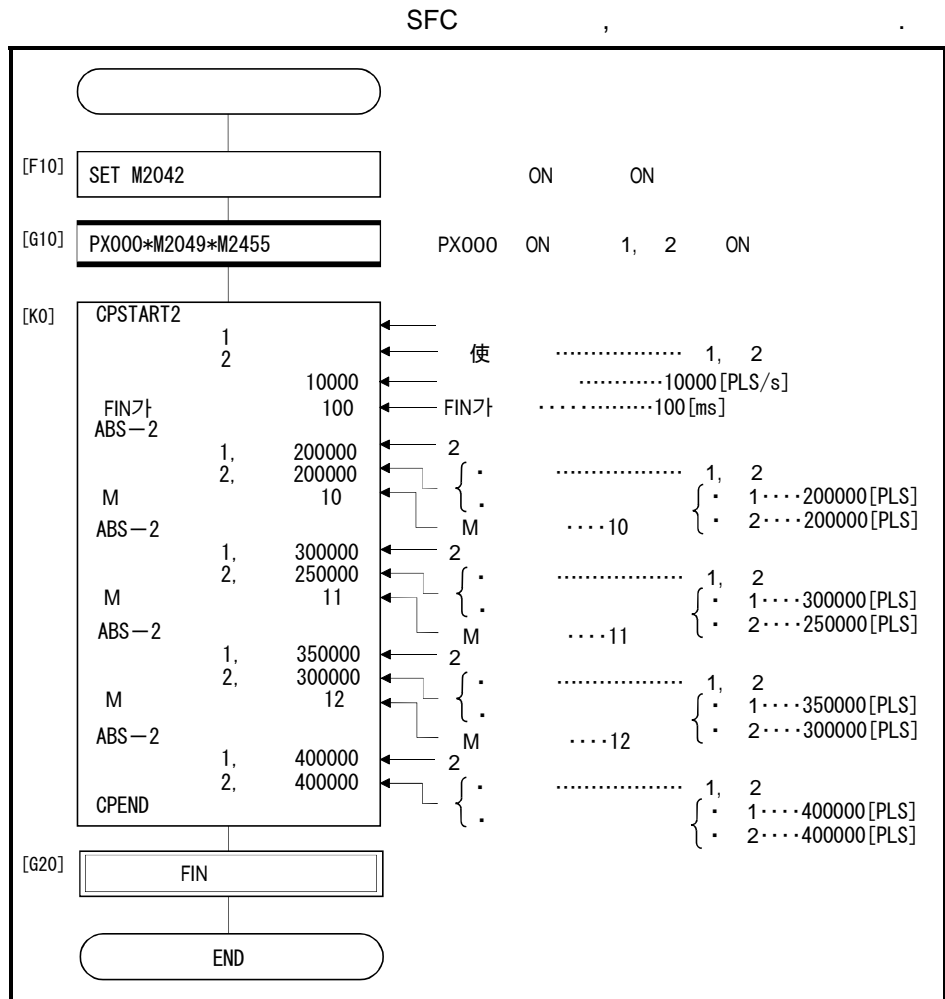
(c)

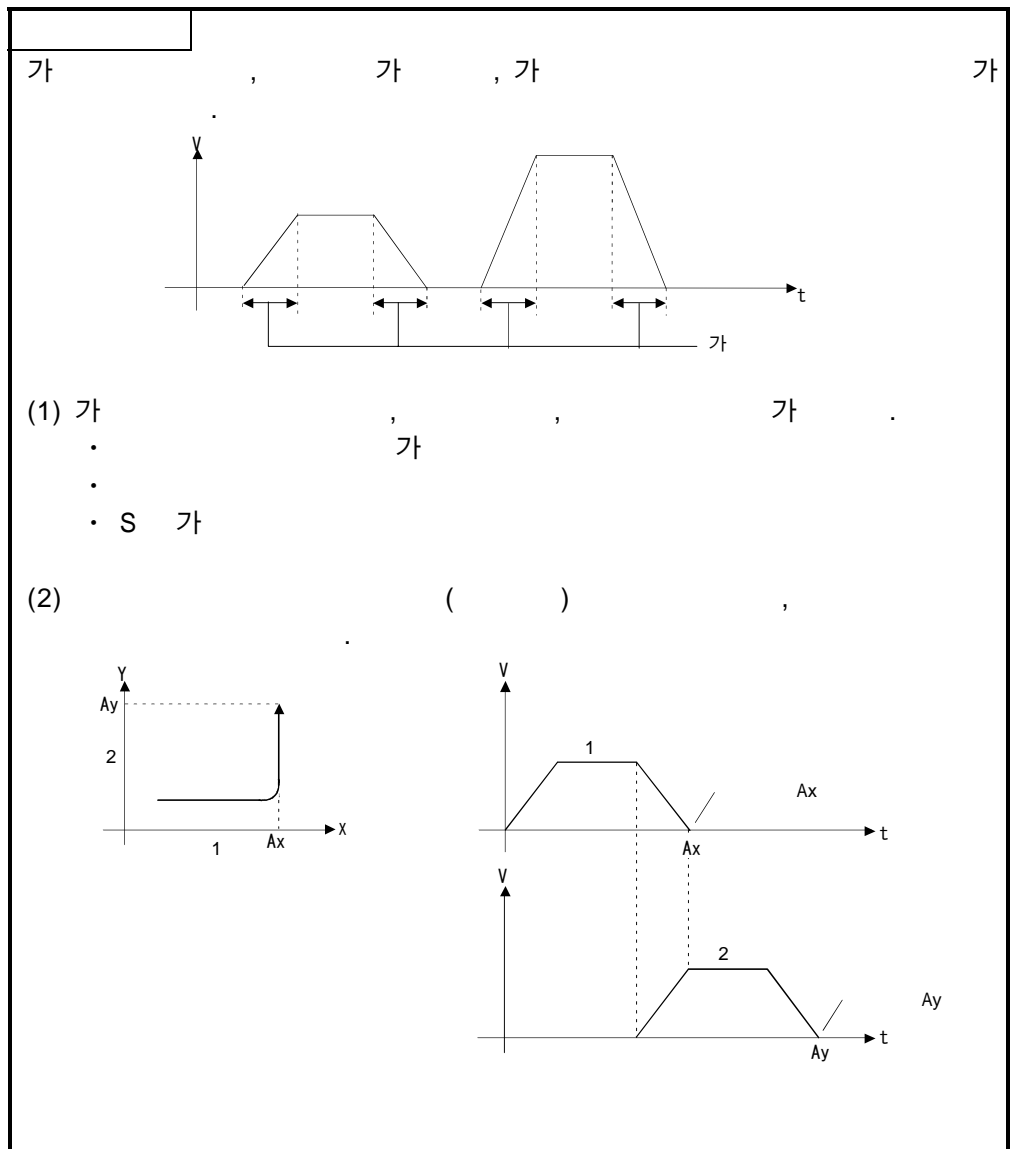


* :

SFC

(d) SFC





[illegible]PFSTART

가

PFSTART

■



B (가) 가 A

A diagram showing a horizontal line with a V-shaped notch in the center. The left side of the notch is labeled 'A' and the right side is labeled 'B'.

(ABS)

D, W, #

, D, W, #

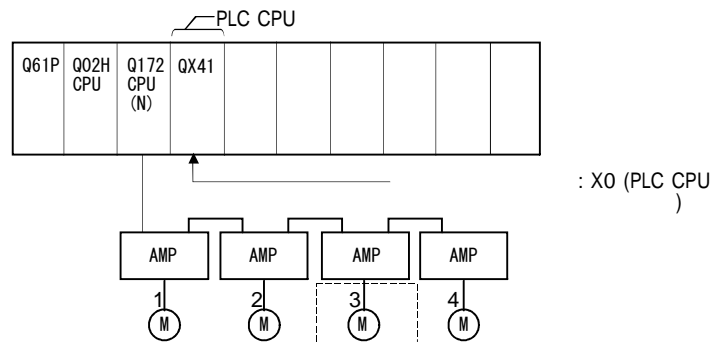
가

()

【 】

(1)

PLC CPU(1) CPU(2) 3



(2)

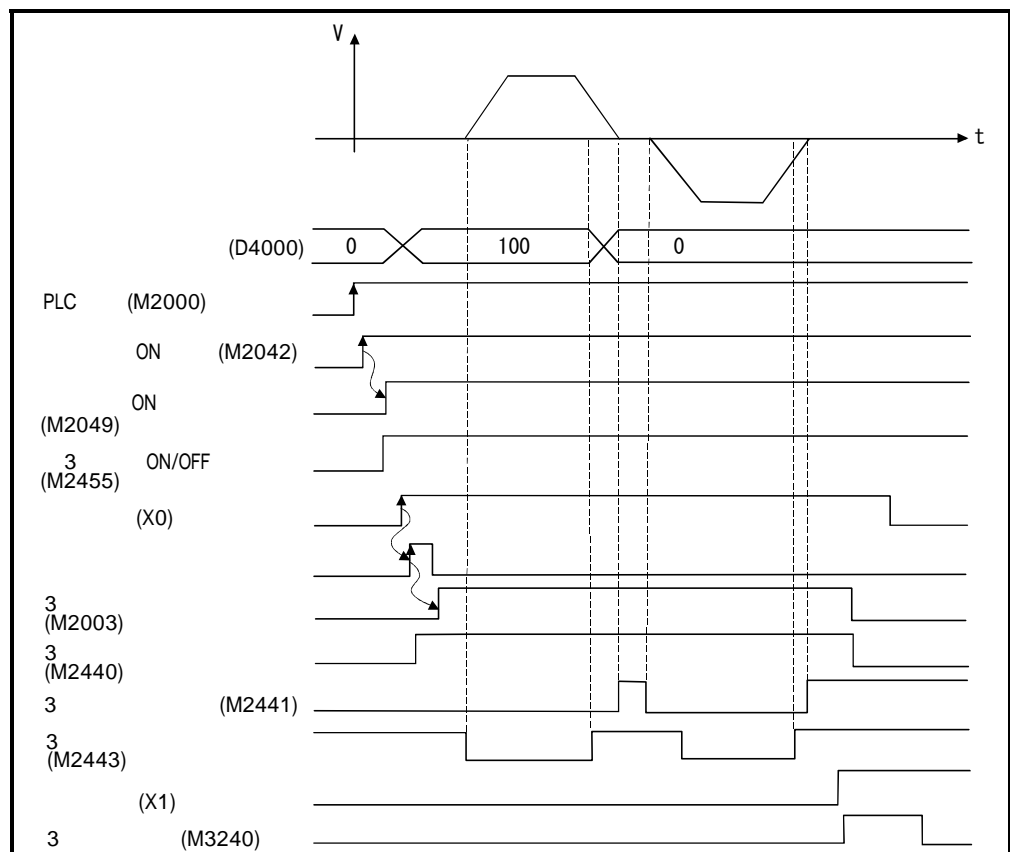
(a)

No.	100
	3
	D4000
	20000

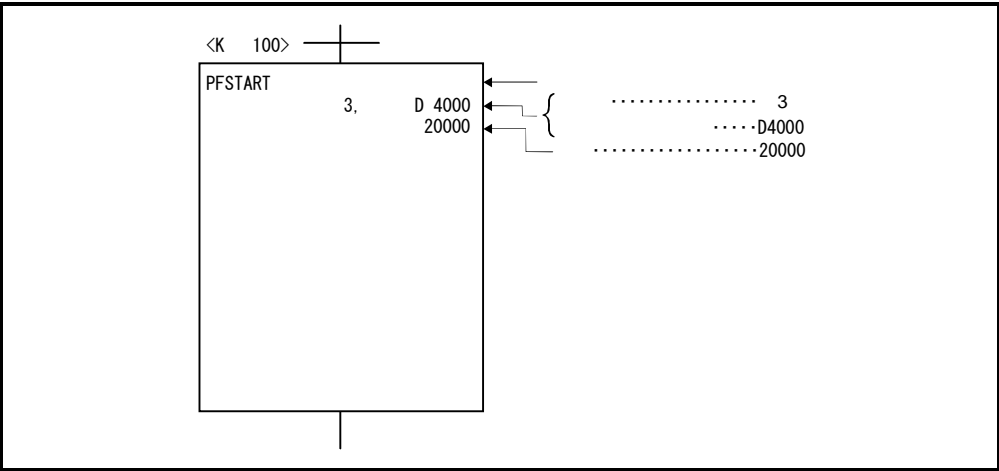
(b)

..... X0 (OFF ON)
(PLC CPU)

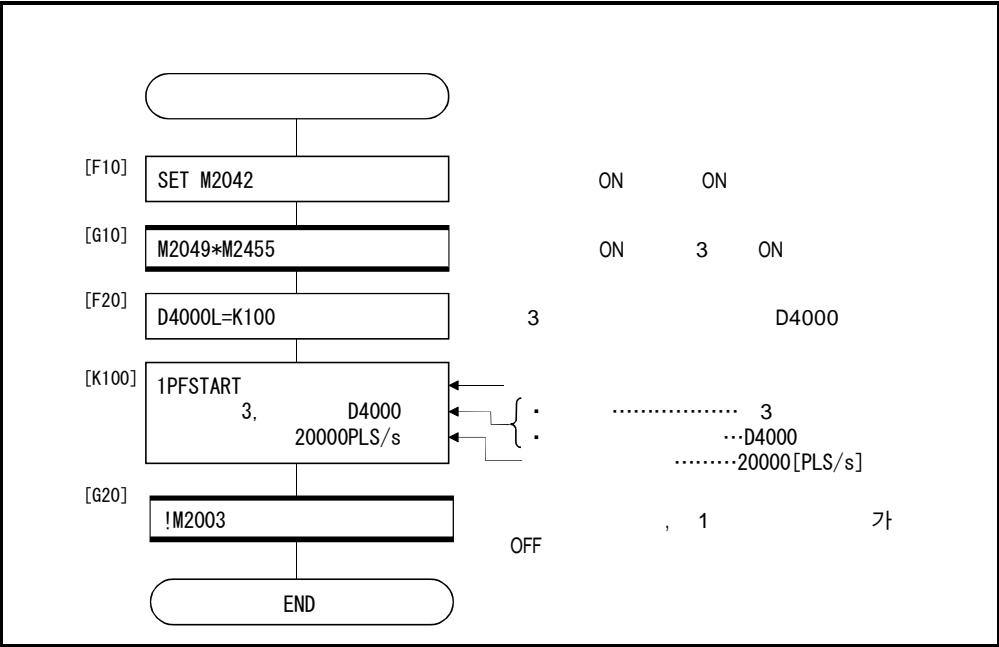
(3)



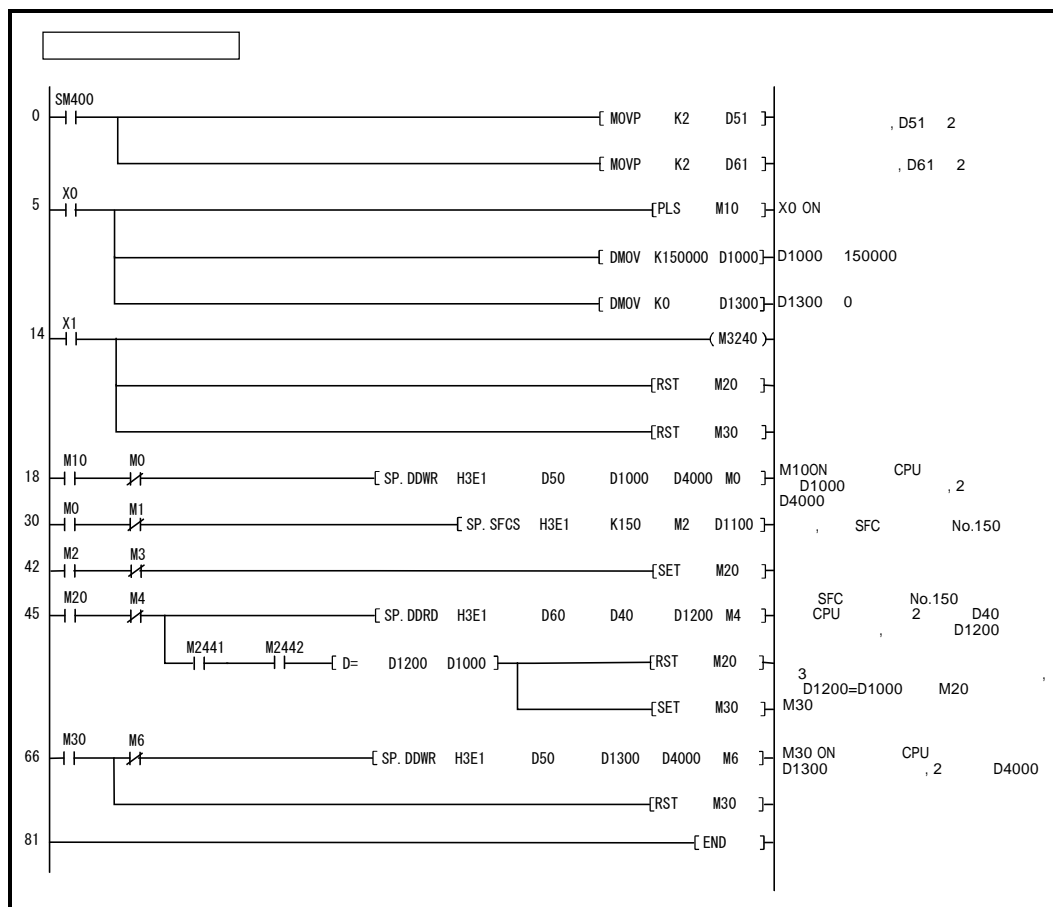
(4) , No.100 ,



(5) SFC SFC ,
(a) SFC SFC ,
, PLC CPU(1) S(P).SFCS



(b)



(c)

(GSV P)

CPU (1 :M2400~M2495)

基本設定

ベース設定 マルチCPU設定 システム基本設定

マルチCPU台数(*)
2 台
シーケンサCPUを含む。
CPUの台数を設定
してください。

動作モード(*)
CPU停止エラー時の動作モード
☒ 1号機のエラーで全号機停止
☒ 2号機のエラーで全号機停止
☒ 3号機のエラーで全号機停止
☒ 4号機のエラーで全号機停止

自動リフレッシュ設定
既定

CPU	各CPU送信範囲			CPU側デバイス	
	点数(*)	先頭	最終	先頭	最終
1号機	0				
2号機	6	0800	0805	M2400	M2495
3号機					
4号機					

先頭デバイスの使用可能デバイスは、D,W,M,Y,Bです。
各CPU送信範囲の点数の単位はワードです。

(*) 設定箇所はマルチCPU時、同一設定にしてください。

OK キャンセル

CPU (2 : M3200~M3295)

基本設定

ベース設定 マルチCPU設定 システム基本設定

マルチCPU台数(*)
2 台
シーケンサCPUを含む。
CPUの台数を設定
してください。

動作モード(*)
CPU停止エラー時の動作モード
☒ 1号機のエラーで全号機停止
☒ 2号機のエラーで全号機停止
☒ 3号機のエラーで全号機停止
☒ 4号機のエラーで全号機停止

自動リフレッシュ設定
既定

CPU	各CPU送信範囲			CPU側デバイス	
	点数(*)	先頭	最終	先頭	最終
1号機	6	0800	0805	M3200	M3295
2号機	0				
3号機					
4号機					

先頭デバイスの使用可能デバイスは、D,W,M,Y,Bです。
各CPU送信範囲の点数の単位はワードです。

(*) 設定箇所はマルチCPU時、同一設定にしてください。

OK キャンセル

6.

6. 19

1 , .
START .

																			No.	
			No.	/				M						가	急停止減速時間	S T O P	S			
START	*	*																	○	*

○ :
* :

【 】

START

- (1) .
- (2) (START) , .
- (3) , 3 .
- (4) , .

【 】

- (1) , , 가 .

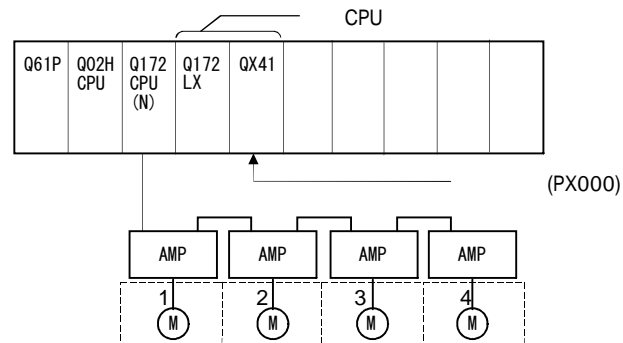
		D9198	D9190
	(M9079) : ON	가 No.	19
START			
		, 가 No.	(3. 5)
가	(M2001+n) : OFF		

- (2) START No. .

【 】

(1)

1 2, 3, 4



(2)

No.

(a) $\dots\dots\dots 3$

(b)	No.
-----	-----

No.		
No.1	1, 軸	
No.14	3	
No.45	4	

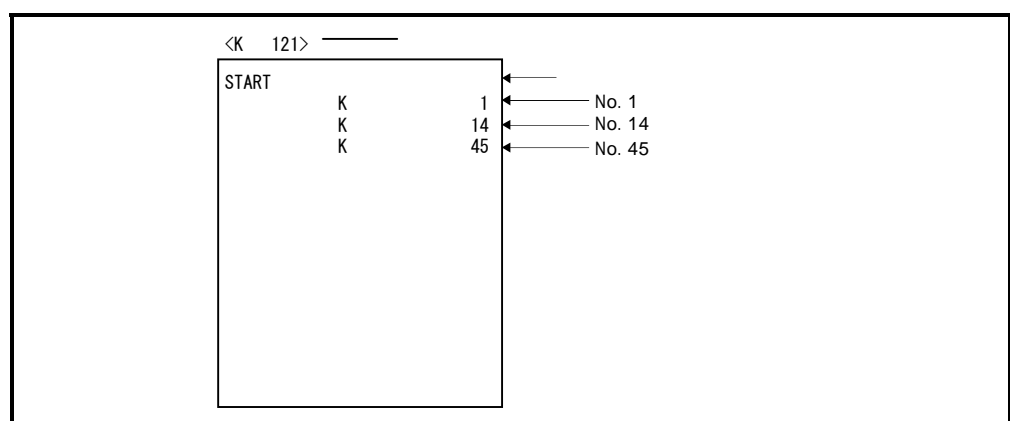
(3)

(a) No. No. 121

(b) PX000 (OFF→ ON)

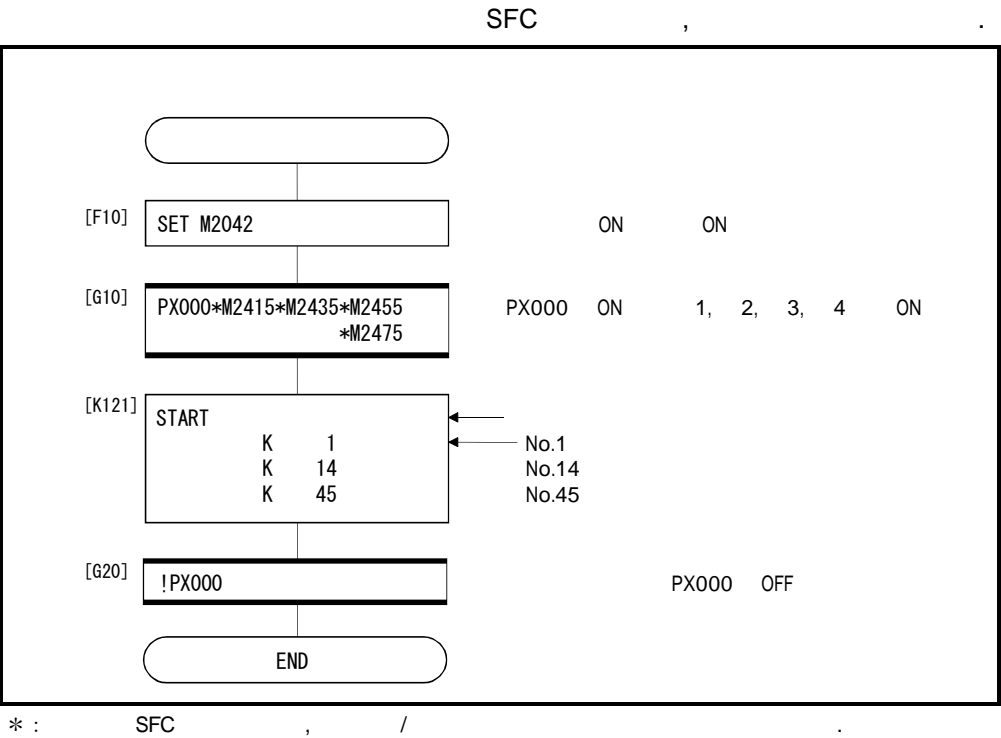
(4)

No.121



* : SFC

(5) SFC



6.

6.20 JOG

JOG
JOG , SFC
(JOG ,
)
JOG , JOG

6.20.1 JOG

JOG , JOG

6.2 JOG

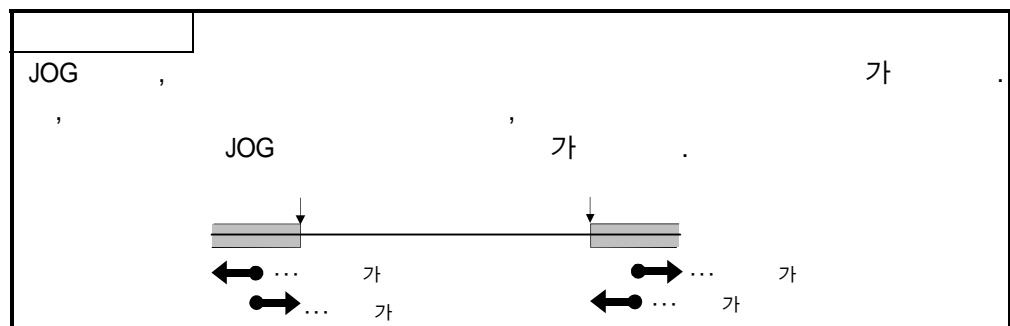
No.													
		mm		inch		degree		PLS					
1	JOG	0.01~ 6000000.00	mm /min	0.001~ 600000.000	inch /min	0.001~ 2147483.647	degree /min	1~ 10000000	PLS/s	20000	PLS/s	• JOG • JOG • JOG • JOG	—
2		1~64								1	—	• JOG	4.4

(1) JOG

JOG
• JOG
• JOG
• JOG

(2)

가
가, 가

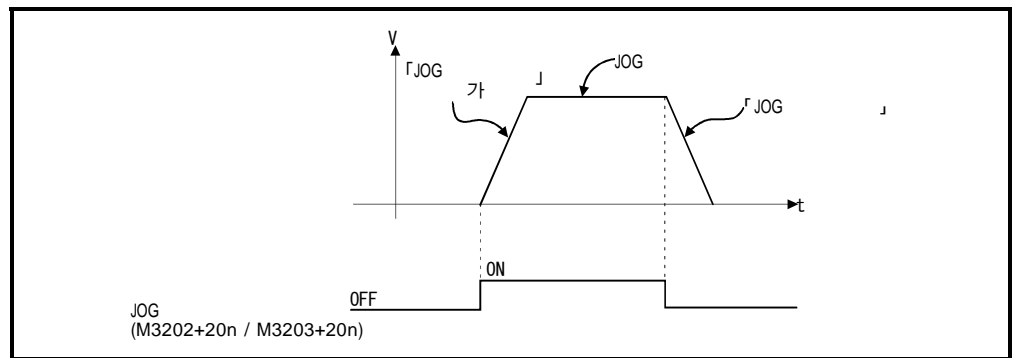


6. 20. 2

JOG
 JOG , JOG
 • JOG M3202+20n
 • JOG M3203+20n

【 】

(1) JOG 가 ON , JOG JOG
 , JOG 가 OFF ,
 가 , JOG



JOG 가 ON JOG

(2) JOG , JOG

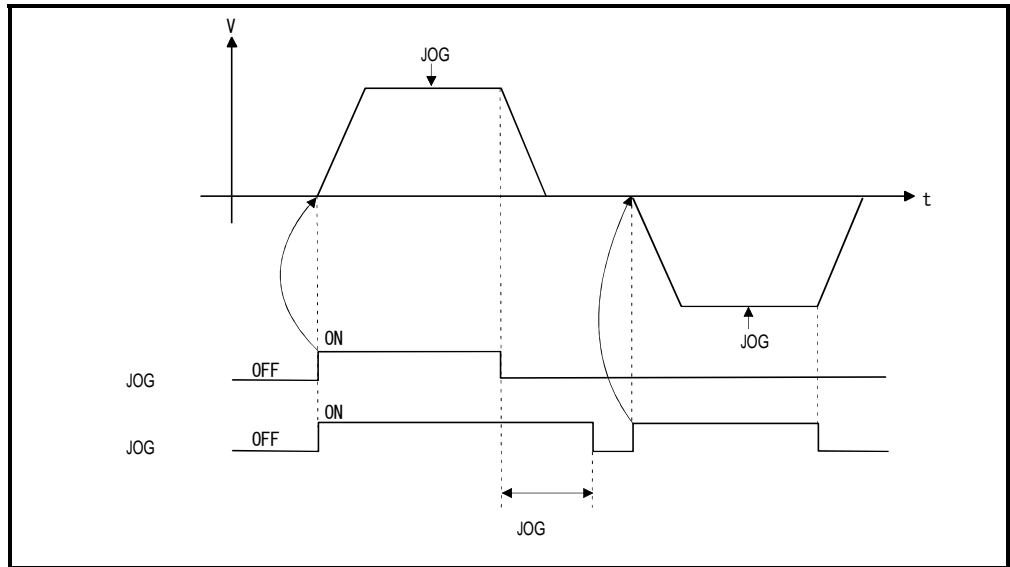
No.	JOG		JOG		mm		inch		degree		PLS	
	JOG	JOG										
1	M3202	M3203	D641	D640	1~ 600000000	$\times 10^{-2}$ mm /min	1~ 600000000	$\times 10^{-3}$ inch /min	1~ 2147483647	$\times 10^{-3}$ degree /min	1~ 10000000	PLS/s
2	M3222	M3223	D643	D642								
3	M3242	M3243	D645	D644								
4	M3262	M3263	D647	D646								
5	M3282	M3283	D649	D648								
6	M3302	M3303	D651	D650								
7	M3322	M3323	D653	D652								
8	M3342	M3343	D655	D654								
9	M3362	M3363	D657	D656								
10	M3382	M3383	D659	D658								
11	M3402	M3403	D661	D660								
12	M3422	M3423	D663	D662								
13	M3442	M3443	D665	D664								
14	M3462	M3463	D667	D666								
15	M3482	M3483	D669	D668								
16	M3502	M3503	D671	D670								
17	M3522	M3523	D673	D672								
18	M3542	M3543	D675	D674								
19	M3562	M3563	D677	D676								
20	M3582	M3583	D679	D678								
21	M3602	M3603	D681	D680								
22	M3622	M3623	D683	D682								
23	M3642	M3643	D685	D684								
24	M3662	M3663	D687	D686								
25	M3682	M3683	D689	D688								
26	M3702	M3703	D691	D690								
27	M3722	M3723	D693	D692								
28	M3742	M3743	D695	D694								
29	M3762	M3763	D697	D696								
30	M3782	M3783	D699	D698								
31	M3802	M3803	D701	D700								
32	M3822	M3823	D703	D702								

* : Q172CPU(N) , No.1~ No.8 가

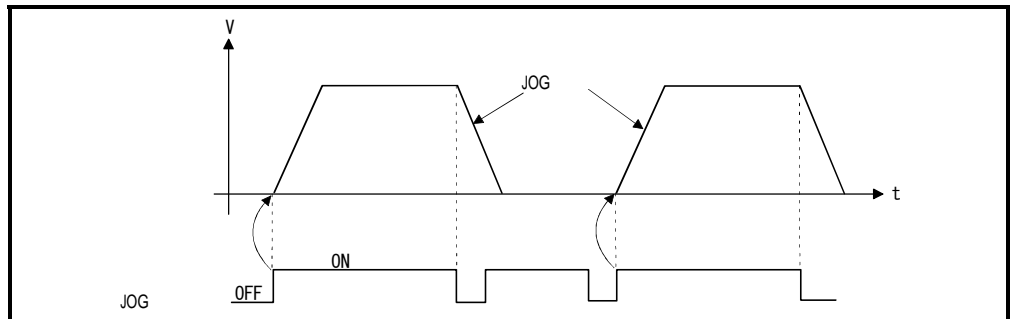
SFC		JOG		100	
:[mm])/1000		(:[inch],[degree])		JOG	
<div style="border: 1px dashed black; padding: 10px; margin: 10px 0;"> JOG 6000.00[mm/min] , "600000" JOG </div>					

【 】

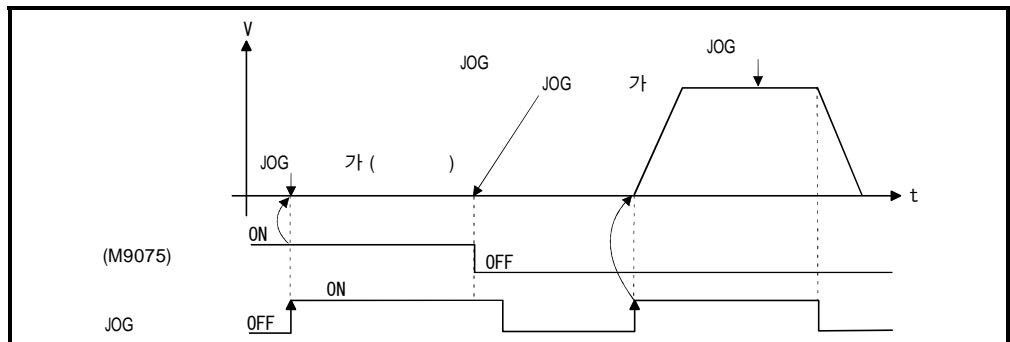
(1) JOG (M3203+20n) JOG (M3203+20n)가
ON , JOG JOG OFF
 , JOG 가 ON JOG
 , JOG OFF ON JOG



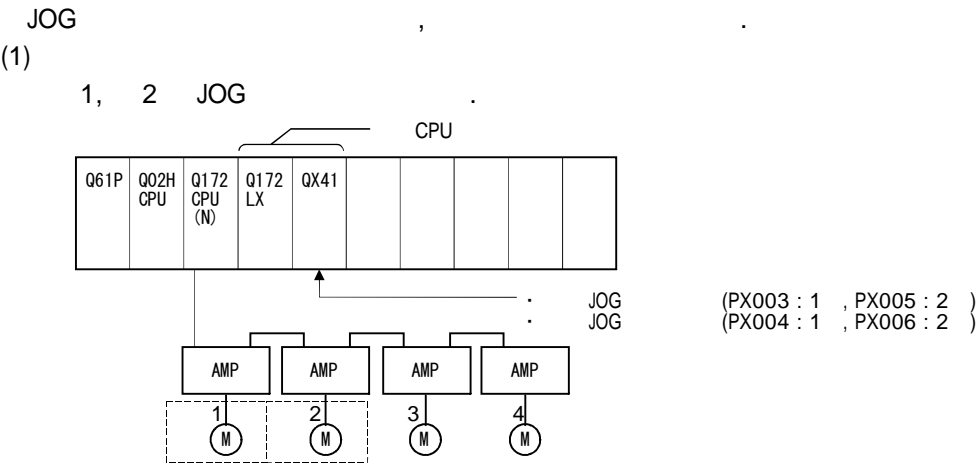
(2) JOG OFF JOG 가 ON ,
JOG , JOG OFF ON JOG



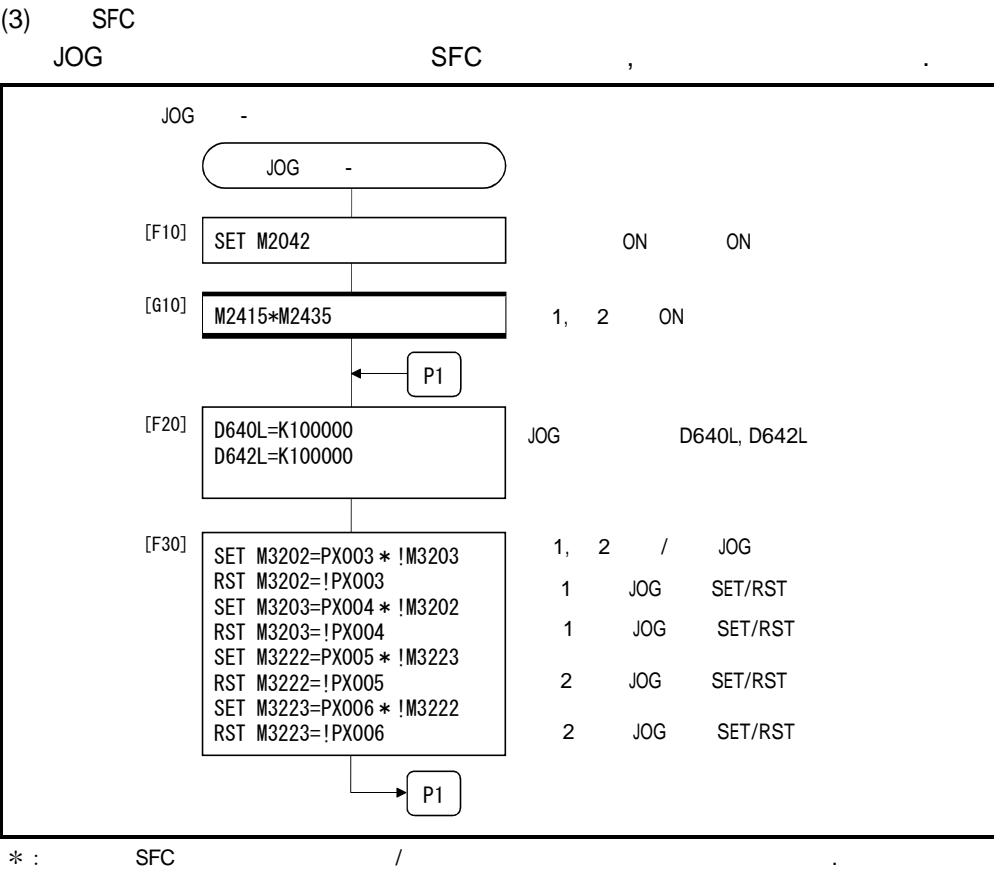
(3) , JOG (M3202+20n/M3203+20n)
JOG , JOG OFF ON , JOG



[]



- (2) JOG
- (a) No. 1, 2
- (b) JOG 100000
- (c) JOG
- | | | |
|-----|---------|------------|
| JOG | 1 | : PX003, |
| | 2 | : PX005 ON |
| JOG | 1 | : PX004, |
| | 2 | : PX006 ON |

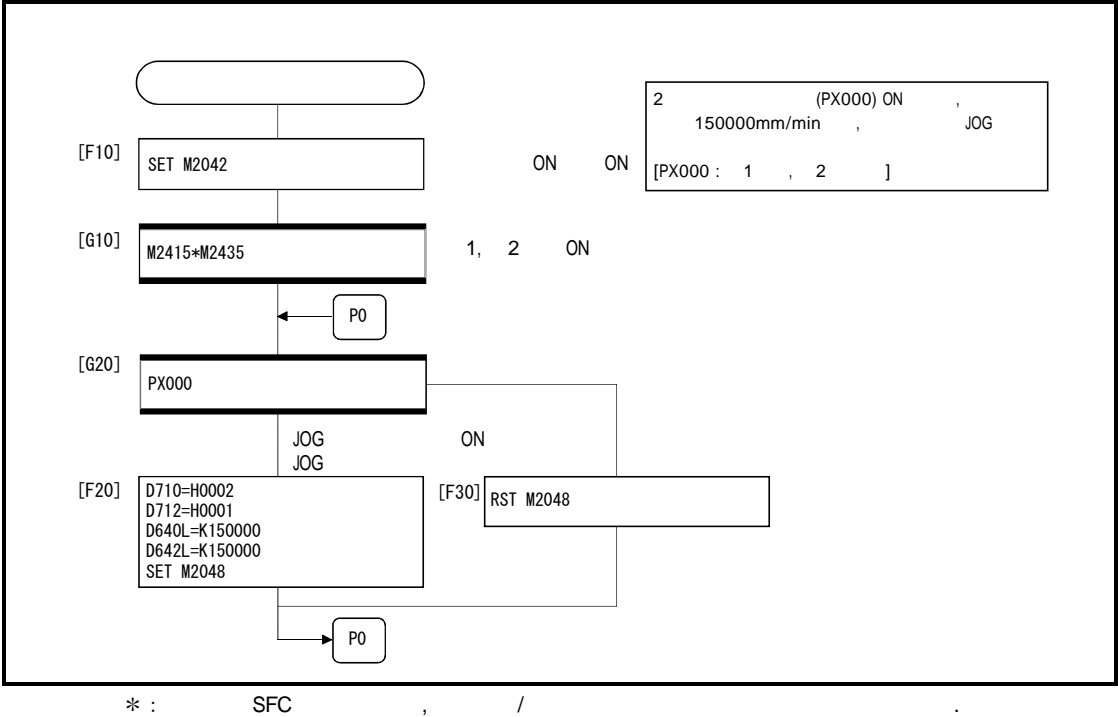
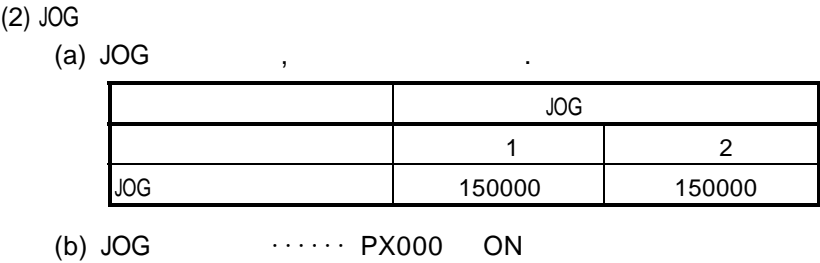
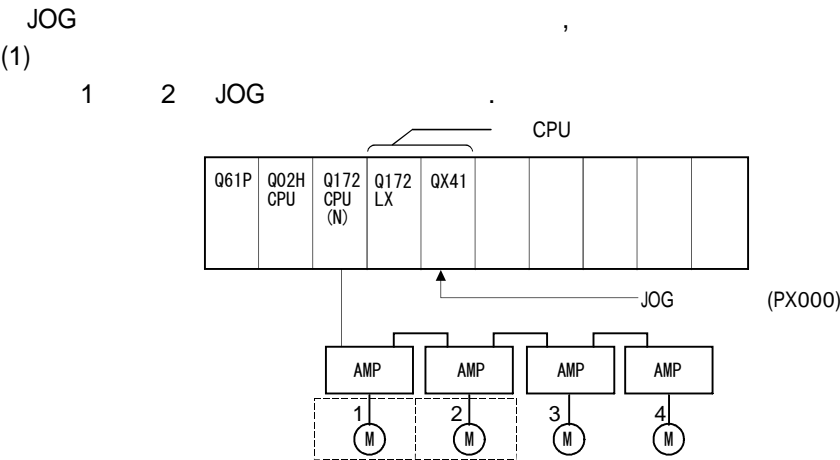


(3) JOG

No.	JOG		JOG									
					mm		inch		degree		PLS	
	JOG	JOG										
1	M3202	M3203	D641	D640	1~ 600000000	$\times 10^{-2}$ mm /min	1~ 600000000	$\times 10^{-3}$ inch /min	1~ 2147483647	$\times 10^{-3}$ degree /min	1~ 10000000	PLS/s
2	M3222	M3223	D643	D642								
3	M3242	M3243	D645	D644								
4	M3262	M3263	D647	D646								
5	M3282	M3283	D649	D648								
6	M3302	M3303	D651	D650								
7	M3322	M3323	D653	D652								
8	M3342	M3343	D655	D654								
9	M3362	M3363	D657	D656								
10	M3382	M3383	D659	D658								
11	M3402	M3403	D661	D660								
12	M3422	M3423	D663	D662								
13	M3442	M3443	D665	D664								
14	M3462	M3463	D667	D666								
15	M3482	M3483	D669	D668								
16	M3502	M3503	D671	D670								
17	M3522	M3523	D673	D672								
18	M3542	M3543	D675	D674								
19	M3562	M3563	D677	D676								
20	M3582	M3583	D679	D678								
21	M3602	M3603	D681	D680								
22	M3622	M3623	D683	D682								
23	M3642	M3643	D685	D684								
24	M3662	M3663	D687	D686								
25	M3682	M3683	D689	D688								
26	M3702	M3703	D691	D690								
27	M3722	M3723	D693	D692								
28	M3742	M3743	D695	D694								
29	M3762	M3763	D697	D696								
30	M3782	M3783	D699	D698								
31	M3802	M3803	D701	D700								
32	M3822	M3823	D703	D702								

* Q172CPU(N) , No.1~ No.8 가

【 】



6.21

1 1~3 가 , , .

가
3

Q173PX 2	, 1 (, 0)
Q173PX	.
(, Q173PX 1)	.

【 】

(1) , ,

, 가 가 ON .

		가
P1	D714, D715	M2051
P2	D716, D717	M2052
P3	D718, D719	M2053

(2) , ,

(a)

$$[] = [1] \times [] \times [1]$$

1 , .

mm	0.1[μm]
inch	0.00001[inch]
degree	0.00001[degree]
PLS	1[PLS]

가 [mm] , 1 (0.1[μm])×(1[PLS])×(1)

(b)

$$[] = [1ms] \times [1]$$

(3)

(D714~D719)

(1~32)

(4)

1
1

1	No.	
D720	1	1~10000*2
D721	2	
D722	3	
D723	4	
D724	5	
D725	6	
D726	7	
D727	8	
D728	9	
D729	10	
D730	11	
D731	12	
D732	13	
D733	14	
D734	15	
D735	16	
D736	17	
D737	18	
D738	19	
D739	20	
D740	21	
D741	22	
D742	23	
D743	24	
D744	25	
D745	26	
D746	27	
D747	28	
D748	29	
D749	30	
D750	31	
D751	32	

*1 : Q172CPU(N) , 1~ 8 가

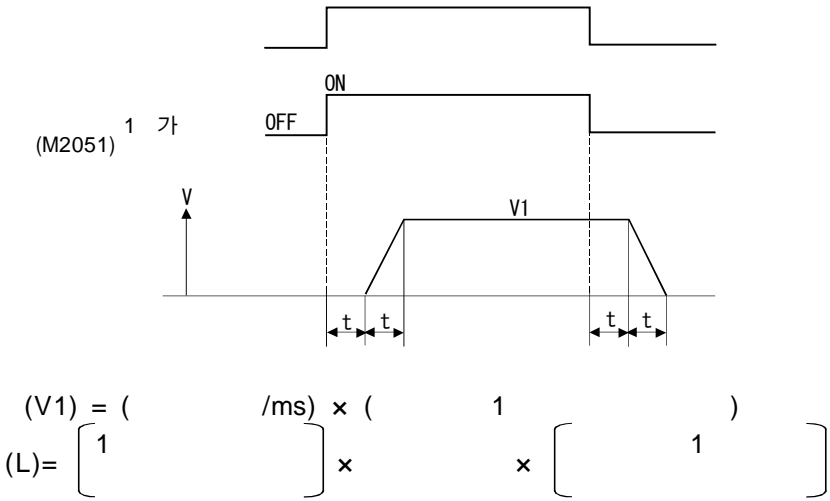
*2 : SW6RN-SV13Q /22Q(Ver.OOB) , (1~100)가

(5) 1 가
「 1 」
(M9077) , "1" (D9185~D9187)

(6)

1(P1) : D752	0~59
2(P2) : D753	
3(P3) : D754	

(a)



(b)

$$(t) = (\quad 1 \quad +1) \times 56.8[ms]$$

1) , 56.8~3408[ms]가

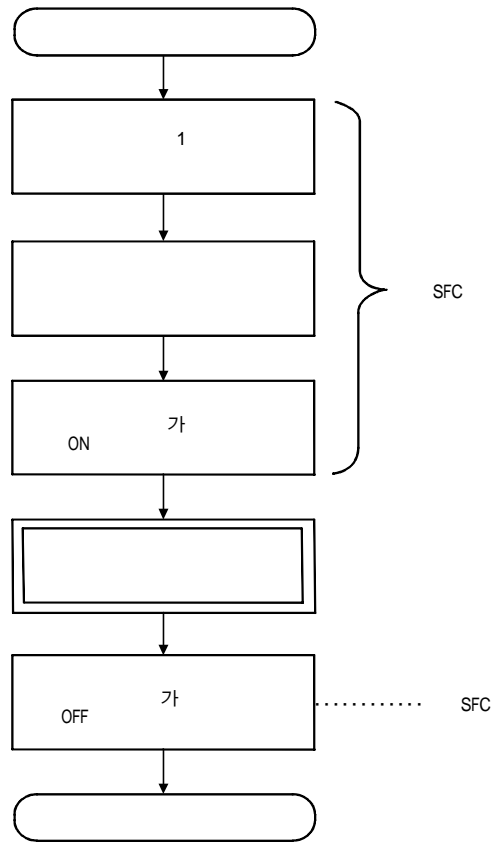
(7)

1~32	<ul style="list-style-type: none"> 가 1~32
	<ul style="list-style-type: none">
4	3

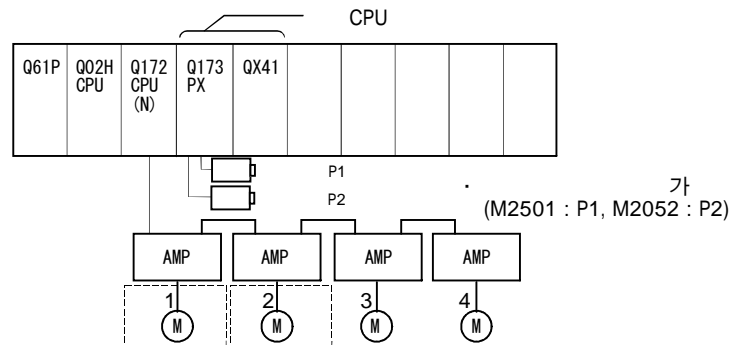
【 】

- (1) 가 ON . 가 CPU , 가 OFF .
- (2) , 300[%] .
- (3) , JOG 가 ON , [214]가 가가 가가 가가 가 ON , .
- (4) , No. 가 ON , [214]가 가가 , 가 ON .
- (5) 가 OFF , 가 ON , [214]가 (OFF), 가 ON .
- (6) 가 OFF , , 가 (D9185~D9187) 가 ON , (M9077)가 ON OFF 가 ON , ON .

【 】



1


$$\dots \quad 1, \quad 2$$

1

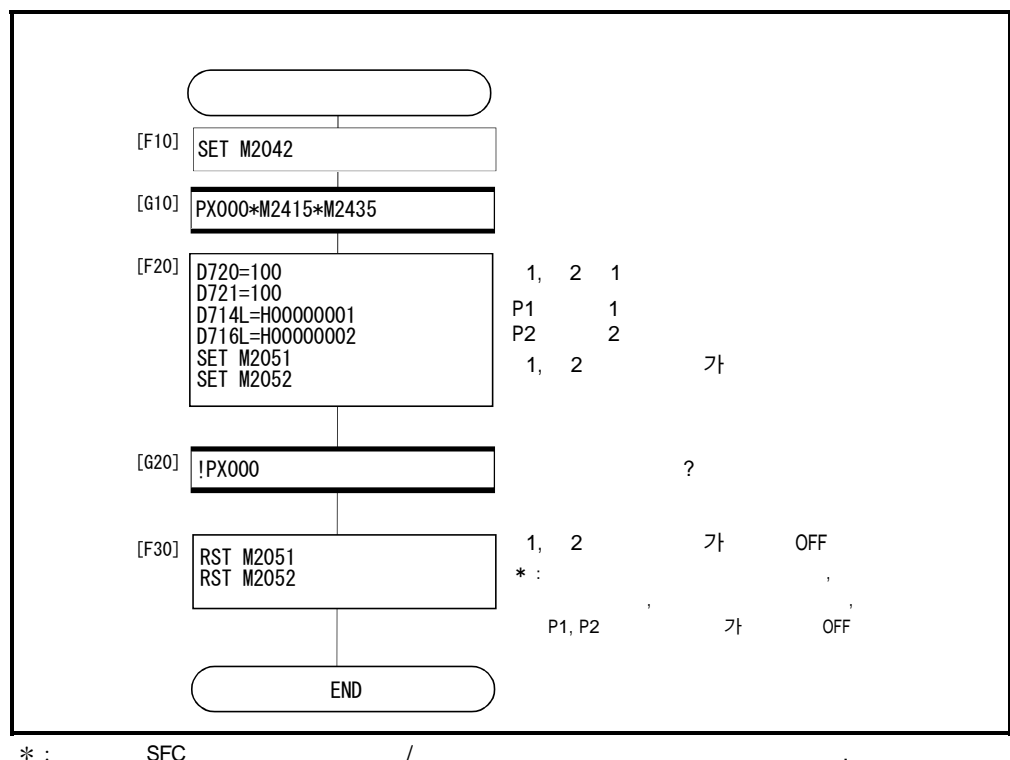
..... 100

가 M2051(1), M2052(2) SET

..... M2052(1), M2052(2) RST

SFC

SFC



6.

6. 22

- (1) , .
- (2) , 3 가 .
 .
 .
 .
 }

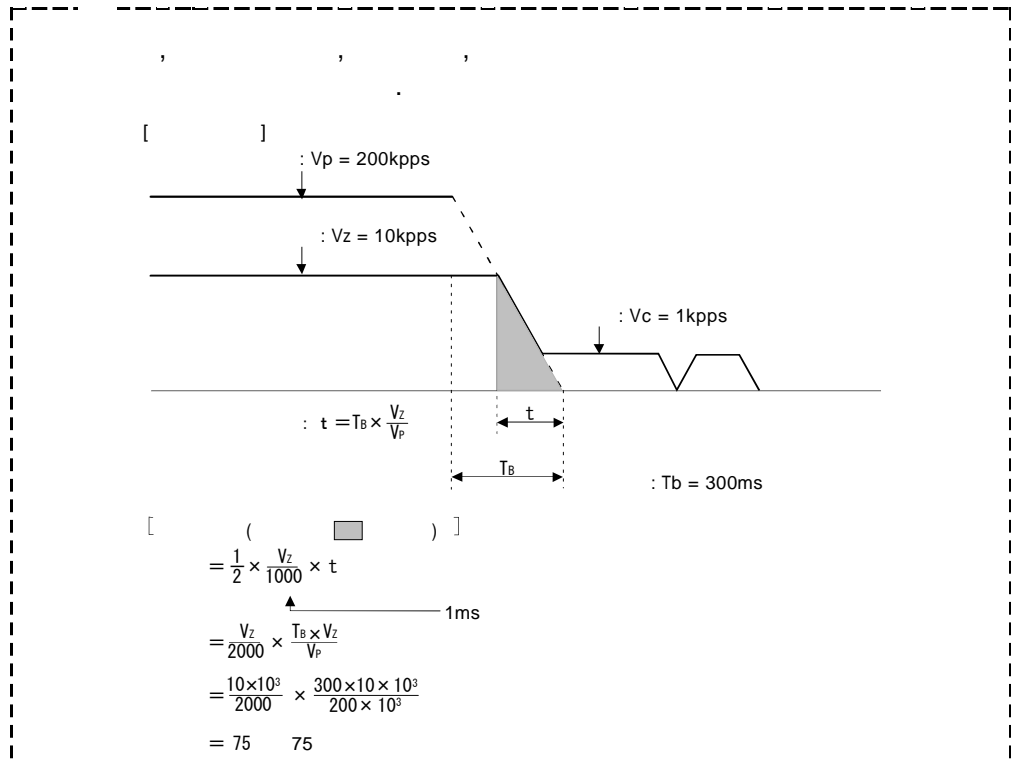
 .
- (3) , .

6. 22. 1

6. 3

No.												
		mm		inch		degree		PLS				
1		0 : () 1 : (가)								0	.	—
2		0 : 1 : 2 : 1 3 : 2								0	.	—
3		-2147483648 ~2147483647	$\times 10^{-1}$ μm	-2147483648 ~2147483647	$\times 10^{-5}$ inch	0~35999999	$\times 10^{-5}$ degree	-2147483648 ~2147483647	PLS	0	.	—
4		0. 01~ 6000000. 00	mm /min	0. 001~ 600000. 000	inch /min	0. 001~ 2147483. 647	degree /min	1~10000000	PLS/s	1	.	—
5		0. 01~ 6000000. 00	mm /min	0. 001~ 600000. 000	inch /min	0. 001~ 2147483. 647	degree /min	1~10000000	PLS/s	1	.	—
6	ON	0~ 214748364. 7	μm	0~ 21474. 83647	inch	0~ 21474. 83647	degree	0~ 2147483647	PLS	—	.	6. 22. 1 (1)
7		1~64								1	.	—

- (1) ON
- (a) , ON
- (b) ON , ,
- 가 .
- (c) ON , ,
- .



1 Z (

가 .

가 1

ABS() , JOG

1 , Z 가 .

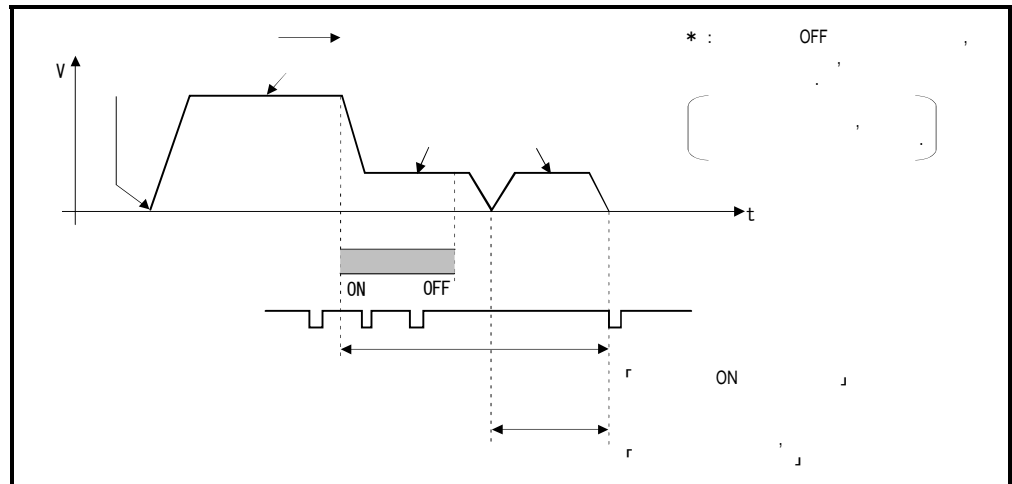
6. 22. 2

(1)

가 ON→OFF

(2)

, 6. 32



6. 32

(3)

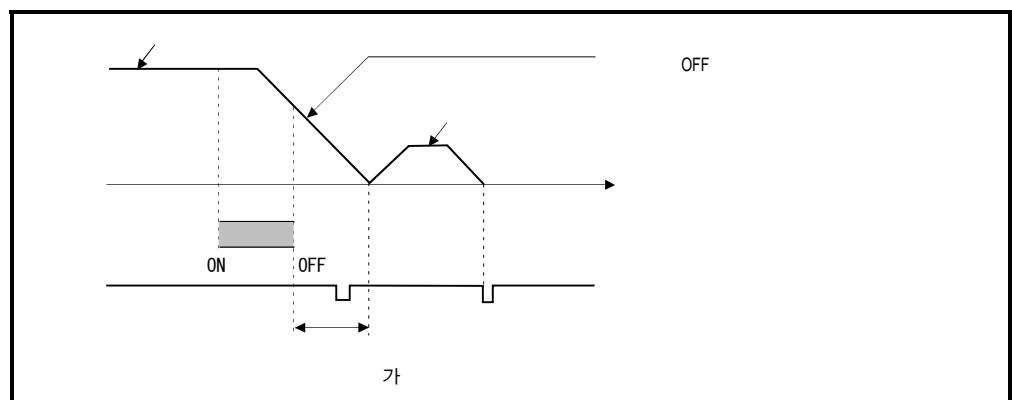
, 6. 22. 5

(4)

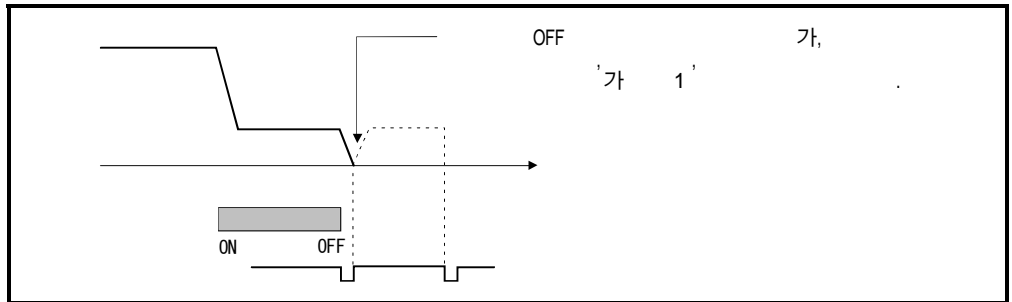
(a)

ON

가 OFF



(b) 「 」 , 1 1/2 , 가
 OFF
 「 」 1/2 ,
 가 1 .



, JOG ON ,
 가 ,
 (1) ON→OFF
 (2) , OFF→ON

6. 22. 3

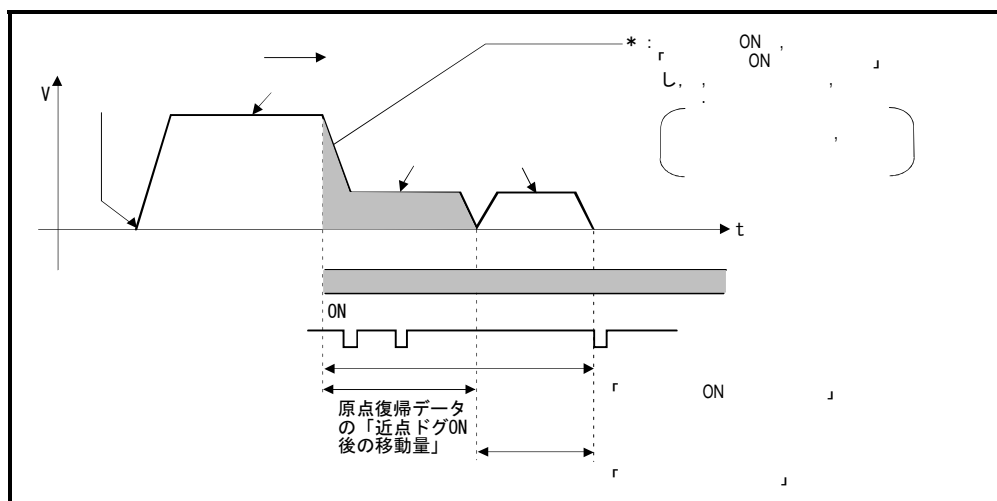
(1)

ON , (ON)

ON , (6.22.1)

(2)

, 6.33 .



6. 33

(3)

, 6. 22. 5 .

(4)

(a) OFF ,

(b) _____, ON

ON ,
OFF ,

6. 22. 4

(1)

(2)

(a)

1

가,

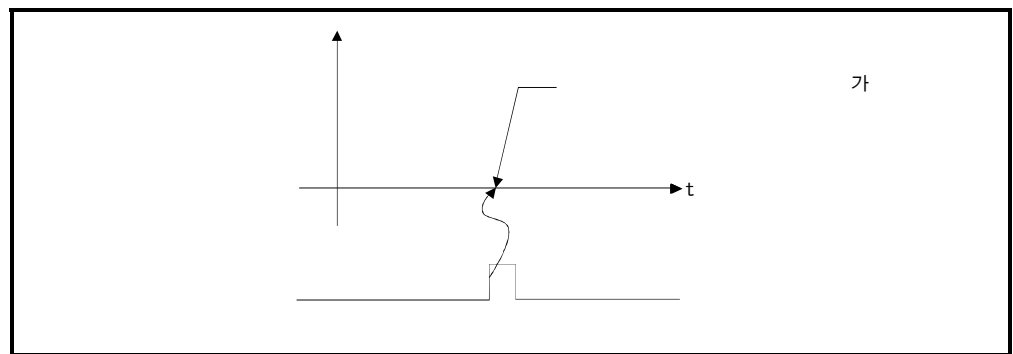
가

(b)

2

가,

가



6. 34

(3)

, 6. 22. 5

(4)

(a)

가

가

"

JOG

"가

(M2406+20n)

,

가 1

(b)

(c)

, ZERO .

ZERO

 $O :$

【 】

(1) (6.22.1)

- 6. 22. 2
- 6. 22. 3
- 6. 22. 4

【 】

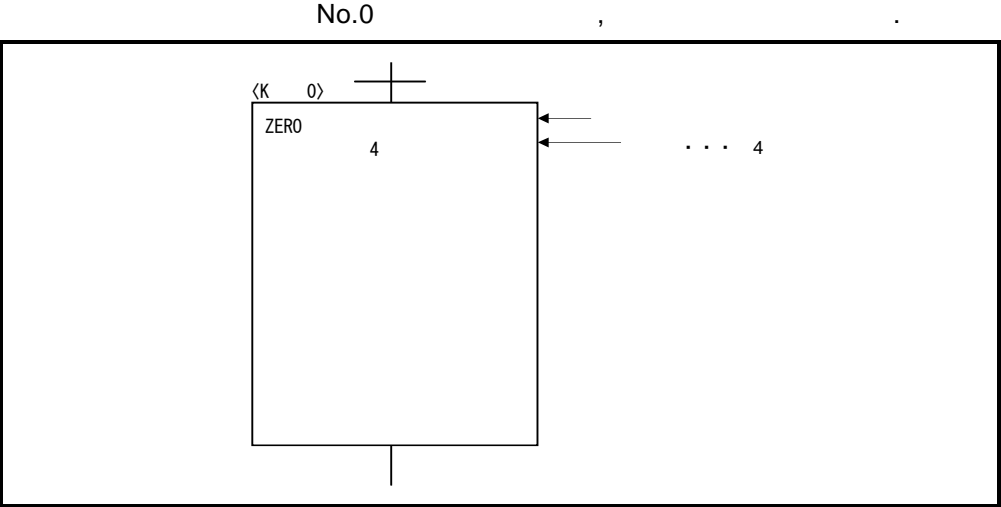
(1)

4



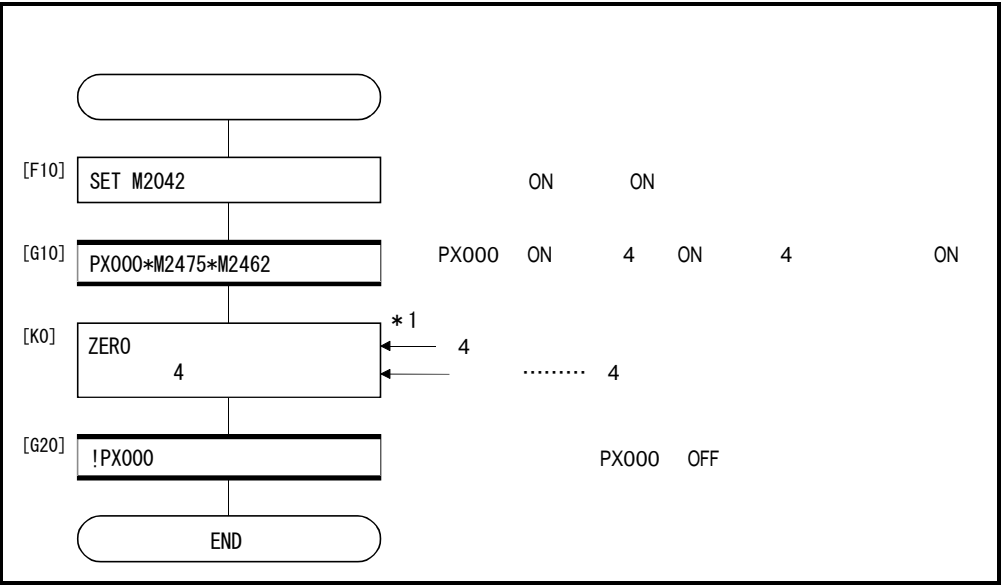
(PX000)

(2)



(3) SFC

SFC



*1 : 가 ON 가
*2 : SFC /

6.

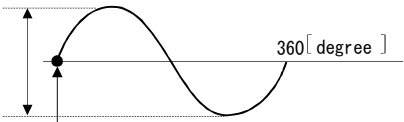
6. 23

			No.	/				M						가			S T O P	S		W A I T I O N / O F F	
OSC		1	△	○				△								△			△		가

○ :
△ :

【 】

가



(1)

, 1~2147483647

(2)

가, sin 가
, 0~359.9[degree]

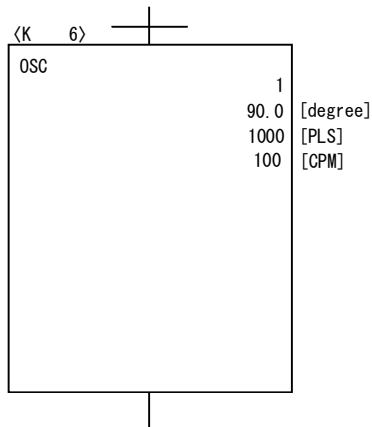
(3)

1 sin 가
, 1~5000[CPM]

가										
90	270									

•

•

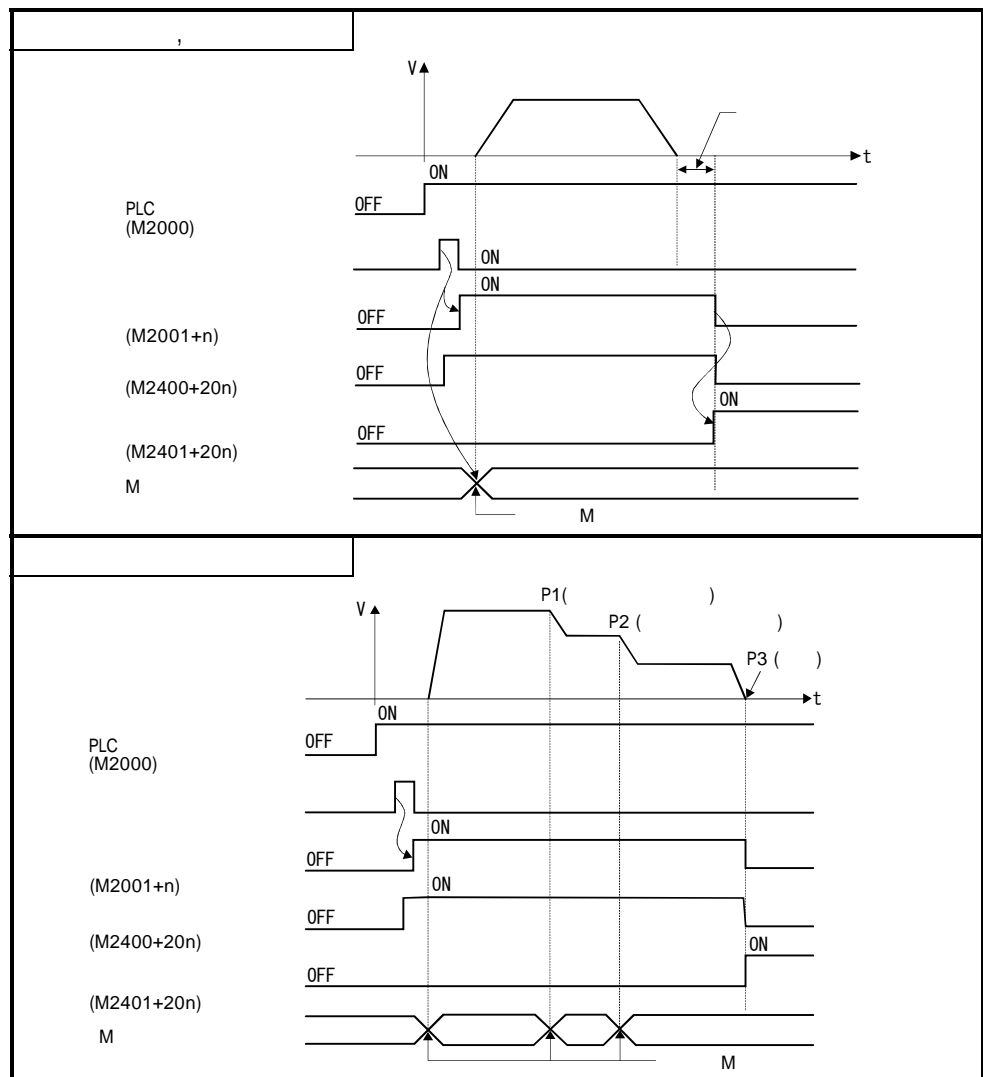


CPU / ,

7.1 M

- M , 가 0~255 .
- M SFC , (,)
- (1) M
- M , . 가 .
- (2) M
- (a) M (,)
- M
- , M
- (b) M ,
- (M2400+20n) .

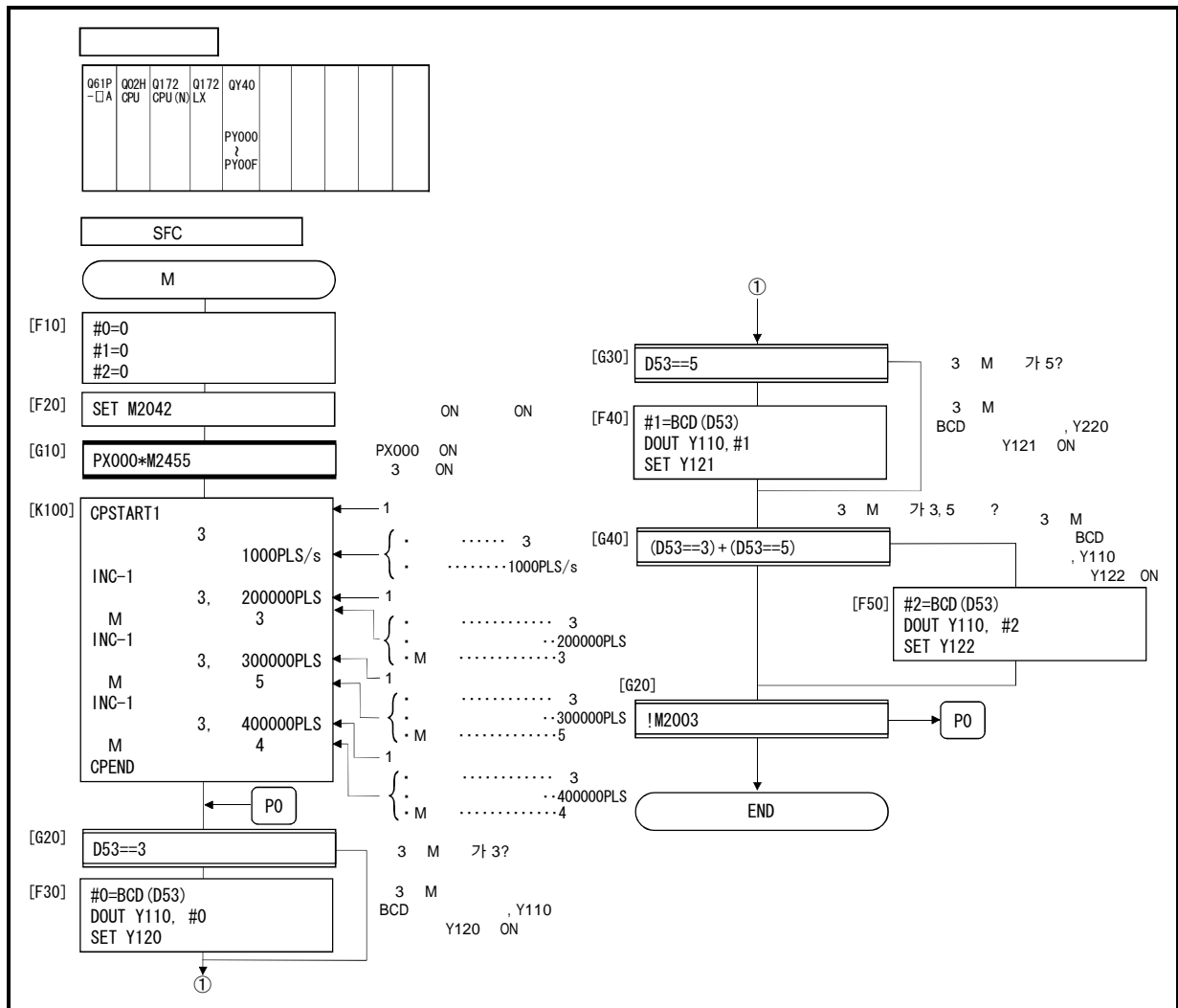
(c) M
(M2401+20n)



(3) M
M 0 , M 가 .
, M 가)
, M 가 ,
M .

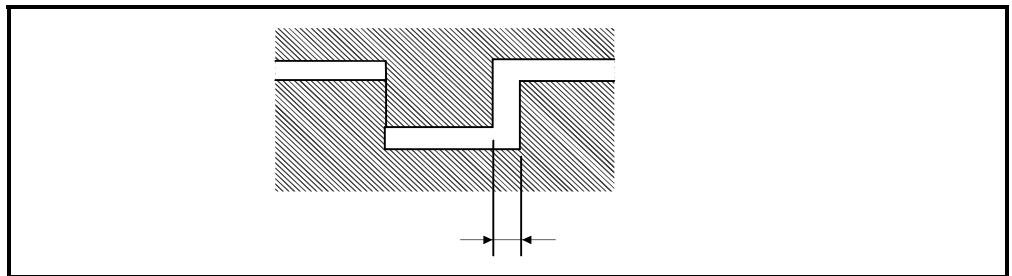
(4)
(a) M SFC ,
No. 3
M M BCD
Y110~Y11F
M
a) M 가 3 Y120 ON
b) M 가 5 Y121 ON
c) M 가 (3 5) ... Y122 ON

(b) SFC , .



7.2

, () , JOG , .



7. 1

(1)

, , ,
([mm],[inch],[degree],[PLS]) ,

- (a) 가 [mm]
- 0~6553. 5
 - $0 \leq \frac{(\quad)}{(1 \quad)} \leq 65535 [PLS]$
- (b) 가 [inch],[degree]
- 0~0. 65535
 - $0 \leq \frac{(\quad)}{(1 \quad)} \leq 65535 [PLS]$
- (c) 가 [PLS]
- 0~65535
 - $0 \leq \frac{(\quad) \times (1 \quad)}{(1 \quad)} \leq 65535 [PLS]$

(2)

, 7. 2 .

7. 2

ON	• = , . • , .
JOG	• JOG , .
	• , .
	• , .
	• , .
	• OFF , .

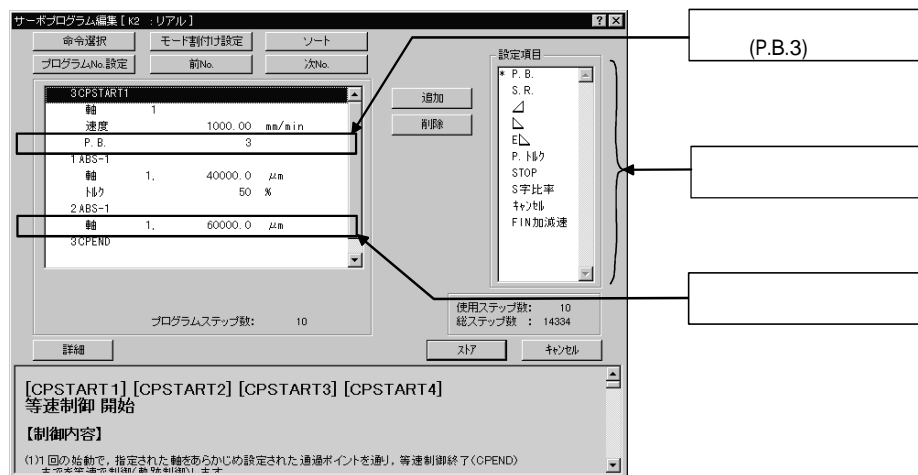
(1)	, 가 .
(2)	, .

7.3

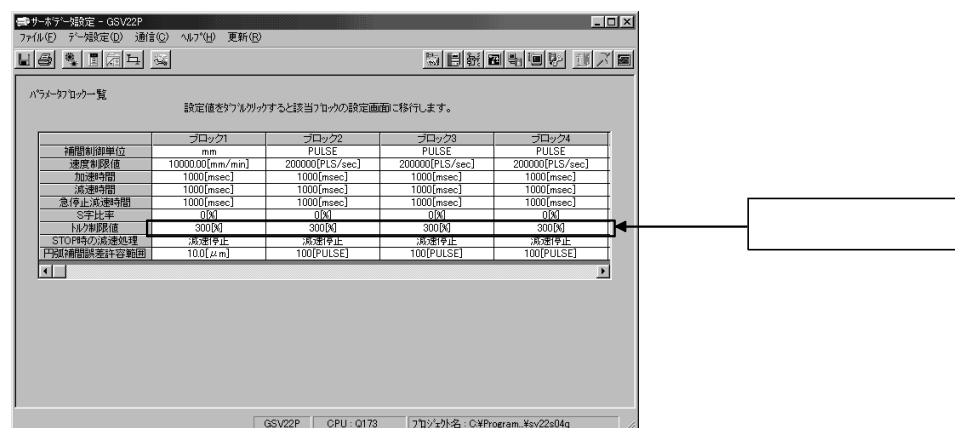
- (1) 1~500[%] 가 .
- (2)
- (a) (4. 4) " " No. ,
- (b) " " ,
- (c) SFC SFC (CHGT) , 'Q173CPU/Q172CPU (SV 13/SV22) (SFC)

[(CPSTART1)]

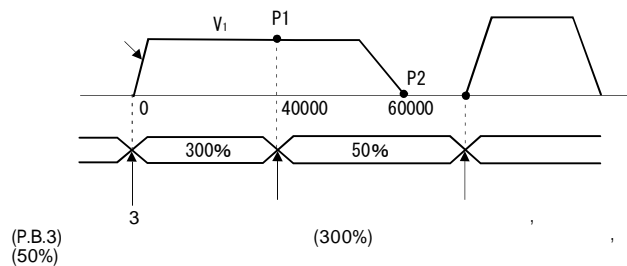
(1)



(2)



(3)



7.4


- ,
ON
SFC
(1)
(2) 가
(a)
(b) (ON)
(c)
(d)
(3) 「 -Power Off 」, 「
가 가
(Power Off
(a)
/ / /
ON/OFF
SFC, M9028() ON 가
MR-H BN (22kw) [BCD-B13E000-B2], MR-J2- B
[BCD-B2OW200-A1],MR-H BN(30kw)//MR-H BN4/
MR-J2S- B/MR-J2M-B/MR-J2-03B5[]
, 1
):
(.)
CPU
) : 가

ON , 가
 , 「Q173CPU/Q172CPU
 (SV13/SV22) (SFC)」 「18.1
 」
 , 「 1.1 」

(4) OS ,
 [2025] " "() ,

OS S/W ()		
L	[2025] 가 ON CPU OFF ON , [1201]가 가 ON	가 ON [2025]
K	[2025] ON CPU OFF ON , [1201]가 가 가	[2025]

) : SV13/SV22 ,



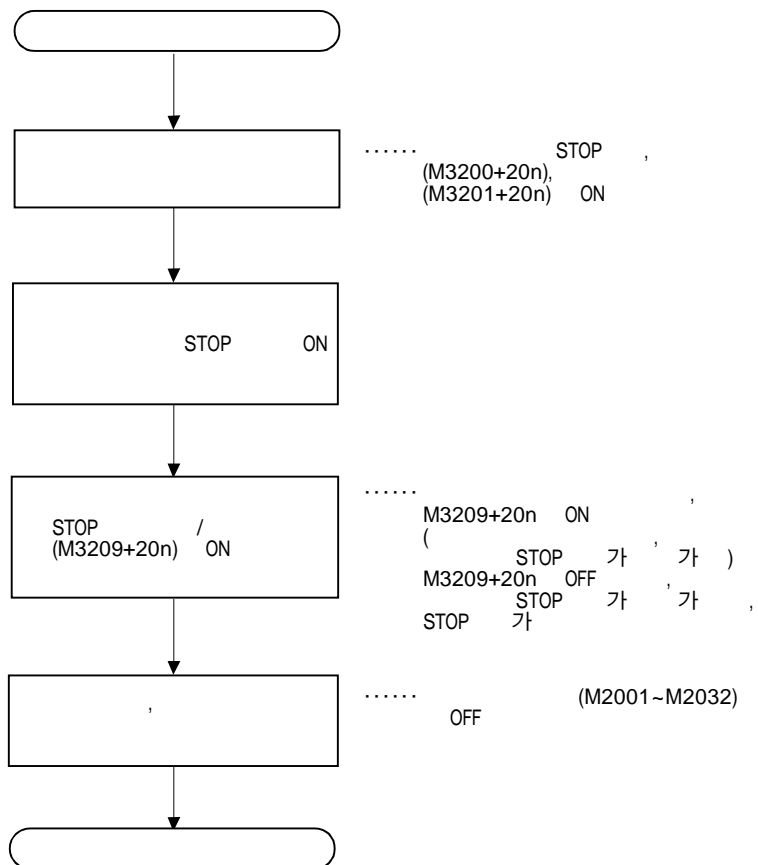
-
-
-

-
- (1) , -2147483648~2147483647
 가
 , [degree]
- (2) ,
- (3) ,

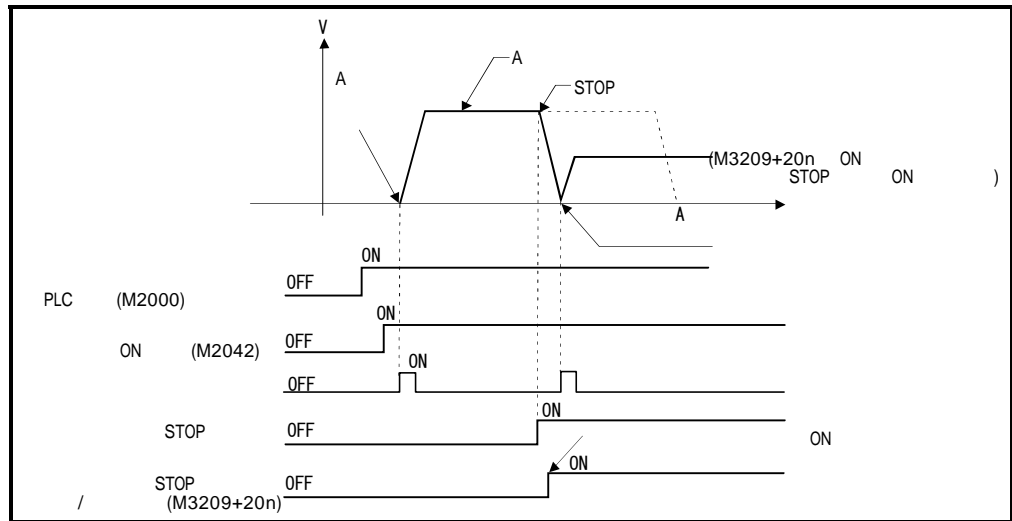
7.5

가 가 , ON() ,
 가 가 .
 , " " 가 가 .
 • CP (6. 17. 6)
 .
 , STOP ON , [***]가
 , M3209+20n ON , , STOP 가 ON

(1) STOP SFC ,



(2)



7. 6

CPU

(D, W)

(1) 가

()	2	$10^{-1}[\mu\text{m}]$, $10^{-5}[\text{inch}]$, $10^{-5}[\text{degree}]$, [PLS]	
	2	$10^{-1}[\mu\text{m}]$, $10^{-5}[\text{inch}]$, $10^{-5}[\text{degree}]$, [PLS]	
()	2	[PLS]	
M	1	—	
	1	[%]	
	1	[%]	
	2	[r/min]	
	2	[PLS]	
가	2	[PLS]	SV22가
	2	[PLS]	
가 M	1	—	
	2	[PLS]	
1	2	[PLS]	
No.	1	—	
	2	$10^{-1}[\mu\text{m}]$, $10^{-5}[\text{inch}]$, [PLS]	
(4)	2	—	

(2)

			가
QI72EX	TREN	0.8[ms]	2
QI73PX			3
PLC	PX		8

) : PLC , 1 가

7.7

ON
 【 】

(1) ON

【 】

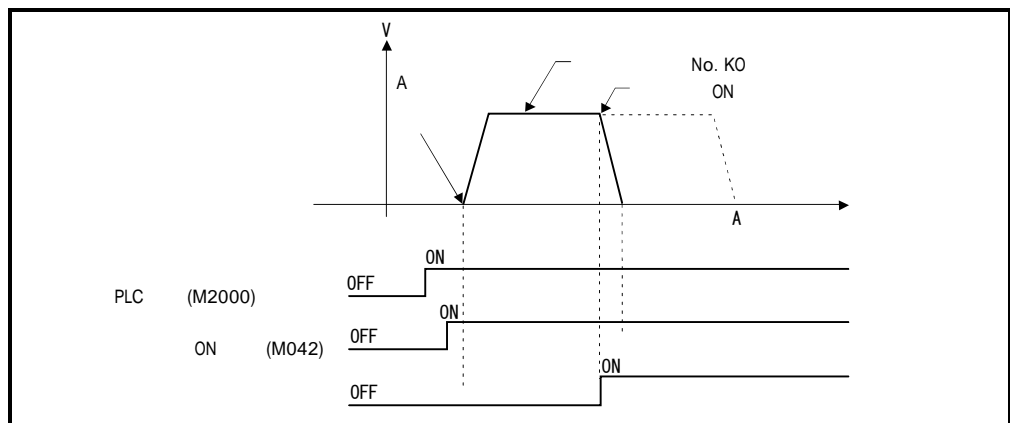
(1)
 가
 X, Y, M, B, F

【 】

(1) (ZERO), (START)
 가/ 가 , (5.2 (2))

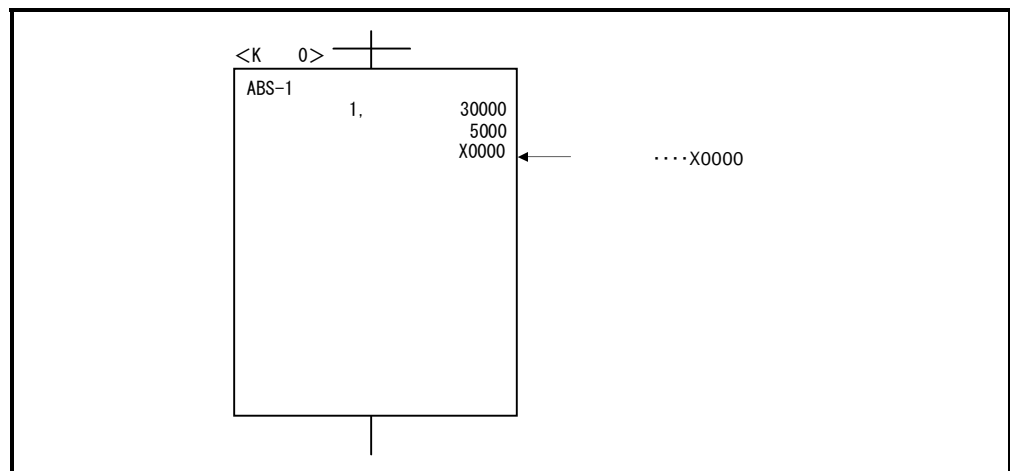
【 】

ON



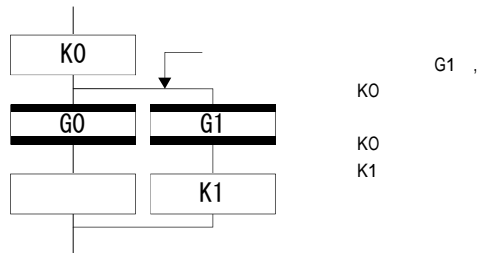
【 】

SFC



7.7.1 .

- SFC 가 , , SFC .



7. 8

- (1) , ABS .
- (a) .
- 3.5[ms] 180° . (, .)
 - . (, .)
- (b) .
- ON .
 - OFF .
 - .
- (c) Power Off , OFF ON .
- (, .)
- (2) /
- / , .

	MR-H BN (22kw) : BCD-B13W000-B2 MR-H BN (30kw) : MR-H BN4 : MR-J2S- B : MR-J2M-B : MR-J2- B : BCD-B20W200-A1 MR-J2-03B5 :	
	MR-H BN : BCD-B13W000-B1 MR-J2- B : BCD-B20W200-A0	가
	FR-V500 :	

[illegible]

1 CPU가

- CPU , 가
- (1)
- ,
 - No. (M9079)가 ON No. (D9189)
 - (M9190)
- (2)
- (a)
- 가 ,
- SFC , 1~999
- SFC /
- SFC , 1000~1999
- SFC
- 가 , 2000~2999

(b) , 가 ON , 가

1. 1

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	
	D6	D26	D46	D66	D86	D106	D126	D146	D166	D186	D206	D226	D246	D266	D286	D306	M2407+20n
	D7	D27	D47	D67	D87	D107	D127	D147	D167	D187	D207	D227	D247	D267	D287	D307	
	D8	D28	D48	D68	D88	D108	D128	D148	D168	D188	D208	D228	D248	D268	D288	D308	M2408+20n

	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	
	D326	D346	D366	D386	D406	D426	D446	D466	D486	D506	D526	D546	D566	D586	D606	D626	M2407+20n
	D327	D347	D367	D387	D407	D427	D447	D467	D487	D507	D527	D547	D567	D587	D607	D627	
	D328	D348	D368	D388	D408	D428	D448	D468	D488	D508	D528	D548	D568	D588	D608	D628	M2408+20n

* Q172CPU(N) 1~ 8 가

(c) 가 , 가 , 가 , GSV13P/GSV22P ,

(d) (M3208+20n) ON (M3207+20n)

(1)	(M3208+20n : ON)
(2)	

1. 1 (D9190)

1. 2 * n , No.(1~32) 1. 2 .

1. 2

D9190																			
1	No.	No. 1~64	No. "1" ,	No. 1~64															
n03*	/ (,) (,))	(1) 가 <table border="1"><tr><td></td><td colspan="2"></td></tr><tr><td>degree</td><td>0~35999999</td><td>$\times 10^{-5}$ [degree]</td></tr></table>				degree	0~35999999	$\times 10^{-5}$ [degree]	(1) () (2)	(1) 가 [degree] 0~35999999									
degree	0~35999999	$\times 10^{-5}$ [degree]																	
(2) 2147483648(H80000000)	(3) 가	(2) $0 \sim \pm (2^{31} - 1)$																	
4		(1) 1~ (2) <table border="1"><tr><td></td><td colspan="2"></td></tr><tr><td>mm</td><td>1~ 600000000</td><td>$\times 10^{-2}$ [mm/min]</td></tr><tr><td>inch</td><td>1~ 600000000</td><td>$\times 10^{-3}$ [inch/min]</td></tr><tr><td>degree</td><td>1~ 2147483647</td><td>$\times 10^{-3}$ [degree/min]</td></tr><tr><td>PLS</td><td>1~ 10000000</td><td>[PLS/s]</td></tr></table>				mm	1~ 600000000	$\times 10^{-2}$ [mm/min]	inch	1~ 600000000	$\times 10^{-3}$ [inch/min]	degree	1~ 2147483647	$\times 10^{-3}$ [degree/min]	PLS	1~ 10000000	[PLS/s]	(1) 0 (2)	1~
mm	1~ 600000000	$\times 10^{-2}$ [mm/min]																	
inch	1~ 600000000	$\times 10^{-3}$ [inch/min]																	
degree	1~ 2147483647	$\times 10^{-3}$ [degree/min]																	
PLS	1~ 10000000	[PLS/s]																	
5		0~5000	(0)	0~5000															
6	M	M 0~255	(0)	M 0~255															
7		1~500		1~500															

1. 2

D9190								
n08*	() ()	(1) 가 <table><tr><td></td><td></td></tr><tr><td>degree</td><td>0~35999999 × 10⁻⁵[degree]</td></tr></table>			degree	0~35999999 × 10 ⁻⁵ [degree]		(1) 가 [degree] 0~35999999
degree	0~35999999 × 10 ⁻⁵ [degree]							
(2) 가 -2147483648 (H80000000)	(2) 0 ~ ± (2 ³¹ - 1)							
n09*	() ()	(1) 가 <table><tr><td></td><td></td></tr><tr><td>degree</td><td>0~35999999 × 10⁻⁵[degree]</td></tr></table>			degree	0~35999999 × 10 ⁻⁵ [degree]		(1) 가 [degree] 0~35999999
degree	0~35999999 × 10 ⁻⁵ [degree]							
(2) 0	(2) 1 ~ (2 ³¹ - 1)							
n10*	() ()	(1) 가 <table><tr><td></td><td></td></tr><tr><td>degree</td><td>0~35999999 × 10⁻⁵[degree]</td></tr></table>			degree	0~35999999 × 10 ⁻⁵ [degree]		(1) 가 [degree] 0~35999999
degree	0~35999999 × 10 ⁻⁵ [degree]							
(2) 가 -2147483648 (H80000000)	(2) 0 ± (2 ³¹ - 1)							
11		가, 0~3	(3)	0~3				
12			(200000[PLS/s]	[PLS] 1~10000000 [PLS/s]				
13	가	가, 0	(1000)	가 1~65535				
	FIN 가	FIN 가, 1~5000		FIN 가 1~5000				
14		, 0		1~65535				
15		, 0		1~65535				
16		, 1~500		(300[%])	1~500			

1. 2

()

D9190																
17		가, <table><tr><td></td><td colspan="2"></td></tr><tr><td>mm</td><td rowspan="4">1~100000</td><td>$\times 10^{-1} [\mu\text{m}]$</td></tr><tr><td>inch</td><td>$\times 10^{-5} [\text{inch}]$</td></tr><tr><td>degree</td><td>$\times 10^{-5} [\text{degree}]$</td></tr><tr><td>PLS</td><td>[PLS]</td></tr></table>				mm	1~100000	$\times 10^{-1} [\mu\text{m}]$	inch	$\times 10^{-5} [\text{inch}]$	degree	$\times 10^{-5} [\text{degree}]$	PLS	[PLS]	(100[PLS])	
mm	1~100000	$\times 10^{-1} [\mu\text{m}]$														
inch		$\times 10^{-5} [\text{inch}]$														
degree		$\times 10^{-5} [\text{degree}]$														
PLS		[PLS]														
18		가, 1~32767	1	1~32767												
19	START	(1) START (2) START (3)		(1) START (2) START (3)												
20				CPSTART CPEND												
21																
22	S	S 가 , S 0~100[%]	S 100[%]	S 0~100[%]												
23	VSTART	VSTART~VEND , FOR~NEXT 1		VSTART~VEND , FOR~NEXT												
24	No.	No.가 0~4095		No. 0~4095												
25		, 1~2147483647 가		1~2147483647												
26		0~3599 ($\times 0.1[\text{degree}]$) 가		0~3599($\times 0.1[\text{degree}]$)												
27		가, 1~5000[CPM] 가		1~5000[cpm]												
28		가 0~999		0~999												

1. 2

()

D9190				
900				No.
901		No. , No.가		No.
902		가 가 (가)		
903		, 가		
904		가		
905		가 가 (VPF, VPR, VSTART, ZERO, VVF VVR, OSC)		
906	No.			No.
907		→가		M2043 (/가)
908		가 →		M2044(/가)

(2) (100~199)

1.4

* :

1.4 (100~199)

							J O G				O S C			
100	○	○	○	○	○	○	○	○	○	○	○	• PLC (M2000), PCPU (M9074)가 OFF		• CPU RUN
														• PLC (M2000) ON
101	○	○	○	○	○	○	○	○	○	○	○	• (M2001~M2032)가 ON		• () OFF
103	○	○	○	○	○	○	○	○	○	○	○	• (M3200+20n) ON		• (M3200+20n) OFF
104	○	○	○	○	○	○	○	○	○	○	○	• (M3201+20n) ON		• (M3201+20n) OFF
105	○					○	○				○	• ,		• JOG
106*	○	○				○	○				○	○	•	•
107	○						○					가 [, ,]		•
108*	○						○					가 [, ,]		•
109	○						○					가 [, ,]		•
110*	○						○					가,		•
111				○								가 ,		•

1. 4

(100~199) ()

							J O G				O S C			
115									○			ON (M2401+20n)가		가 JOG ON
116							○					• JOG 가, 0 • JOG 가, JOG	JOG	• ()
117							○					• JOG		•
118					○							• 가 • 가		• 가
120									○			• ZCT • (M2406+20n)가 OFF	가	•
140	○											• 0		• 0
141									○			• 가, 가		•
142				○					○			•		•
151												• 가 (가 가)		• , 가 ,
152												• 가 OFF (M20420FF)		
153												• 가		

(3) (200~299)

1.5

1.5 (200~299)

							J O G				O S C				
200	○	○	○	○	○	○	○	○			○	○	PLC (M2000)가 OFF		PLC (M2000) ON
201										○			PLC (M2000)가 OFF		PLC (M2000) ON (M3200+20n), (M3201+20n) OFF
202										○			(M3200+20n) ON	[JOG ON 가]	
203										○			(M3201+20n) ON		
204	○	○	○	○	○	○	○	○	○	○	○	○	PLC (M2000) OFF PLC (M2000) OFF → ON		PLC (M2000) OFF → ON [PLC (M2000)가 OFF → ON]
206										○			([BS])		JOG ON 가 OFF JOG ON 가 [가 ON]
207	○				○	○	○				○		/		가
208	○				○	○		○					/		()

1.5

(200~299)

()

							J O G				O S C			
209				○					○			(CHANGE)		
210				○								(CHANGE)		가
211						○								
214									○			가		
215												가		~
														가
														SFC
220												가 degree 0~35999999	(M2001+n OFF)	가 degree 0~ 35999999
												가		
225						○						가		1~

(4) , (300~399)

1.6

1.6 , (300~399)

							J O G				O S C			
300	○	○	○	○	○	○	○	○	○	○	○	• • • OFF		• (1) (M2001~M2032) OFF (2) (M2415+20n) ON
301										○		•		•
302	○					○						•		•
303	○	○		○	○	○					○	•		•
304							○					• JOG (M3202+20n, M3203+20n) OFF		• JOG (M3202+20n, M3203+20n) OFF
305				○	○		○				○	• 가, 0~		• 0~
	○	○	○			○						• 0~		• 0~
309												• degree 0~35999999 ($\times 10^{-5}$ [degree])		• 0~35999999($\times 10^{-5}$ [degree])
310											○	• • 0		•
311												• (CHGT) 1~500[%]		• 1~500[%]
312												• (CHGT)		•

(5) (900~999)

1. 7 , (900~999)

							J O G				O S C		
900												· 「 」 · (MR-J2- B) ·	가 · ·
901												· 「 」 · 「PowerOff 」 · OFF	가 · ·

1.3

, SFC
, 1000~1099
가
(1) (1000~1099)

, 1.8

1.8 (1000~1099)

							J O G				O S C			
1000	○	○	○	○	○	○	○	○	○	○	○	• STOP() 가 ON		• STOP OFF
1001	○	○	○	○	○	○	○	○	○	○	○	• (가) FLS(LS) 가 OFF		• JOG ,
1002	○	○	○	○	○	○	○	○	○	○	○	• () RLS(LS) 가 OFF		• JOG ,
1003											○	• DOG () 가 ON		• , JOG ON
1004	○	○	○	○	○	○	○	○	○	○	○	• 가 (M2415+20n : OFF) (1) OFF (2) ON (3) (4) (5)		• 가 (M2415+20n : OFF)가
1005	○	○	○	○	○	○	○	○	○	○	○	• (M2408+20n) ON		• (M3208+ 20n) (M2408+20n)

(2) (1100~1199)

1.9

1. 9 (1100~1199)

							J O G				O S C			
1101	○	○	○	○	○	○	○	○	○	○	○	• (가) • RLS (LS) 가 OFF	「STOP 」	• JOG •
1102	○	○	○	○	○	○	○	○	○	○	○	• () • RLS (LS) 가 OFF		• JOG •
1103										○		• STOP ()가 ON		• JOG ON
1104	○	○	○	○	○	○	○	○	○	○	○	• ON		•
1105	○	○	○	○	○	○	○	○	○	○	○	• OFF가 • () •	M2415+ 20n OFF	• ON • •

(3)

(1200~1299)

1.10

1. 10

(1200~1299)

							J O G				O S C			
1201												• CPU	ON	
1202												↔	ON, [2016]	
1203												가 . [/3.5[ms] > 180 ° (ON/OFF)		
1204												[PLS] [PLS] bit) 가 (ON/OFF)		

(4)

(1300~1399)

1.11

1. 11

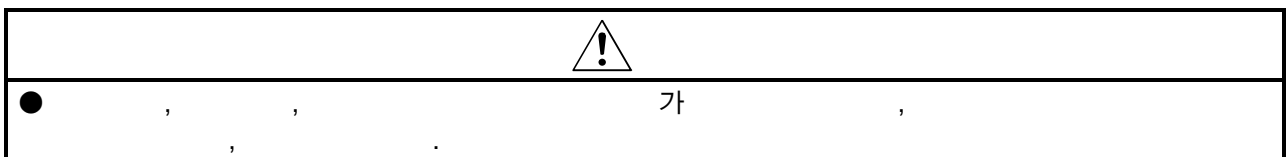
(1300~1399)

							J O G				O S C			
1310												• CPU • CPU		CPU

1. 4

- (1) (2000~2799)
 , 가 , [2000]~[2799]
 , (M2408+20n)가 ON
 , (M3208+20n) ON
 , (, [2100]~[2499]
 , ON .)
-) : 1. ([2030]), 1, 2([2050],
 [2051]) , 가
 , OFF ,RESET
2. [2030], [2050], [2051] , OFF
 , 가 ,
- (2) (2300~2799)
 , 가 , [2300]~[2799]
 , (M2408+20n),가 ON
 , (M3208+20n) ON
 , (, [2100] ~ [2499]
 , ON .

1.12 .



1. 12

(2000~2799)

2010		<ul style="list-style-type: none"> AC160[V] (AC400[V] AC320[V]) 15[ms] 			<ul style="list-style-type: none"> (R.S.T) 가
2012	1	<ul style="list-style-type: none"> SRAM EPR0M 	<ul style="list-style-type: none"> ON PLC (M2000) CPU ON 		
2013					
2014		<ul style="list-style-type: none"> H/W가 CPU H/W가 			CPU
2015	2	EEPROM			
2016	1		<ul style="list-style-type: none"> ON PLC (M2000) CPU ON 		<ul style="list-style-type: none"> 가 (2 /4)
2017					
2019	3	ROM			
2020	2				가
2021	RD (AC400 [V])	<ul style="list-style-type: none"> (RD)가 OFF (SON) 가 ON 1. 2. 			
2021*1		가			가

1. 12 (2000~`2799) ()

2022*1	1	<ul style="list-style-type: none"> (MR-J2M-P8B) 			<ul style="list-style-type: none"> (MR-J2M-P8B) , (MR-J2M-BU)
		<ul style="list-style-type: none"> (MR-J2M-P8B) 			<ul style="list-style-type: none"> (MR-J2M-P8B)
		<ul style="list-style-type: none"> (MR-J2M-BU) 			<ul style="list-style-type: none"> (MR-J2M-BU)
2023*1	2	<ul style="list-style-type: none"> 			<ul style="list-style-type: none"> (MR-J2M-BU)
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none">
		<ul style="list-style-type: none"> (MR-J2M-BU) 			<ul style="list-style-type: none"> (MR-J2M-BU)
2024		<ul style="list-style-type: none"> U, V, W가 			<ul style="list-style-type: none"> 가
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none">
2024*1		<ul style="list-style-type: none"> (MR-J2M-BU) 			<ul style="list-style-type: none"> (MR-J2M-BU)
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none">
2025	()	<ul style="list-style-type: none"> 	<ul style="list-style-type: none"> ON 		<ul style="list-style-type: none"> 2~3
		<ul style="list-style-type: none"> 	<ul style="list-style-type: none"> PLC (M2000) 		<ul style="list-style-type: none"> OFF → ON
		<ul style="list-style-type: none"> (가) 	<ul style="list-style-type: none"> CPU ON 		<ul style="list-style-type: none"> OFF
		<ul style="list-style-type: none"> ON/OFF 			<ul style="list-style-type: none"> [%]
		<ul style="list-style-type: none"> 가 . (가 			<ul style="list-style-type: none"> 가
		<ul style="list-style-type: none"> 가) 			<ul style="list-style-type: none"> 가
2030		<ul style="list-style-type: none"> () 			<ul style="list-style-type: none"> ()
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none">
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none">
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none">
		<ul style="list-style-type: none"> 가 			<ul style="list-style-type: none">

*1 : MR-J2M-B

1. 12 (2000~2799) ()

2031		<ul style="list-style-type: none"> 가, 115[%] 			<ul style="list-style-type: none"> 1, 1가
		<ul style="list-style-type: none"> 가 가 			<ul style="list-style-type: none"> 가 ,
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none"> 1,2 / 1,2 /
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none">
2032		<ul style="list-style-type: none"> U, V, W가 			<ul style="list-style-type: none"> U, V, W가
		<ul style="list-style-type: none"> U, V, W가 			<ul style="list-style-type: none"> U, V, W 가
		<ul style="list-style-type: none"> U, V, W 			<ul style="list-style-type: none"> U, V, W 가
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none">
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none">
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none">
		<ul style="list-style-type: none"> 			<ul style="list-style-type: none">
		<ul style="list-style-type: none"> 가 			<ul style="list-style-type: none">
		<ul style="list-style-type: none"> 가 			<ul style="list-style-type: none"> , 가

1. 12 (2000~2799) ()

2033		<ul style="list-style-type: none"> 400[V] (AC400[V] 800[V]) 가 가 			<ul style="list-style-type: none"> 가 , C-P
					<ul style="list-style-type: none"> C-P
					<ul style="list-style-type: none"> 가 3 ()
					<ul style="list-style-type: none"> (R, S, T)
2034		<ul style="list-style-type: none"> CPU 가 			<ul style="list-style-type: none">
2035		<ul style="list-style-type: none"> CPU , 가 			<ul style="list-style-type: none"> 1 , 1
		<ul style="list-style-type: none"> CPU 가 			<ul style="list-style-type: none">
2036		<ul style="list-style-type: none"> CPU 			<ul style="list-style-type: none">
2038*1	DRU	<ul style="list-style-type: none"> DRU No.2, 23 가 			<ul style="list-style-type: none"> DRU
2042		<ul style="list-style-type: none"> 가 			<ul style="list-style-type: none">

* 1 : MR-J2M-B

1. 12 (2000~2799) ()

2045		<p>가</p> <p>()</p> <p>ON/OFF</p>		<p>가</p> <p>가</p> <p>(MR-H150B</p> <p>)</p> <p>가 (0~</p> <p>+55[])</p> <p>가</p>
2046		<p>가</p> <hr/> <p></p> <hr/> <p></p>		<p>가</p> <hr/> <p>(0~+40[])</p> <hr/> <p></p>
2050	1	<p>200[%]</p> <p>가</p>		<p>가</p> <p>가</p> <p>가</p> <p>U, V, W</p>
2051	2	<p>(95[%]</p> <p>가</p>		<p>가</p> <p>가</p> <p>가</p> <p>/ 1, 2,</p> <p>/ 1,2</p> <p>U, V, W</p> <p>(가)</p>

1. 12 (2000~2799) ()

2052		가,			가 가 1, 2 (가)
2053*1		가 1 (U,V,W) 가			 가 OFF
2054*1		(MR-J2M-BU) 1			
2086	RS232	(

*1 : MR-J2M-B

1. 12 (2000~2799) ()

2102		•			•
2103		•			• • • •
2140		• [2030]가 가 • (85[%])			• [2030]
2141		• [2050],[2051]가 가 • (85[%])			• [2050], [2051]
2146		• CN6 1A,1B ()			• CN6 1A, 1B
2147		• CPU (EMC) 가			•
2149	OFF	• OFF ON (SON) ON • 50[RPM] 215[V]			• ON
2196		• 가 가 가			•

1. 12 (2000~2799) ()

2301 5 2336	()	<div><div>2301</div><div>2302</div><div>2303</div><div>2304</div><div>2305</div><div>2306</div><div>2307</div><div>2308</div><div>2309</div><div>2310</div><div>2311</div><div>2312</div><div>2313</div><div>2314</div><div>2315</div><div>2316</div><div>2317</div><div>2318</div><div>2319</div><div>2320</div><div>2321</div><div>2322</div><div>2323</div><div>2324</div><div>2325</div><div>2326</div><div>2327</div><div>2328</div><div>2329</div><div>2330</div><div>2331</div><div>2332</div><div>2333</div><div>2334</div><div>2335</div><div>2336</div></div> <div><div>1</div><div>1</div><div>2</div><div>2</div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div></div>			

2301		
}		
2339		()

1. 12 (2000~2799) ()

2601 S 2636	()	가	ON PLC (M2000) CPU ON		CPU OFF→ON OFF→ ON PLC (M2000)																																																																								
		<table><tr><td>2601</td><td></td></tr><tr><td>2602</td><td></td></tr><tr><td>2603</td><td></td></tr><tr><td>2604</td><td></td></tr><tr><td>2605</td><td></td></tr><tr><td>2606</td><td></td></tr><tr><td>2607</td><td></td></tr><tr><td>2608</td><td></td></tr><tr><td>2609</td><td></td></tr><tr><td>2610</td><td></td></tr><tr><td>2611</td><td></td></tr><tr><td>2612</td><td></td></tr><tr><td>2613</td><td>1</td></tr><tr><td>2614</td><td>1</td></tr><tr><td>2615</td><td>2</td></tr><tr><td>2616</td><td>2</td></tr><tr><td>2617</td><td></td></tr><tr><td>2618</td><td></td></tr><tr><td>2619</td><td></td></tr><tr><td>2620</td><td></td></tr><tr><td>2621</td><td></td></tr><tr><td>2622</td><td></td></tr><tr><td>2623</td><td>1</td></tr><tr><td>2624</td><td>2</td></tr><tr><td>2625</td><td>3</td></tr><tr><td>2626</td><td>4</td></tr><tr><td>2627</td><td>1</td></tr><tr><td>2628</td><td>2</td></tr><tr><td>2629</td><td></td></tr><tr><td>2630</td><td></td></tr><tr><td>2631</td><td></td></tr><tr><td>2632</td><td>5</td></tr><tr><td>2633</td><td>6</td></tr><tr><td>2634</td><td>PI-PID</td></tr><tr><td>2635</td><td></td></tr><tr><td>2636</td><td>()</td></tr></table>				2601		2602		2603		2604		2605		2606		2607		2608		2609		2610		2611		2612		2613	1	2614	1	2615	2	2616	2	2617		2618		2619		2620		2621		2622		2623	1	2624	2	2625	3	2626	4	2627	1	2628	2	2629		2630		2631		2632	5	2633	6	2634	PI-PID	2635		2636	()
		2601																																																																											
		2602																																																																											
		2603																																																																											
		2604																																																																											
		2605																																																																											
		2606																																																																											
		2607																																																																											
		2608																																																																											
		2609																																																																											
		2610																																																																											
		2611																																																																											
		2612																																																																											
		2613				1																																																																							
		2614				1																																																																							
		2615				2																																																																							
		2616				2																																																																							
		2617																																																																											
		2618																																																																											
		2619																																																																											
		2620																																																																											
		2621																																																																											
		2622																																																																											
		2623				1																																																																							
		2624				2																																																																							
		2625				3																																																																							
		2626				4																																																																							
		2627				1																																																																							
		2628				2																																																																							
		2629																																																																											
		2630																																																																											
		2631																																																																											
		2632				5																																																																							
		2633				6																																																																							
		2634				PI-PID																																																																							
2635																																																																													
2636	()																																																																												
2637 S 2699		가			가																																																																								

1. 12 (2000~2799) ()

		가			
2601 S 2639	()	2601		ON CPU ON	CPU → ON OFF → ON
		2602			
		2603			
		2604			
		2605			
		2606			
		2607			
		2608			
		2609			
		2610	()		
		2611	(3)		
		2612	(4)		
		2613			
		2614			
		2615	PLS		
		2616	PLG		
		2617			
		2618			
		2619			
		2620			
		2621			
		2622	P 1		
		2623			
		2624			
		2625			
		2626			
		2627			
		2628			
		2629			
		2630			
		2631			
		2632	DA1		
		2633			
		2634			
		2635	DA2		
		2636			
		2637			
		2638			
		2639			

1. 12 (2000~2799) ()

2700 ~ 2799			*2 : V500 FR-V5NS FR-
		*2	
	2710	E. OC1 가	
	2711	E. OC2	
	2712	E. OC3	
	2713	E. OV1 가	
	2714	E. OV2	
	2715	E. OV3 ,	
	2716	E. THT ()	
	2717	E. THM ()	
	2718	E. IPF	
	2719	E. UVT	
	2720	E. BE	
	2721	E. GF	
	2722	E. OHT	
	2723	E. OLT	
	2724	E. OPT	
	2725	E. OP1 (1)	
	2726	E. OP2 (2)	
	2727	E. OP3 (3)	
	2728	E. PE	
	2729	E. PUE PU	
	2730	E. RET	
	2731	E. CPU CPU	
	2733	E. FIN	
	2734	E. OS 가	
	2735	E. OSD	
	2736	E. ECT	
	2737	E. OD	
	2738	E. ECA	
	2739	E. MB1 1	
	2740	E. MB2 2	
	2741	E. MB3 3	
	2742	E. MB4 4	
	2743	E. MB5 5	
	2744	E. MB6 6	
	2745	E. MB7 7	
	2746	E. P24 DC24V	
	2747	E. CTE	
	2748	E. LF	
	2749	E. P12 DC12	
	2750	E. EP	
	2756	E. 1 1	
	2757	E. 2 2	
	2758	E. 3 3	
	2761	E. 6 6	
	2762	E. 7 7	

1.5 PC

1.13 PC

D9196		
01	<ul style="list-style-type: none"> • PC 가 • 	<ul style="list-style-type: none"> • PC • • • A OBD-PCF/A30CD-PCF가
02	<ul style="list-style-type: none"> • CRC 가 	<ul style="list-style-type: none"> • • •
03	<ul style="list-style-type: none"> • ID가 	<ul style="list-style-type: none"> • A OBD-PCF/A30CD-PCF가 • A OBD-PCF/A30CD-PCF
04	<ul style="list-style-type: none"> • 가 	<ul style="list-style-type: none"> • •
05	<ul style="list-style-type: none"> • PC 가 	<ul style="list-style-type: none"> • PC

2

2. 1

SFC , CPU
CPU ON/OFF

	•
	•
	•
	•
	•
()	<p>< ></p> <p>S : (CPU)</p> <p>U : (SFC)</p> <p>S/U : (CPU) /</p> <p>< >→ (CPU)</p> <p>: (CPU)</p> <p>: (ON,</p> <p>: 가</p> <p>: 가 ()</p> <p>:</p>

2. 1

				()	
M9000		OFF : ON :	가 CPU 1 ON	S ()	
M9005	AC/DC DOWN	OFF : AC/DC DOWN ON : AC/DC DOWN	AC 20ms ON, OFF ON DC 10ms ON, OFF ON		
M9006		OFF : ON :	BAT. LED OFF " "		
M9007		OFF : ON :	BAT. LED ON " "		
M9008		OFF : ON :	가 ON ON		
M9010		OFF : ON :	가 ON ON		*1
M9025		OFF : ON:	가 OFF ON, D9025~D9028	U	
M9026		OFF: ON :	(D9025~D9028) 가 ON, 가 OFF	S ()	
M9028		OFF : ON :	가 ON, BCD D9025~ D9028	U	
M9036	ON	ON _____ OFF	RUN / STOP ON	S ()	
M9037	OFF	ON _____ OFF	RUN / STOP OFF		
M9060		OFF ON:		U	*1
M9073	PCPU WDT	OFF : ON :	CPU "WDT" ON CPU WDT (D9184)	S ()	
M9074	PCPU	ON : PCPU OFF : PCPU	PLC (M2000) OFF ON ON PLC (M2000)가 OFF OFF	S ()	
M9075		ON : OFF :	(M9078)가 ON	S ()	
M9076		ON : ON OFF : OFF	ON/OFF	S ()	
M9077		ON : 1 OFF :	(D714~D719) / M9077 ON (D9185~D9187)	S ()	
M9078		ON : OFF :	ON M9078 ON (D9182, D9183)		
M9079		ON : OFF :	SFC (K) ON D9198, D9190		

*1 : Q 가

2. 1 ()

				()	
M9104		OFF ON :	• OFF ON , D9104 CPU .	U	*1
M9105		ON : OFF :	• CPU ON . OFF .	S ()	
M9216	1 MULTR	OFF ON : 1	• MULTR 1 ON .	S ()	
M9217	2 MULTR	OFF ON : 2	• MULTR 2 ON .		
M9218	3 MULTR	OFF ON : 3	• MULTR 3 ON .		
M9219	4 MULTR	OFF ON : 4	• MULTR 4 ON .		
M9240	1	OFF : 1 ON : 1	• 1 CPU OFF . • 1 CPU (CPU) ON . • 가 .	S ()	
M9241	2	OFF : 2 ON : 2	• 2 CPU OFF . • 2 CPU (CPU) ON . • "MULTI CPU DOWN" (: 7000) .		
M9242	3	OFF : 3 ON : 3	• 3 CPU OFF . • 3 CPU (CPU) ON . • "MULTI CPU DOWN" (: 7000) .		
M9243	4	OFF : 4 ON : 4	• 4 CPU OFF . • 4 CPU (CPU) ON . • "MULTI CPU DOWN" (: 7000) .		
M9244	1	OFF : 1 ON : 1	• 1 CPU () OFF . • 1 CPU ON . *2		
M9245	2	OFF : 2 ON : 2	• 2 CPU () OFF . • 2 CPU ON . *2		
M9246	3	OFF : 3 ON : 3	• 3 CPU () OFF . • 3 CPU ON . *2		
M9247	4	OFF : 4 ON : 4	• 4 CPU () OFF . • 4 CPU ON . *2		

*1 :

Q

가

*2 :

1

	$\begin{aligned} S &: (CPU) \\ U &: (SFC) \\ S/U &: (CPU) / \\ < > \rightarrow & (CPU) \\ &: (CPU) \\ &: (ON,) \\ &: 가 \\ &: \\ &: 가 () \end{aligned}$
--	----------------------------------------------------------------------------------------------------------------------------------------------------------

2. 2

				()	
D9000	No.	No.	가 I/O No.가	S()	*
D9005	AC/DC DOWN No.	AC/DC DOWN	CPU가 , 85% (AC /65%DC) 가 +1 , BIN		
D9008			가 BIN , 「 Q173CPU/Q172CPU (SV13/SV22) (SFC) 「 18.4 CPU		
D9010			D9008 가 (, 2), BCD 2 B15 ~ B8 B7 ~ B0 () 95 10 (0~99) (1~12) H9510		
D9011			D9008 가 , BCD 2 B15 ~ B8 B7 ~ B0 () 25 10 (1~31) (0~23) H2510	S()	*
D9012			D9008 가 , BCD 2 B15 ~ B8 B7 ~ B0 () 35 48 (0~59) (0~59) H3548		
D9013			(D9014) 가 가 가 0 : 1 : No. (No.) 2 : No.	S()	*
D9014			(D9008) 가 No. (No.) * . . . No. CPU No.가 (가) 1 : 1 2 : 2 3 : 3 4 : 4 No. . . . No.		
D9015	CPU	CPU	CPU 가 B15 B12B11 B8 B7 B4 B3 B0 ② ① : CPU 0 : RUN : STOP 2 : STOP : RUN/STOP 4 :	S()	
D9017		(1ms)	1ms 0~65535(ms)	S/U()	*
D9019		(1ms)	1ms 0~65535(ms)		
D9025		(,)	D9025 (, 2), BCD B15 B12 B11 B8 B7 B4 B3 B0 () 93 , 7 H9307	S/U()	
D9026		(,)	D9026 , BCD B15 B12 B11 B8 B7 B4 B3 B0 () 31 , 10 H3110		
D9027		(,)	D9027 , BCD B15 B12 B11 B8 B7 B4 B3 B0 () 35 , 48 H3548		

* :

Q

가

2. 2 ()

				()	
D9028		()	<div><div><div>D9028</div><div>BCD</div><div><div>B15.....B12B11.....B8B7.....B4B3.....B0</div><div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div><div></div></div></div></div>		

* : Q 가

2. 2

()

				()	
D9196	PC	PC	<div><div>가</div><div>00 : 01 : 02 : CRC 03 : 04 : 05 : (00)</div></div>	S ()	
D9197			<div><div>[μs]</div><div></div></div>	S ()	
D9200		CPU	<div><div>CPU가</div><div><div>B15B12B11B8B7B4B3B0</div><div><div>③②①</div></div></div><div><div>: CPU0 : RUN 1 : STOP 2 : LCLR</div><div>: OFF</div><div><div>: B8~B12가SW1~SW5</div><div>0 OFF, 1 ON B13~B15</div></div></div></div>	S ()	*
D9201	LED	CPU-LED	<div><div>CPU LED가</div><div>012</div><div><div>B15B12B11B8B7B4B3B0</div><div><div>⑧⑦⑥⑤④③②①</div></div></div><div><div>: RUN: BOOT : ERROR: : M.RUN: : BAT.ALARM: MODE MODE 0 : 1 : 2 :</div></div></div>	S ()	

* : Q 가

3.1 M

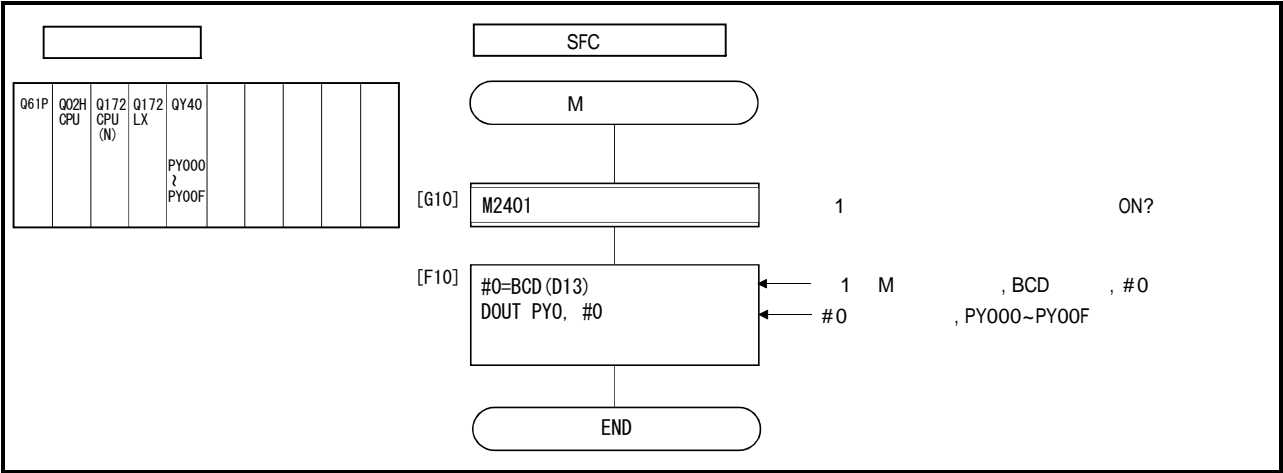
， M

• M2400+20n ()

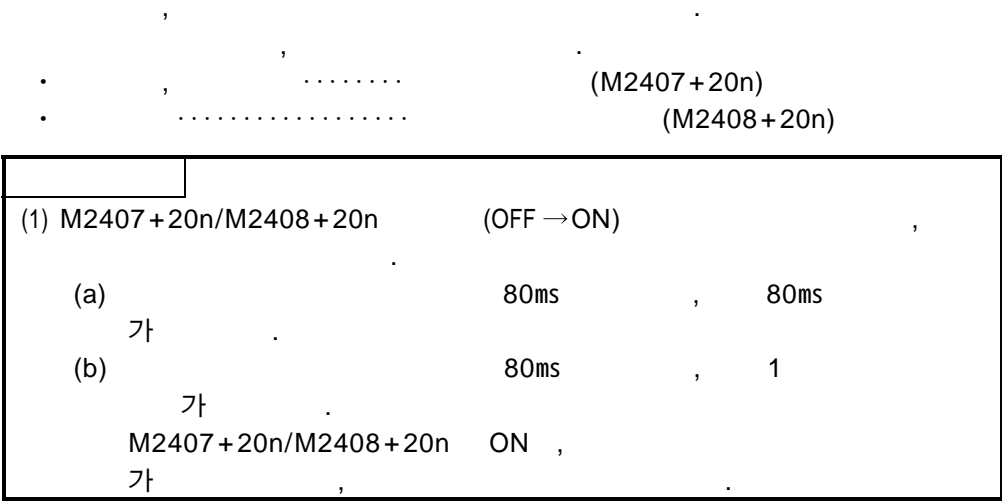
• M2401+20n ()

【 】

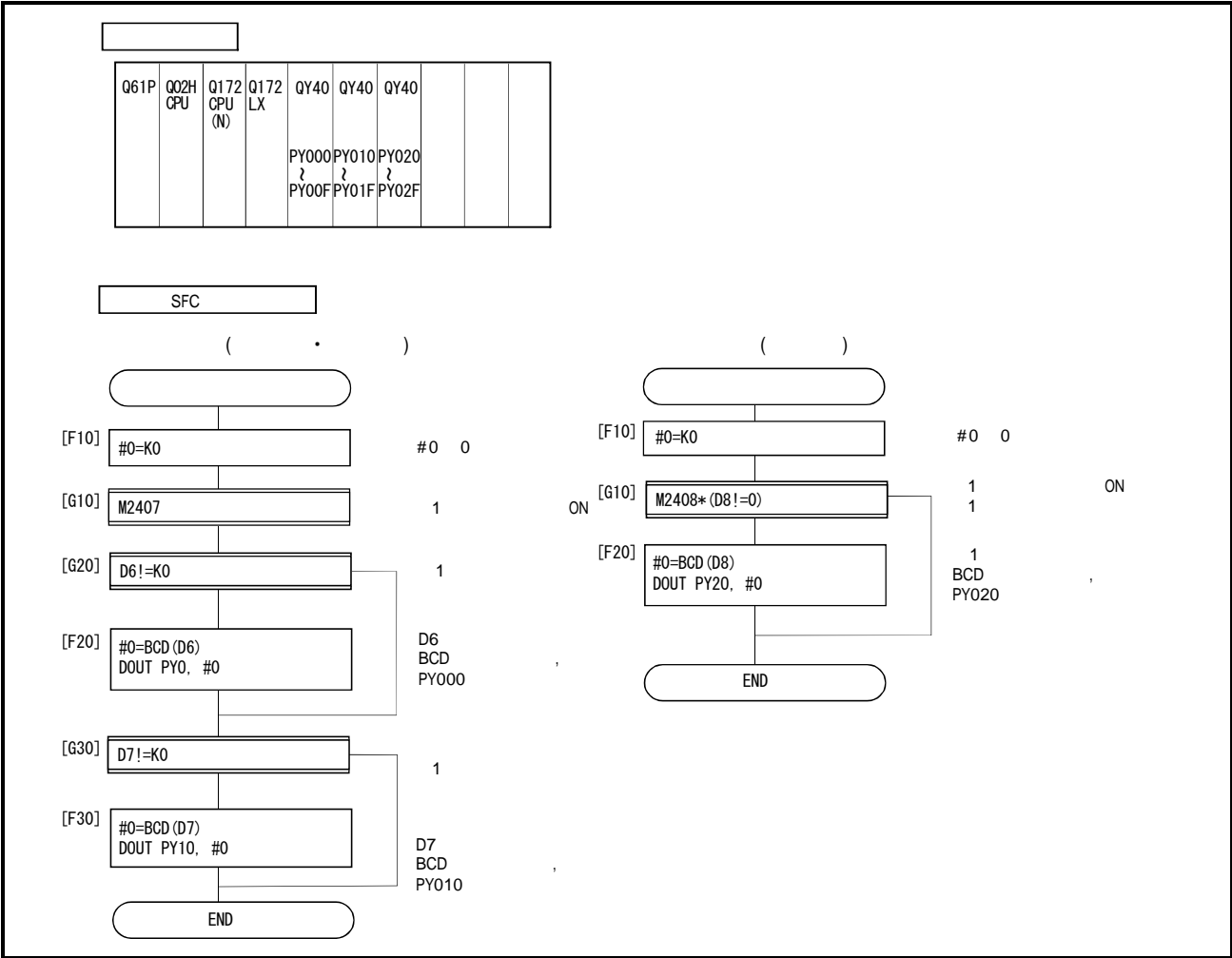
(1) 1 , M , PY000~PY00F



3. 2



(1) 1, BCD, PY000~PY00F(), PY010~PY01F(), PY020~PY02F()



(1) , , M , No.
가 .

M	/	2	<table><tr><td></td><td></td></tr><tr><td>D</td><td>800~8191</td></tr><tr><td>W</td><td>0000~1FFF</td></tr><tr><td>#</td><td>0000~7999</td></tr></table>			D	800~8191	W	0000~1FFF	#	0000~7999					
	D	800~8191														
	W	0000~1FFF														
	#	0000~7999														
		2														
	1															
	1															
	1															
No.	1															
		2														
		2														
		2														
가		1														
		2														
		1														
		1														
		1														
		1														
	STOP	1														
		2														
	S	1														
No.	1															
FIN가	1															
()	1															
(ON/OFF)																
		<table><tr><td></td><td></td></tr><tr><td>X</td><td>0000~1FFF</td></tr><tr><td>Y</td><td>0000~1FFF</td></tr><tr><td>M/L</td><td>0~8191</td></tr><tr><td>M</td><td>9000~9255</td></tr><tr><td>B</td><td>0000~1FFF</td></tr><tr><td>F</td><td>0~2047</td></tr></table>			X	0000~1FFF	Y	0000~1FFF	M/L	0~8191	M	9000~9255	B	0000~1FFF	F	0~2047
X	0000~1FFF															
Y	0000~1FFF															
M/L	0~8191															
M	9000~9255															
B	0000~1FFF															
F	0~2047															

* : 가 .

2	SFC	32
(: #OL, DOL)	.

(2)

CPU가

	↓	ON 「 」 가
CPSTART (FOR~NEXT)	↓ ON) ↓ 「 」 CPU가	「 」

5 CPU

CPU

(1) [ms] ()

	Q173CPU (N)				Q172CPU (N)	
(SV22)	1~4	5~12	13~24	25~32	1~4	5~8
(SV13)	1~8	9~16	17~32	—	1~8	—
[ms]	0.88	1.77	3.55	7.11	0.88	1.77

(2) CPU [ms]

		Q173CPU (N)				Q172CPU (N)	
		0.88[ms]	1.77[ms]	3.55[ms]	7.11[ms]	0.88[ms]	1.77[ms]
*1	WAIT ON/OFF+	1.1~1.6	2.5~3.2	4.3~6	8.1~11.1	1.1~1.6	2.5~3.2
		1.8~2.3	3~3.9	4.8~6.6	9.4~11.5	1.8~2.3	3~3.9
		1.2~2	2.8~3.6	4.5~5.9	8.5~11	1.2~2	2.8~3.6
		0.8	1.7	3.5	3.5	0.8	1.7
*2		1.7~2.5	3.5~4.2	5~6.5	8.6~12	1.7~2.5	3.5~4.2
PLC (M2000) ON→PCPU (M9074)ON		39~433					

*1 : FEED , (/)

*2 :

*3 : MR-H BN 0.8ms MR-H-BN 0.8ms

1.7ms

(3) ()

					*1
M2320					M9000
M2321	AC/DC DOWN				M9005
M2322					M9006
M2323					M9007
M2324					M9008
M2325					M9010
M2326	ON				M9036
M2327	OFF				M9037
M2328					M9026
M2329	PCPU WDT				M9073
M2330	PCPU				M9074
M2331					M9075
M2332					M9076
M2333					M9077
M2334					M9078
M2335					M9079
M2336	1				M9240
M2337	2				M9241
M2338	3				M9242
M2339	4				M9243
M2340	1				M9244
M2341	2				M9245
M2342	3				M9246
M2343	4				M9247
M2344					M9105
M2345	1 MULTR				M9216
M2346	2 MULTR				M9217
M2347	3 MULTR				M9218
M2348	4 MULTR				M9219
M2349	가				—
}					
M2399					

*1 :

(4)

No.																																																																																																														
1	M2400~M2419	<table><tr><td></td><td></td><td></td><td></td><td></td></tr><tr><td>0</td><td></td><td></td><td></td><td></td></tr><tr><td>1</td><td></td><td></td><td></td><td></td></tr><tr><td>2</td><td></td><td></td><td></td><td></td></tr><tr><td>3</td><td></td><td></td><td></td><td></td></tr><tr><td>4</td><td></td><td></td><td></td><td></td></tr><tr><td>5</td><td>.</td><td></td><td></td><td></td></tr><tr><td>6</td><td></td><td></td><td></td><td></td></tr><tr><td>7</td><td></td><td></td><td></td><td></td></tr><tr><td>8</td><td></td><td></td><td></td><td></td></tr><tr><td>9</td><td></td><td></td><td></td><td></td></tr><tr><td>10</td><td></td><td></td><td></td><td></td></tr><tr><td>11</td><td></td><td>FLS</td><td></td><td></td></tr><tr><td>12</td><td></td><td>RLS</td><td></td><td></td></tr><tr><td>13</td><td></td><td>STOP</td><td></td><td></td></tr><tr><td>14</td><td></td><td>DOG/CHANGE</td><td></td><td></td></tr><tr><td>15</td><td></td><td></td><td></td><td></td></tr><tr><td>16</td><td></td><td></td><td></td><td></td></tr><tr><td>17</td><td>가</td><td>—</td><td>—</td><td>—</td></tr><tr><td>18</td><td>가 (SV22)가*1</td><td>가</td><td></td><td></td></tr><tr><td>19</td><td>M</td><td></td><td></td><td></td></tr></table>									0					1					2					3					4					5	.				6					7					8					9					10					11		FLS			12		RLS			13		STOP			14		DOG/CHANGE			15					16					17	가	—	—	—	18	가 (SV22)가*1	가			19	M			
0																																																																																																														
1																																																																																																														
2																																																																																																														
3																																																																																																														
4																																																																																																														
5	.																																																																																																													
6																																																																																																														
7																																																																																																														
8																																																																																																														
9																																																																																																														
10																																																																																																														
11						FLS																																																																																																								
12						RLS																																																																																																								
13						STOP																																																																																																								
14						DOG/CHANGE																																																																																																								
15																																																																																																														
16																																																																																																														
17	가	—	—	—																																																																																																										
18	가 (SV22)가*1	가																																																																																																												
19	M																																																																																																													
2	M2420~M2439																																																																																																													
3	M2440~M2459																																																																																																													
4	M2460~M2479																																																																																																													
5	M2480~M2499																																																																																																													
6	M2500~M2519																																																																																																													
7	M2520~M2539																																																																																																													
8	M2540~M2559																																																																																																													
9	M2560~M2579																																																																																																													
10	M2580~M2599																																																																																																													
11	M2600~M2619																																																																																																													
12	M2620~M2639																																																																																																													
13	M2640~M2659																																																																																																													
14	M2660~M2679																																																																																																													
15	M2680~M2699																																																																																																													
16	M2700~M2719																																																																																																													
17	M2720~M2739																																																																																																													
18	M2740~M2759																																																																																																													
19	M2760~M2779																																																																																																													
20	M2780~M2799																																																																																																													
21	M2800~M2819																																																																																																													
22	M2820~M2839																																																																																																													
23	M2840~M2859																																																																																																													
24	M2860~M2879																																																																																																													
25	M2880~M2899																																																																																																													
26	M2900~M2919																																																																																																													
27	M2920~M2939																																																																																																													
28	M2940~M2959																																																																																																													
29	M2960~M2979																																																																																																													
30	M2980~M2999																																																																																																													
31	M3000~M3019																																																																																																													
32	M3020~M3039																																																																																																													

* 1 : SV13/SV22

* 2 : Q172CPU(N)

* 3 : Q172CPU(N)

No.1~ No.8

9

(5) ()

					*1 *2
M3072	PLC				M2000
M3073					M2040
M3074	ON				M2042
M3075	/가		가		M2043
M3076	JOG				M2048
M3077	1 가				M2051
M3078	2 가				M2052
M3079	3 가				M2053
M3080	가		—	—	—
↵					
M3135					

*1 : OFF ON , 가 ON , ON OFF 가 OFF .
ON/OFF 가 , 가 .

*2 : 가 .

(6) ()

					*1 *2
M3136					M9025
M3137					M9028
M3138					M9060
M3139					M9104
M3140	가		—	—	—
↵					
M3199					

*1 : OFF ON , 가 ON , ON OFF 가 OFF .
ON/OFF 가 , 가 .

*2 : 가 .

(7)

No.								
1	M3200~M3219							
2	M3220~M3239							
3	M3240~M3259							
4	M3260~M3279	0						
5	M3280~M3299	1						
6	M3300~M3319	2		JOG				
7	M3320~M3339	3		JOG				
8	M3340~M3359	4		OFF				
9	M3360~M3379	5	· 가					
10	M3380~M3399	6	가	—	—	—		
11	M3400~M3419	7						
12	M3420~M3439	8						
13	M3440~M3459	9		STOP				
14	M3460~M3479	10		가		—	—	—
15	M3480~M3499	11						
16	M3500~M3519	12						
17	M3520~M3539	13		(SV22) ^{*1}		가		
18	M3540~M3559	14		(SV22) ^{*1}				
19	M3560~M3579	15		OFF				
20	M3580~M3599	16		가		—	—	—
21	M3600~M3619	17						
22	M3620~M3639	18						
23	M3640~M3659	19		FIN				
24	M3660~M3679							
25	M3680~M3699							
26	M3700~M3719							
27	M3720~M3739							
28	M3740~M3759							
29	M3760~M3779							
30	M3780~M3799							
31	M3800~M3819							
32	M3820~M3839							

*1 : SV13/SV22

*2 : Q172CPU(N)

*3 : Q172CPU(N) 9

가 .

No.1 ~ No.8

가

가

(8)

No.																																																																			
1	D0～D19	<table><tr><td></td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>0</td><td rowspan="7"></td><td rowspan="7"></td><td rowspan="7"></td><td rowspan="7"></td><td rowspan="7"></td></tr><tr><td>1</td></tr><tr><td>2</td></tr><tr><td>3</td></tr><tr><td>4</td></tr><tr><td>5</td></tr><tr><td>6</td></tr><tr><td>7</td></tr><tr><td>8</td></tr><tr><td>9</td></tr><tr><td>10</td><td rowspan="2">ON</td><td rowspan="2"></td><td rowspan="2"></td><td rowspan="2"></td><td rowspan="2"></td></tr><tr><td>11</td></tr><tr><td>12</td><td>No.</td><td></td><td></td><td></td><td></td></tr><tr><td>13</td><td>M</td><td rowspan="2"></td><td rowspan="2"></td><td rowspan="2"></td><td rowspan="2"></td></tr><tr><td>14</td></tr><tr><td>15</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>16</td><td rowspan="2"></td><td rowspan="2"></td><td rowspan="2"></td><td rowspan="2"></td><td rowspan="2"></td></tr><tr><td>17</td></tr><tr><td>18</td><td rowspan="2">STOP</td><td rowspan="2"></td><td rowspan="2"></td><td rowspan="2"></td><td rowspan="2"></td></tr><tr><td>19</td></tr></table>											0						1	2	3	4	5	6	7	8	9	10	ON					11	12	No.					13	M					14	15						16						17	18	STOP					19
0																																																																			
1																																																																			
2																																																																			
3																																																																			
4																																																																			
5																																																																			
6																																																																			
7																																																																			
8																																																																			
9																																																																			
10	ON																																																																		
11																																																																			
12	No.																																																																		
13	M																																																																		
14																																																																			
15																																																																			
16																																																																			
17																																																																			
18	STOP																																																																		
19																																																																			
2	D20～D39																																																																		
3	D40～D59																																																																		
4	D60～D79																																																																		
5	D80～D99																																																																		
6	D100～D119																																																																		
7	D120～D139																																																																		
8	D140～D159																																																																		
9	D160～D179																																																																		
10	D180～D199																																																																		
11	D200～D219																																																																		
12	D220～D239																																																																		
13	D240～D259																																																																		
14	D260～D279																																																																		
15	D280～D299																																																																		
16	D300～D319																																																																		
17	D320～D339																																																																		
18	D340～D359																																																																		
19	D360～D379																																																																		
20	D380～D399																																																																		
21	D400～D419																																																																		
22	D420～D439																																																																		
23	D440～D459																																																																		
24	D460～D479																																																																		
25	D480～D499																																																																		
26	D500～D519																																																																		
27	D520～D539																																																																		
28	D540～D559																																																																		
29	D560～D579																																																																		
30	D580～D599																																																																		
31	D600～D619																																																																		
32	D620～D639																																																																		

* 1 : SV13/SV22

* 2 : Q172CPU(N)

* 3 : Q172CPU(N)

No.1~ No.8

9

가

가

가

(9)

No.						
1	D640, D641	<div><div></div><div>0</div><div>1</div></div> JOG				
2	D642, D643					
3	D644, D645					
4	D646, D647					
5	D648, D649					
6	D650, D651					
7	D652, D653					
8	D654, D655					
9	D656, D657					
10	D658, D659					
11	D660, D661					
12	D662, D663					
13	D664, D665					
14	D666, D667					
15	D668, D669					
16	D670, D671					
17	D672, D673					
18	D674, D675					
19	D676, D677					
20	D678, D679					
21	D680, D681					
22	D682, D683					
23	D684, D685					
24	D686, D687					
25	D688, D689					
26	D690, D691					
27	D692, D693					
28	D694, D695					
29	D696, D697					
30	D698, D699					
31	D700, D701					
32	D702, D703					

* 1 : SV13/SV22

가

* 2 : Q172CPU(N)

No.1~ No.8

가

* 3 : Q172CPU(N) 9

가

(10)

					*4
M2000	PLC				M3072
M2001	1				
M2002	2				
M2003	3				
M2004	4				
M2005	5				
M2006	6				
M2007	7				
M2008	8				
M2009	9				
M2010	10				
M2011	11				
M2012	12				
M2013	13				
M2014	14				
M2015	15				
M2016	16				
M2017	17				
M2018	18				
M2019	19				
M2020	20				
M2021	21				
M2022	22				
M2023	23				
M2024	24				
M2025	25				
M2026	26				
M2027	27				
M2028	28				
M2029	29				
M2030	30				
M2031	31				
M2032	32				
M2033	가	---	---	---	---
M2034	PC				
M2035	가	---	---	---	---
M2036	(4)				
M2037	SFC				
M2038					
M2039					
M2040					M3073
M2041					
M2042	ON				M3074
M2043	/가 (가)		가		M3075
M2044	/가 (가)				
M2045	/가 (가)	가			
M2046	(가)				
M2047					
M2048	JOG				M3076
M2049	ON				
M2050					
M2051	1 가				M3077
M2052	2 가				M3078
M2053	3 가				M3079
M2054					
M2055	가	---	---	---	---
M2056	(6)				
M2057					
M2058					
M2059					
M2060					
M2061	1				
M2062	2				
M2063	3				
M2064	4				
M2065	5				*1, *2
M2066	6				
M2067	7				
M2068	8				
M2069	9				

					*4
M2070	10				
M2071	11				
M2072	12				
M2073	13				
M2074	14				
M2075	15				
M2076	16				
M2077	17				
M2078	18				
M2079	19				
M2080	20				
M2081	21				
M2082	22				*1, *2
M2083	23				
M2084	24				
M2085	25				
M2086	26				
M2087	27				
M2088	28				
M2089	29				
M2090	30				
M2091	31				
M2092	32				
M2093					
M2094	가	---	---	---	---
M2095	(8)				
M2096					
M2097					
M2098					
M2099					
M2100					
M2101	1				
M2102	2				
M2103	3				
M2104	4				
M2105	5				
M2106	6				
M2107	7	*3			*1, *2
M2108	8	(12)			
M2109	9				
M2110	10				
M2111	11				
M2112	12				
M2113					
M2114					
M2115					
M2116					
M2117					
M2118		가	---	---	---
M2119	(15)				
M2120					
M2121					
M2122					
M2123					
M2124					
M2125					
M2126					
M2127					
M2128	1				
M2129	2				
M2130	3				
M2131	4				
M2132	5				
M2133	6				
M2134	7				*1, *2
M2135	8				
M2136	9				
M2137	10				
M2138	11				
M2139	12				


()

					*4						*4
M2140	13					M2210					
M2141	14					M2211	26				
M2142	15					M2212					
M2143	16					M2213	27				
M2144	17					M2214					
M2145	18					M2215	28				
M2146	19					M2216					
M2147	20					M2217	29				*1, *2
M2148	21					M2218					
M2149	22					M2219	30				
M2150	23					M2220					
M2151	24					M2221	31				
M2152	25					M2222					
M2153	26					M2223	32				
M2154	27					M2224					
M2155	28					M2225					
M2156	29					M2226					
M2157	30					M2227					
M2158	31					M2228					
M2159	32					M2229					
M2160						M2230					
M2161	1					M2231					
M2162						M2232	(16)	가			
M2163	2					M2233					
M2164						M2234					
M2165	3					M2235					
M2166						M2236					
M2167	4					M2237					
M2168						M2238					
M2169	5					M2239					
M2170						M2240	1				
M2171	6					M2241	2				
M2172						M2242	3				
M2173	7					M2243	4				
M2174						M2244	5				
M2175	8					M2245	6				
M2176						M2246	7				
M2177	9					M2247	8				
M2178						M2248	9				
M2179	10					M2249	10				
M2180						M2250	11				
M2181	11					M2251	12				
M2182						M2252	13				
M2183	12					M2253	14				
M2184						M2254	15				
M2185	13					M2255	16				
M2186						M2256	17				
M2187	14					M2257	18				
M2188						M2258	19				
M2189	15					M2259	20				
M2190						M2260	21				
M2191	16					M2261	22				
M2192						M2262	23				
M2193	17					M2263	24				
M2194						M2264	25				
M2195	18					M2265	26				
M2196						M2266	27				
M2197	19					M2267	28				
M2198						M2268	29				
M2199	20					M2269	30				
M2200						M2270	32				
M2201	21					M2271	32				
M2202						M2272					
M2203	22					M2273					
M2204						M2274					
M2205	23					M2275					
M2206						M2276	(8)	가			
M2207	24					M2277					
M2208						M2278					
M2209	25					M2279					

()

					*4						*4
M2280	(20) 가	—	—	—	—	M2300	(20) 가	—	—	—	—
M2281						M2301					
M2282						M2302					
M2283						M2303					
M2284						M2304					
M2285						M2305					
M2286						M2306					
M2287						M2307					
M2288						M2308					
M2289						M2309					
M2290						M2310					
M2291						M2311					
M2292						M2312					
M2293						M2313					
M2294						M2314					
M2295						M2315					
M2296						M2316					
M2297						M2317					
M2298						M2318					
M2299						M2319					

* 1 : Q172CPU(N) No.1~ No.8 가 .
 * 2 : Q172CPU(N) 9 가 .
 * 3 : SV13/SV22 가 .
 * 4 : 가 .

	
● SFC , 가 , 가	

(11)

D704	PLC			
D705				
D706	ON			
D707	/가 (SV22)			
D708	JOG			
D709	가	—	—	—
D710				
D711	JOG			
D712				
D713				
D714	1			
D715	No.			
D716	2			
D717	No.			
D718	3			
D719	No.			
D720	1			
D721	2			
D722	3			
D723	4			
D724	5			
D725	6			
D726	7			
D727	8			
D728	9			
D729	10			
D730	11			
D731	12			
D732	13			
D733	14			
D734	15			
D735	16			
D736	17			
D737	18			
D738	19			
D739	20			
D740	21			
D741	22			
D742	23			
D743	24			
D744	25			
D745	26			
D746	27			
D747	28			
D748	29			
D749	30			
D750	31			
D751	32			

D752	1		가	
D753	2			
D754	3			
D755	1 가			
D756	2 가			
D757	3 가		—	—
D758	가			
D759	PCPU			
D760	(32) 가			
D761				
D762				
D763				
D764				
D765				
D766				
D767				
D768				
D769				
D770				
D771				
D772				
D773				
D774				
D775				
D776				
D777				
D778				
D779				
D780				
D781				
D782				
D783				
D784				
D785				
D786				
D787				
D788				
D789				
D790				
D791				
D792		ON		
D793				
D794				
D795				
D796				
D797				
D798				
D799				

* 1 : SV13/SV22

* 2 : Q172CPU(N)

* 3 : Q172CPU(N)

가 .

, No.1~ No.8 가 .

9 가 .

NAME _____

(13)

M9073	PCPU WDT		
M9074	PCPU		
M9075			
M9076			
M9077			
M9078			
M9079			

(14)

D9180	가	—	—	—
D9181				
D9182			/	
D9183				
D9184	CPU WDT	CPU WDT		
D9185		가		
D9186				
D9187				
D9188				
D9189	No.			
D9190				
D9191		ON		
D9192				
D9193				
D9194	/가	가		
D9195				
D9196	PC			
D9197		ON		
D9198	가	—	—	—
D9199				
D9200			/	
D9201	LED			

MEMO

1. _____ (「 」 .)
가 , 가 , 가

【 1 18 ()

【 (1) 】

(2)

가 가

 가

2. _____

3. _____, _____.

4. _____

5. 

(())

北海道支店
☎ (011) 890-7515

新潟サービスステーション
☎ (025) 274-9165

北陸支店
☎ (076) 251-0559

京滋機器サービスステーション
☎ (075) 611-6211

関西機電支店
☎ (0726) 41-0441
大阪機器サービスステーション
☎ (06) 6458-9728
堺機器サービスステーション
☎ (0722) 29-5992

姫路機器サービスステーション
☎ (0792) 81-1141

中国支店
☎ (082) 285-2111

東北支店
☎ (022) 236-3818

東京機電支店
☎ (03) 3454-5521
千葉機器サービスステーション
☎ (043) 232-6101
神奈川機器サービスステーション
☎ (042) 779-9711

関越支店
☎ (048) 651-3221

関東機器サービスステーション
☎ (048) 652-0378

静岡機器サービスステーション
☎ (054) 287-8866

浜松サービスステーション
☎ (053) 463-8455

中部支社
☎ (052) 722-7601

神戸機器サービスステーション
☎ (078) 651-0332

四国支店
☎ (087) 831-3186

倉敷機器サービスステーション
☎ (086) 448-5532

北九州機器サービスステーション
☎ (093) 642-8825

九州支社
☎ (092) 483-8208

長崎機器サービスステーション
☎ (095) 865-3667



Q
SV13/SV22
(Q173CPU(N) / Q172CPU(N))

:  **mitsubishi electric corporation**
HEAD OFFICE : MITSUBISHI DENKI BLDG MARUNOUCHI, TOKYO 100, FAX 81-3-3218-3579

: () TEL(02)3660-9531 FAX(02)3664-8335

() : <http://www.hanneung.com>

MELFANS web : <http://www.nagoya.melco.co.jp>

	Q173-P-SV13/22-REAL
	1CT762